

3.3Vdc HCMOS/TTL SMD CRYSTAL CLOCK OSCILLATOR

ASV1 SERIES



7.0 x 5.0 x 1.6mm



RoHS
Compliant

FEATURES:

- Leadless Chip Carrier (LCC)
- HCMOS/TTL Compatible, 3.3Vdc, 2.5Vdc, & 1.8Vdc options
- Reflow capable, Seam Sealed Package

APPLICATIONS:

- Clock signaling for μ Processors, PC Motherboards and Graphic Cards
- High output drive capability (up to 50pf)

STANDARD SPECIFICATIONS:

PARAMETERS

ABRACON P/N	ASV1 Series
Frequency Range	0.500 MHz to 200.000 MHz
Operating Temperature	-10° C to + 70° C (see options)
Storage Temperature	- 55° C to + 125° C
Overall Frequency Stability	± 100 ppm max. (see options)
Supply Voltage (Vdd)	3.3 Vdc $\pm 10\%$ (see options)
Input Current	See Table 1
Symmetry	40/60 % max.@ 1/2Vdd (see options)
Rise time / Fall time (Tr/Tf)	See Table 1
Output Load	15pF (10TTL); 50pF max.
Output Voltage	VOH = 0.9 * Vdd min.; VOL = 0.4 Vdc max.
Start-up Time	10ms max.
Tristate Function	"1" (VIH ≥ 0.7 Vdc) or open: Oscillation "0" (VIL < 0.3 Vdc) : Hi Z
Disable Current (with Power Down)	10 μ A max.
Disable Current (without Power Down)	200 μ A (Option A)
Enable Time (with Power Down)	400ms max.
Enable Time (without Power Down)	100ns (Option A)
Disable Time (with Power Down)	100ns
Disable Time (without Power Down)	100ns (Option A)
Aging at 25°C/ (first year)	± 5 ppm max.
Period Jitter one sigma	1 to 5ps typ. (frequency dependent)

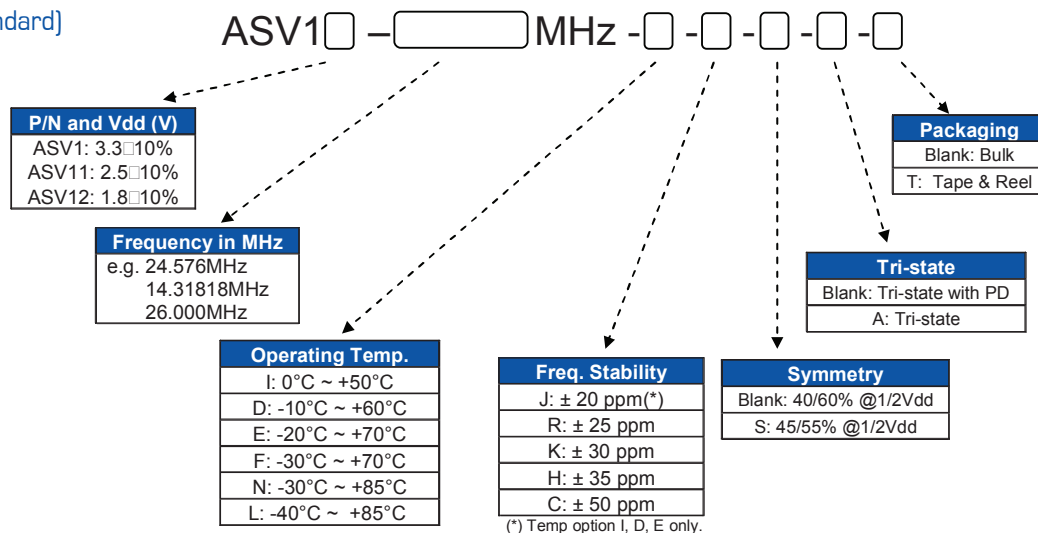
Table 1

Frequency	Idd (max)	Tr/Tf (max)
0.5 ~ 30.0MHz	15mA	10ns
30.01 ~ 70.0MHz	35mA	7ns
70.01~125.0MHz	45mA	7ns
125.01 ~ 165MHz	65mA	5ns

Please contact Abracon for higher frequencies

OPTIONS & PART IDENTIFICATION:

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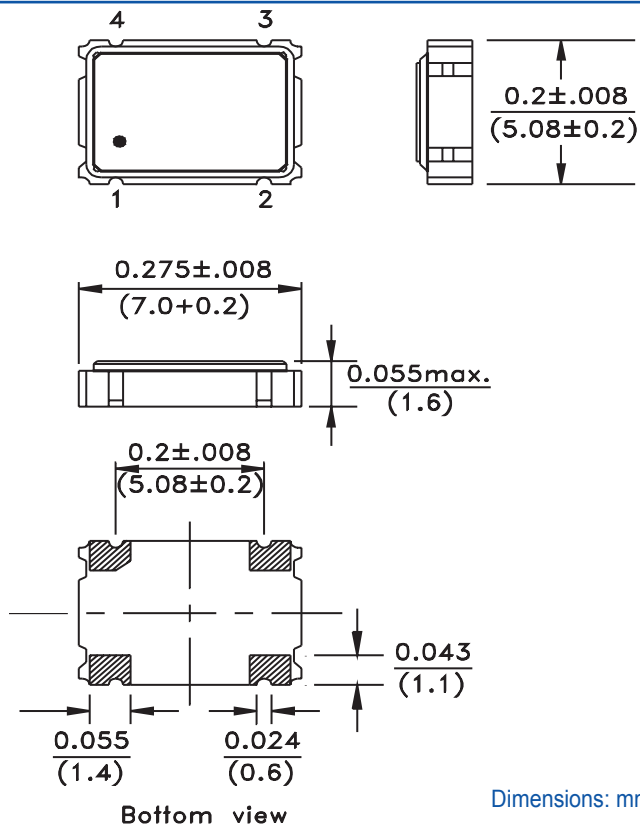


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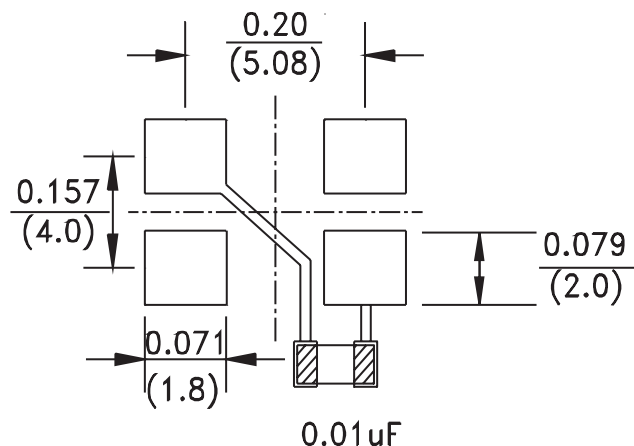


7.0 x 5.0 x 1.6mm

OUTLINE DRAWING:



Recommended land pattern

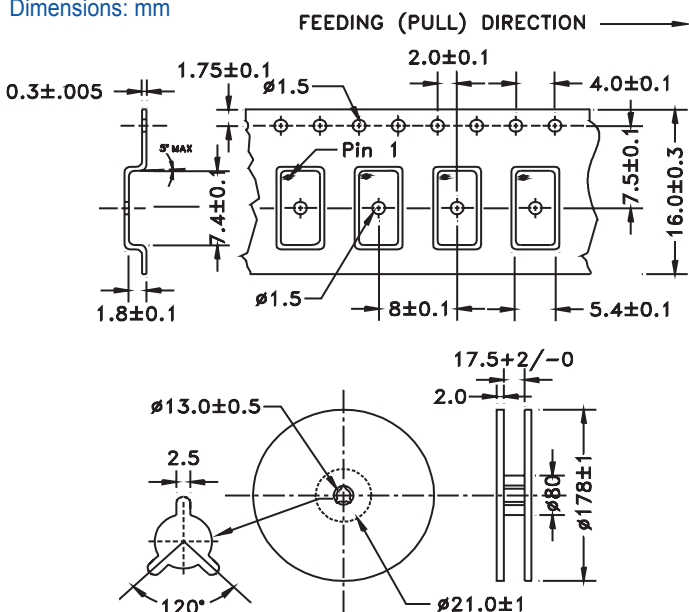


PIN	FUNCTION
1	Tristate Enable/Disable
2	GND/Case
3	Output
4	Vdd

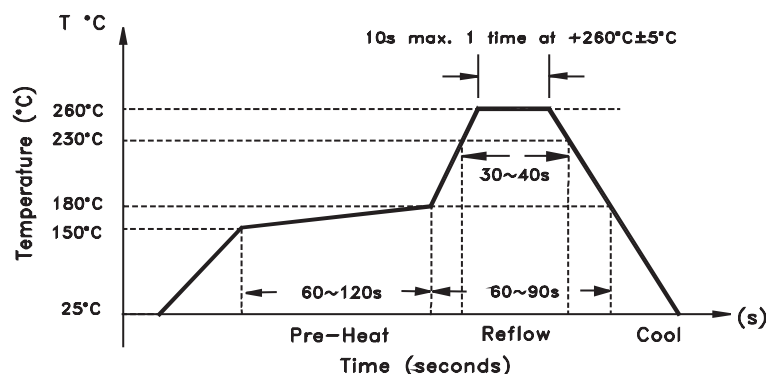
Note: It is recommended to use an approximately 0.01uF bypass capacitor between PIN 2 and 4.

TAPE & REEL: T = Tape and reel 1,000pcs/reel

Dimensions: mm



REFLOW PROFILE:



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ABRACON IS
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