

# Silicon-Based Technology

*Power Schottky Barrier Rectifier*

**SBT140D Series**

SBT140D series are Schottky Barrier Diodes fabricated by a series of proprietary Schottky barrier patents and technologies (SBT<sup>®</sup>) developed by Silicon-Based Technology Corporation, which exhibit high-performance characteristics for modern power switching, conversion and protection applications with high speed and low power consumptions. The package types as described in this data sheet are set forth in routine production; other packages are available upon special orders.

## ■ Features and Advantages:

- Low forward voltage drop( $V_F$ )
- Low reverse leakage current ( $I_R$ )
- Very small conduction power loss
- Very small switching power loss
- Very high switching speed
- Very high reliability

## ■ Static Electrical Characteristics :

	$V_F$ (V) Max.	$I_F$ (A)	$I_R$ ( $\mu$ A) Max./Typ.	$V_R$ (V)	$T_A$ ( $^{\circ}$ C)
SBT140DL	0.45	1.0	100/40	40	25
SBT140DN	0.48				
SBT140DH	0.52				
SBT140DP	0.56				



**Silicon-Based Technology Corporation**

1F, No. 23, R&D Rd. I, Hsinchu Science Park, Taiwan, R.O.C

Tel : 886-3-5777897

Fax : 886-3-5779832

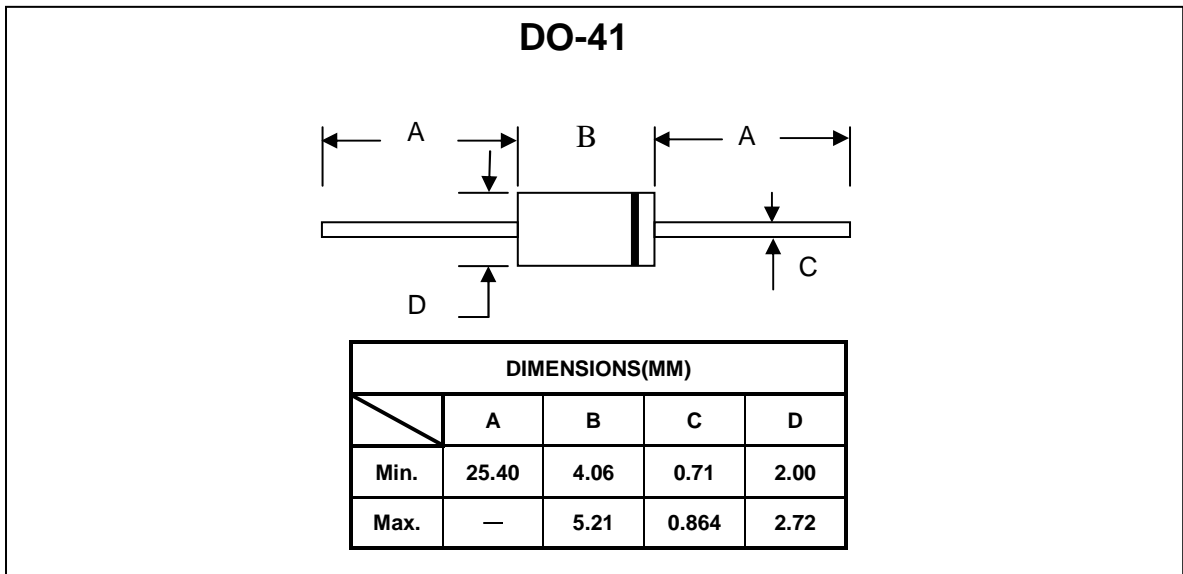
**■ Maximum Ratings and Electrical Characteristics :**(@T<sub>A</sub>=25°C unless otherwise specified)

Characteristics		SBT140DL	SBT140DN	SBT140DH	SBT140DP	Unit
Peak Repetitive Reverse Voltage, V <sub>RRM</sub>		40				V
Working Peak Reverse Voltage, V <sub>RWM</sub>						
DC Blocking Voltage, V <sub>R</sub>						
RMS Reverse Voltage, V <sub>R(RMS)</sub>		28				V
Average Rectified Output Current, I <sub>O</sub>	@T <sub>T</sub> =130°C	1.0				A
Non-Repetitive Peak Forward Surge Current under 8.3ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method), I <sub>FSM</sub>		40				A
Forward Voltage, V <sub>FM</sub>	@I <sub>F</sub> =1.0A	0.45	0.48	0.52	0.56	V
Peak Reverse Current at Rated DC Blocking Voltage, I <sub>RM</sub>	Max.	100				μA
	Typ.	40				
Typical Junction Capacitance, C <sub>J</sub> (Note 1)		70				pF
Operating (Storage) Temperature Range, T <sub>J</sub> (T <sub>STG</sub> )		-65 to +150				°C

Notes: 1. Measured at 1.0 MHz and with an applied DC reverse voltage of 4.0V.

**■ Package Data :**

- Case: Molded Plastic Material (UL Flammability Classification 94V-0)
- Terminals: Solderable Plated Terminals (MIL-STD-202, Method 208)
- Lead Free Plating (Matte Tin Finish)
- Polarity: Cathode Band as shown in page3 or Cathode Notch
- Approx. Weight: 0.330 grams for DO-41



■ **Ordering Information** (Note 2)

Part Number	Marking Code	Packaging Type	Shipping
			13" Tape & Real
SBT140DX	SBT1DDX	DO-41	5K

Notes: 2. For Packaging Details, please go to our Website at <http://www.sbt.com.tw>

X = Device type, e.g.: X=L, X=N, X=H, X=P

3. Bulk package is also available upon request.

4. Day code marking is YWW or YM, in which Y represents year (For example: 2005 is marked by 5); WW represents weekth in a year (For example: 5th week is marked by 05); M represents month in a year (For example: March is marked by C; November is marked by K).

