

Silicon-Based Technology Corp.

Small-Signal Schottky Barrier Diodes

SBT42D/43D Series

SBT42D/43D series are Schottky Barrier Diodes fabricated by a series of proprietary Schottky barrier patents and technologies (SBT[®]) developed by Silicon-Based Technology Corporation, which exhibit high-performance characteristics for modern switching, conversion and protection applications with high speed and low power consumptions. The package types as described in this data sheet are set forth in routine production; other packages are available upon special orders.

■ Features and Advantages:

- Low forward voltage drop (V_F)
- Low reverse leakage current (I_R)
- Very small conduction power loss
- Very small switching power loss
- Very high switching speed
- Very high reliability

■ Electrical Characteristics : (@ $T_A=25^\circ\text{C}$ unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Reverse Breakdown Voltage (Note 2)	$V_{(BR)R}$	30	-	-	V	$I_R=100\mu\text{A}$ Pulses
Maximum Forward Voltage Drop (Note 2)	All Types SBT42D SBT42D SBT43D SBT43D	- - - - -	- - -	0.60 0.35 0.45 0.30 0.35	V	$I_F=200\text{mA}$ $I_F=10\text{mA}$ $I_F=50\text{mA}$ $I_F=2.0\text{mA}$ $I_F=15\text{mA}$
Maximum Peak Reverse Current (Note 2)	I_{RM}	-	-	0.50 100	μA	$V_R=25\text{V}$ $V_R=25\text{V}, T_J=100^\circ\text{C}$
Junction Capacitance	C_j	-	10	-	pF	$V_R=3.0\text{V}, f=1.0\text{MHz}$
Reverse Recovery Time	t_{rr}	-	-	5.0	ns	$I_F=I_R=10\text{mA}$, $I_{rr}=0.1 \times I_R, R_L=100\Omega$
Rectification Efficiency	η_v	80	-	-	%	$R_L=100\Omega, C_L=300\text{pF}$, $F=45\text{MHz}, V_{RF}=2.0\text{V}$



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**■ Maximum Ratings : (@T_A=25°C unless otherwise specified)**

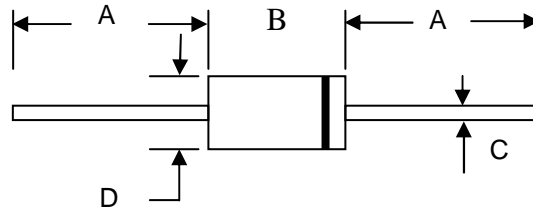
Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	30	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _R		
RMS Reverse Voltage	V _{R(RMS)}	21	V
Forward Continuous Current (Note 1)	I _{FM}	200	mA
Repetitive Peak Forward Current (Note 1) @t<1.0s Duty Cycle<50%	I _{FRM}	500	mA
Non-Repetitive Peak Forward Surge Current @t=10ms	I _{FSM}	4.0	A
Power Dissipation (Note 1)	P _d	200	mW
Thermal Resistance Junction to Ambient Air (Note 1)	R _{θJA}	500	K/W
Operating and Storage Temperature Range	T _j , T _{STG}	-55 to +125	°C

- Notes: 1. Valid provided that leads are kept at ambient temperature.
2. t<300µs, Duty Cycle<2%.
3. RoHS revision 13.2.2003. Glass and High Temperature Solder Exemptions Applied, see EU Directive Annex Notes6 and 7.

■ Package Data :

- Case: Molded Plastic Material (UL Flammability Classification 94V-0)
- Terminals: Solderable Plated Terminals (MIL-STD-202, Method 208)
- Lead Free Plating (Matte Tin Finish)
- Polarity: See device configurations below
- Approx. Weight: 0.13 grams
- Package outline and dimensions (see below)

DO-35



DIMENSIONS (MM)				
	A	B	C	D
Min.	25.40	-	-	-
Max.	-	4.00	0.60	2.00

■ Ordering Information (Note 4)

Part Number	Marking Code	Packaging Type	Shipping
			7" Tape & Real
SBT42D	SBT42D	DO-35	3K
SBT43D	SBT43D	DO-35	3K

Notes: 4. Website at <http://www.sbt.com.tw>

5. Bulk package in a box form is also available upon request.

6. Day code marking is YM, in which Y represents year (For example: 2005 is marked by 5);

M represents month in a year (For example: March is marked by C; November is marked by K).

