SBT6263D series are Schottky Barrier Diodes fabricated by a series of proprietary Schottky barrier patents and technologies (SBT®) developed by Silicon-Based Technology Corporation, which exhibit high-performance characteristics for modern switching, conversion and protection applications with high speed and low power consumptions. The package types as described in this data sheet are set forth in routine production; other packages are available upon special orders.

■ Features and Advantages:

- Low forward voltage drop(V_F)
- Low reverse leakage current (I_R)
- Very small conduction power loss
- Very small switching power loss
- Very high switching speed
- Very high reliability

■ Electrical Characteristics : (@T_A=25℃ unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Reverse Breakdown Voltage (Note 2)	$V_{(BR)R}$	60	-	-	-	I_R=10μA
Reverse Leakage Current (Note 2)	I_R	-	-	200	nA	V_R=50V
Forward Voltage	V_{FM}	-	-	0.32 0.60	V	I_F=1.0mA I_F=15mA
Total Capacitance	C_T	-	-	2.2	pF	V_R=0V, f=1.0MH$_Z$
Reverse Recovery Time	t_{rr}	-	-	1.0	ns	I_F=I_R=5.0mA, I_{rr}=0.1×I_R,R_L=100Ω

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■ Maximum Ratings : (@T_A=25℃ unless otherwise specified)

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	60	V
RMS Reverse Voltage	$V_{R(RMS)}$	42	V
Forward Continuous Current	I_F	15	mA
Non-Repetitive Peak Forward Surge Current @t≤1.0s @t=10ms	I_{FSM}	50 2.0	mA A
Power Dissipation (Note 1)	P_D	333	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{\theta JA}$	300	℃/W
Operating Temperature Range	T_j	-55 to +125	℃
Storage Temperature Range	T_{STG}	-55 to +150	℃

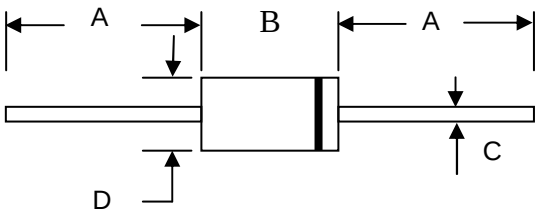
Notes: 1. Part mounted on FR4 PC Board with recommended pad layout, which can be found on our website at http://www.sbt.com.tw.

2. Short duration test pulse is used to minimize self-heating effect.

■ Package Data :

- Case**:** Molded Plastic Material (UL Flammability Classification 94V-0)
- Terminals**:** Solderable Plated Terminals (MIL-STD-202, Method 208)
- Lead Free Plating (Matte Tin Finish)
- Polarity**:** See device configurations below
- Approx. Weight**:** 0.13 grams.
- Package outline and dimensions (see below)

DO-35



DIMENSIONS (MM)				
	A	B	C	D
Min.	25.40	-	-	-
Max.	-	4.00	0.60	2.00

■ Ordering Information (Note 3)

Part Number	Marking Code	Packaging Type	Shipping
			7″ Tape & Real
SBT6263D	SBT62D	DO-35	3K

Notes: 3. Website at http://www.sbt.com.tw

4. Bulk package in a box form is also available upon request.

5. Day code marking is YM, in which Y represents year (For example: 2005 is marked by 5);
M represents month in a year (For example: March is marked by C; November is marked by K).