

Silicon-Based Technology Corp.

Power Schottky Barrier Diodes

SBT160L-40 Series

SBT160L-40 series are Schottky Barrier Diodes fabricated by a series of proprietary Schottky barrier patents and technologies (SBT[®]) developed by Silicon-Based Technology Corporation, which exhibit high-performance characteristics for modern switching, conversion and protection applications with high speed and low power consumptions. The package types as described in this data sheet are set forth in routine production; other packages are available upon special orders.

■ Features and Advantages:

- Low forward voltage drop (V_F)
- Low reverse leakage current (I_R)
- Very small conduction power loss
- Very small switching power loss
- Very high switching speed
- Very high reliability

■ Electrical Characteristics : (@ $T_A=25^\circ\text{C}$ unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Reverse Breakdown Voltage (Note 2)	$V_{(BR)R}$	40	-	-	V	$I_R=100\mu\text{A}$
Forward Voltage (Note 2)	V_F	-	-	0.48	V	$I_F=1\text{A}$
Reverse Leakage Current (Note 2)	I_R	-	-	40	μA	$V_R=40\text{V}$
Total Capacitance	C_T	-	-	70	pF	$V_R=4\text{V}, f=1\text{MHz}$
Reverse Recovery Time	t_{rr}	-	-	-	-	-



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**■ Maximum Ratings : (@T_A=25°C unless otherwise specified)**

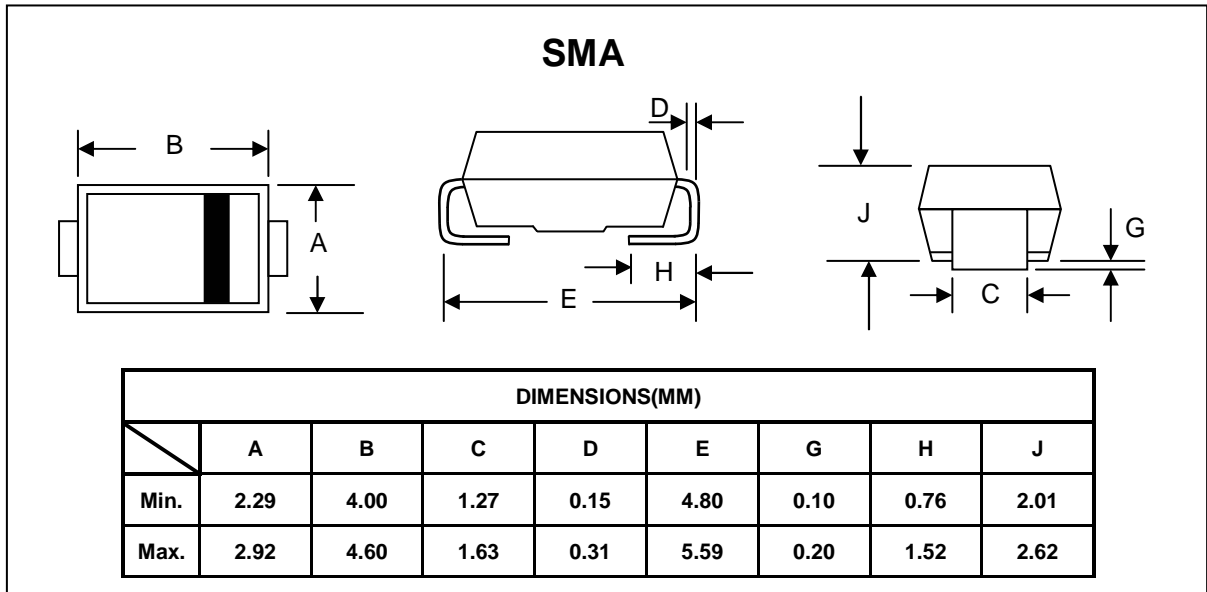
Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	40	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _R	40	V
Average Rectified Current (Note 1)	I _O	1	A
Forward Surge Current (Note 1) @t<1.0s	I _{FSM}	40	A
Power Dissipation (Note 1)	P _d	-	mW
Thermal Resistance Junction to Ambient (Note 1)	R _{θJA}	-	°C/W
Operating Temperature Range	T _j	150	°C
Storage Temperature Range	T _{STG}	-40 to +150	°C

Notes: 1. Part mounted on FR4 PC Board with recommended pad layout, which can be found on our website at <http://www.sbt.com.tw>.

2. Short duration test pulse is used to minimize self-heating effect.

■ Package Data :

- Case: Molded Plastic Material (UL Flammability Classification 94V-0)
- Terminals: Solderable Plated Terminals (MIL-STD-202, Method 208)
- Lead Free Plating (Matte Tin Finish)
- Polarity: See device configurations below
- Approx. Weight: 0.0669 grams.
- Package outline and dimensions (see below)



■ **Ordering Information** (Note 3)

Part Number	Marking Code	Packaging Type	Shipping	
			7" Tape & Real	13" Tape & Real
SBT160L-40	SBT1DAN	SMA	1.5K	5K

Notes: 3. Website at <http://www.sbt.com.tw>

- 4. Bulk package in a box form is also available upon request.
- 5. Day code marking is YM, in which Y represents year (For example: 2005 is marked by 5);
M represents month in a year (For example: March is marked by C; November is marked by K).



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