

### FEATURES

- Low  $V_{CE(sat)}$  Device
- 10 $\mu$ s Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS SPT
- Isolated AISiC Base With AlN Substrates

### APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM500GCM33-TL000 is a Low  $V_{CE(sat)}$  3300V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10 $\mu$ s short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

### ORDERING INFORMATION

Order As:

### DIM500GCM33-TL000

Note: When ordering, please use the complete part number

### KEY PARAMETERS

$V_{CES}$	<b>3300V</b>
$V_{CE(sat)}$ * (typ)	<b>2.0V</b>
$I_C$ (max)	<b>500A</b>
$I_{C(PK)}$ (max)	<b>1000A</b>

\* Measured at the auxiliary terminals

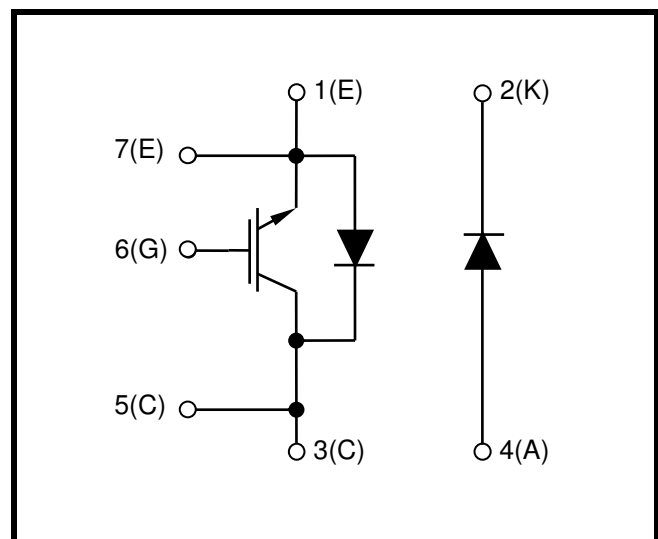


Fig. 1 Circuit configuration



Outline type code: G  
(See Fig. 11 for further information)

Fig. 2 Package

## ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

$T_{case} = 25^{\circ}\text{C}$  unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
$V_{CES}$	Collector-emitter voltage	$V_{GE} = 0\text{V}$	3300	V
$V_{GES}$	Gate-emitter voltage		$\pm 20$	V
$I_C$	Continuous collector current	$T_{case} = 115^{\circ}\text{C}$	500	A
$I_{C(PK)}$	Peak collector current	1ms, $T_{case} = 140^{\circ}\text{C}$	1000	A
$P_{max}$	Max. transistor power dissipation	$T_{case} = 25^{\circ}\text{C}$ , $T_j = 150^{\circ}\text{C}$	5.2	kW
$I^2t$	Diode $I^2t$ value (IGBT arm)	$V_R = 0$ , $t_p = 10\text{ms}$ , $T_j = 125^{\circ}\text{C}$	80	$\text{kA}^2\text{s}$
	Diode $I^2t$ value (Diode arm)		80	$\text{kA}^2\text{s}$
$V_{isol}$	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	6000	V
$Q_{PD}$	Partial discharge – per module	IEC1287, $V_1 = 3500\text{V}$ , $V_2 = 2600\text{V}$ , 50Hz RMS	10	pC

## THERMAL AND MECHANICAL RATINGS

Internal insulation material:	AlN
Baseplate material:	AlSiC
Creepage distance:	33mm
Clearance:	20mm
CTI (Comparative Tracking Index):	>600

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$R_{th(j-c)}$	Thermal resistance – transistor (per arm)	Continuous dissipation – junction to case	-	-	24	$^{\circ}\text{C}/\text{kW}$
$R_{th(j-c)}$	Thermal resistance – diode (IGBT arm)	Continuous dissipation – junction to case			48	$^{\circ}\text{C}/\text{kW}$
	Thermal resistance – diode (Diode arm)				48	$^{\circ}\text{C}/\text{kW}$
$R_{th(c-h)}$	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	8	$^{\circ}\text{C}/\text{kW}$
$T_j$	Junction temperature	Transistor	-	-	150	$^{\circ}\text{C}$
		Diode	-	-	150	$^{\circ}\text{C}$
$T_{stg}$	Storage temperature range	-	-40	-	125	$^{\circ}\text{C}$
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

**ELECTRICAL CHARACTERISTICS**
**T<sub>case</sub> = 25 °C unless stated otherwise.**

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I <sub>CES</sub>	Collector cut-off current	V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub>			2	mA
		V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub> , T <sub>case</sub> = 125 °C			30	mA
		V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub> , T <sub>case</sub> = 150 °C			50	mA
I <sub>GES</sub>	Gate leakage current	V <sub>GE</sub> = ± 20V, V <sub>CE</sub> = 0V			1	µA
V <sub>GE(TH)</sub>	Gate threshold voltage	I <sub>C</sub> = 40mA, V <sub>GE</sub> = V <sub>CE</sub>		5.7		V
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 500A		2.0		V
		V <sub>GE</sub> = 15V, I <sub>C</sub> = 500A, T <sub>j</sub> = 125 °C		2.6		V
		V <sub>GE</sub> = 15V, I <sub>C</sub> = 500A, T <sub>j</sub> = 150 °C		2.8		V
I <sub>F</sub>	Diode forward current	DC		500		A
I <sub>FM</sub>	Diode maximum forward current	t <sub>p</sub> = 1ms		1000		A
V <sub>F</sub> <sup>†</sup>	Diode forward voltage (IGBT arm)	I <sub>F</sub> = 500A		2.4		V
	Diode forward voltage (Diode arm)			2.4		V
	Diode forward voltage (IGBT arm)	I <sub>F</sub> = 500A, T <sub>j</sub> = 125 °C		2.5		V
	Diode forward voltage (Diode arm)			2.5		V
	Diode forward voltage (IGBT arm)	I <sub>F</sub> = 500A, T <sub>j</sub> = 150 °C		2.4		V
	Diode forward voltage (Diode arm)			2.4		V
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz		90		nF
Q <sub>g</sub>	Gate charge	±15V Including external C <sub>ge</sub>		9		µC
C <sub>res</sub>	Reverse transfer capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz		2		nF
L <sub>M</sub>	Module inductance – per arm			25		nH
R <sub>INT</sub>	Internal resistance – per arm			260		µΩ
SC <sub>Data</sub>	Short circuit current, I <sub>SC</sub>	T <sub>j</sub> = 150 °C, V <sub>CC</sub> = 2500V t <sub>p</sub> ≤ 10µs, V <sub>GE</sub> ≤ 15V V <sub>CE(max)</sub> = V <sub>CES</sub> - L * x di/dt IEC 60747-9		1900		A

**Note:**
<sup>†</sup> Measured at the power busbars, not the auxiliary terminals

<sup>\*</sup> L is the circuit inductance + L<sub>M</sub>

## ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}\text{C}$  unless stated otherwise

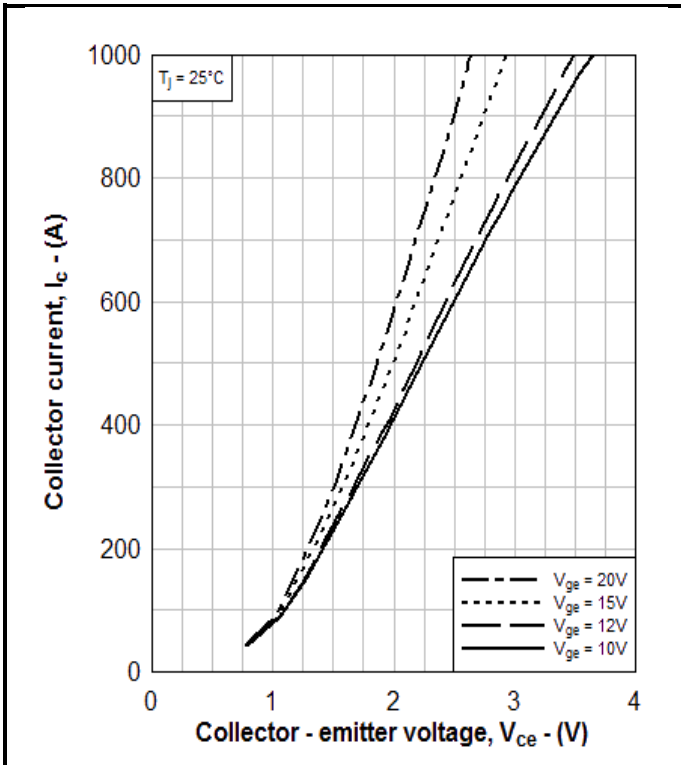
Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(off)}$	Turn-off delay time	$I_C = 500\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 1800\text{V}$ $R_{G(ON)} = 4.7\Omega$ $R_{G(OFF)} = 4.7\Omega$ $C_{ge} = 100\text{nF}$ $L_S \sim 100\text{nH}$		2700		ns
$t_f$	Fall time			610		ns
$E_{OFF}$	Turn-off energy loss			1250		mJ
$t_{d(on)}$	Turn-on delay time			960		ns
$t_r$	Rise time			430		ns
$E_{ON}$	Turn-on energy loss			800		mJ
$Q_{rr}$	Diode reverse recovery charge	$I_F = 500\text{A}$ $V_{CE} = 1800\text{V}$ $di_F/dt = 1400\text{A}/\mu\text{s}$		280		$\mu\text{C}$
$I_{rr}$	Diode reverse recovery current			310		A
$E_{rec}$	Diode reverse recovery energy			350		mJ

$T_{case} = 125^{\circ}\text{C}$  unless stated otherwise

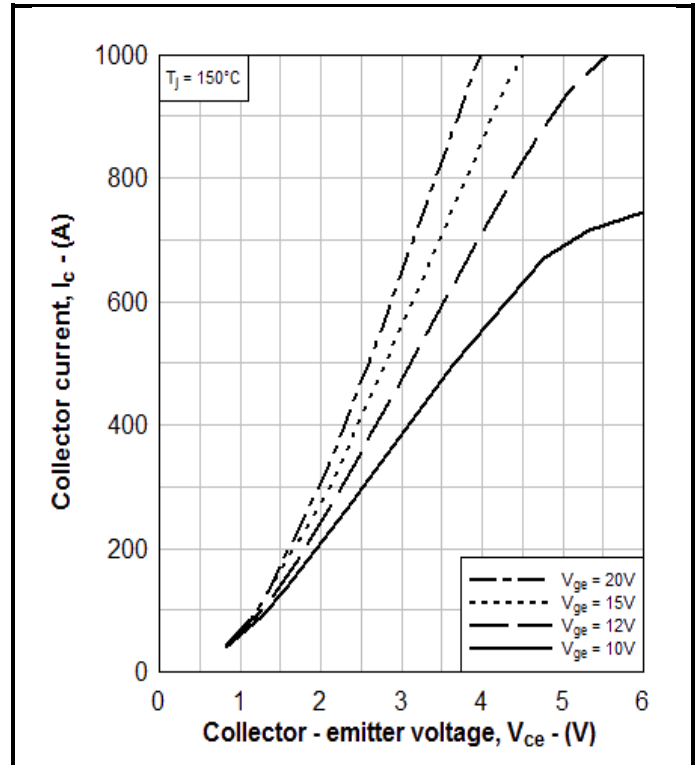
Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(off)}$	Turn-off delay time	$I_C = 500\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 1800\text{V}$ $R_{G(ON)} = 4.7\Omega$ $R_{G(OFF)} = 4.7\Omega$ $C_{ge} = 100\text{nF}$ $L_S \sim 100\text{nH}$		2750		ns
$t_f$	Fall time			590		ns
$E_{OFF}$	Turn-off energy loss			1350		mJ
$t_{d(on)}$	Turn-on delay time			1000		ns
$t_r$	Rise time			460		ns
$E_{ON}$	Turn-on energy loss			1050		mJ
$Q_{rr}$	Diode reverse recovery charge	$I_F = 500\text{A}$ $V_{CE} = 1800\text{V}$ $di_F/dt = 1400\text{A}/\mu\text{s}$		470		$\mu\text{C}$
$I_{rr}$	Diode reverse recovery current			390		A
$E_{rec}$	Diode reverse recovery energy			600		mJ

$T_{case} = 125^{\circ}\text{C}$  unless stated otherwise

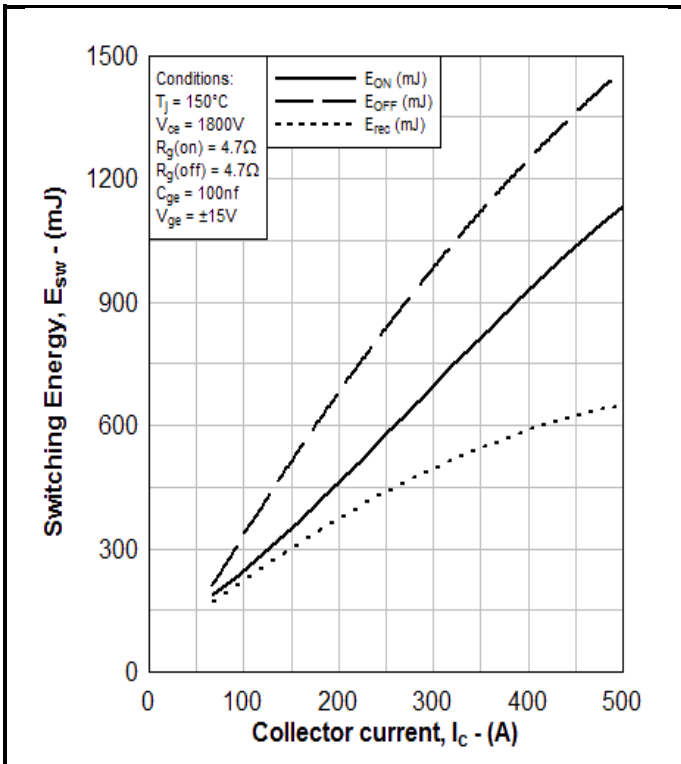
Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(off)}$	Turn-off delay time	$I_C = 500\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 1800\text{V}$ $R_{G(ON)} = 4.7\Omega$ $R_{G(OFF)} = 4.7\Omega$ $C_{ge} = 100\text{nF}$ $L_S \sim 100\text{nH}$		2760		ns
$t_f$	Fall time			590		ns
$E_{OFF}$	Turn-off energy loss			1500		mJ
$t_{d(on)}$	Turn-on delay time			940		ns
$t_r$	Rise time			460		ns
$E_{ON}$	Turn-on energy loss			1150		mJ
$Q_{rr}$	Diode reverse recovery charge	$I_F = 500\text{A}$ $V_{CE} = 1800\text{V}$ $di_F/dt = 1400\text{A}/\mu\text{s}$		530		$\mu\text{C}$
$I_{rr}$	Diode reverse recovery current			400		A
$E_{rec}$	Diode reverse recovery energy			650		mJ



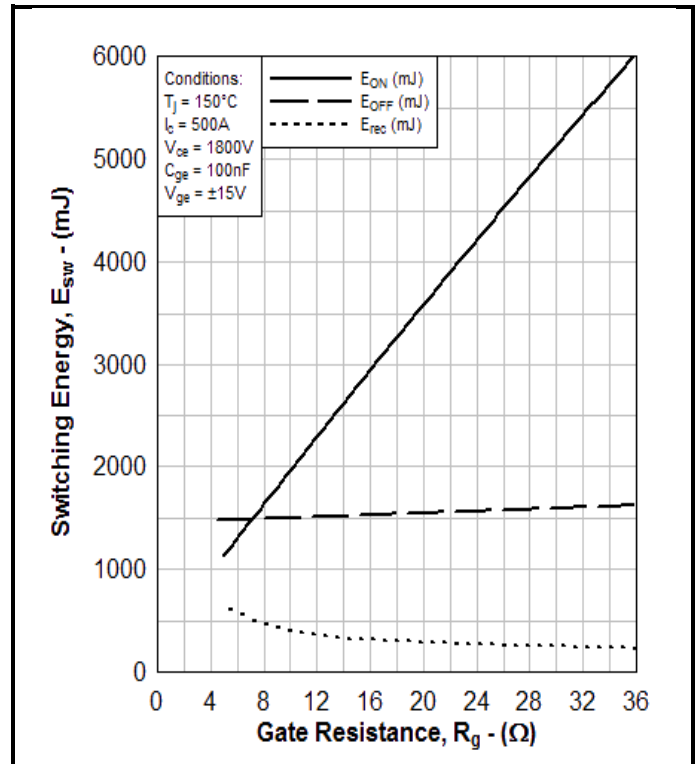
**Fig. 3 Typical output characteristics**



**Fig. 4 Typical output characteristics**



**Fig. 5 Typical switching energy vs collector current**



**Fig. 6 Typical switching energy vs gate resistance**

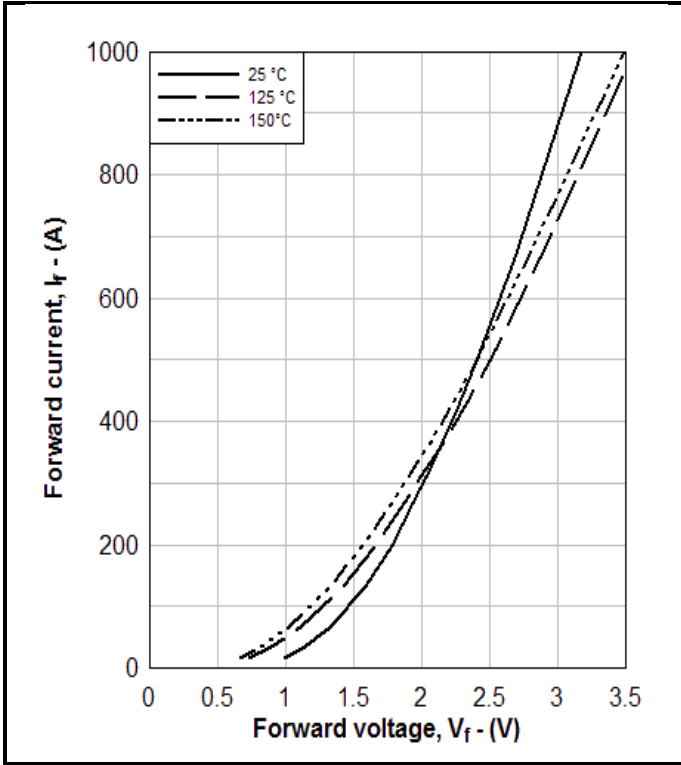


Fig. 7 Diode typical forward characteristics

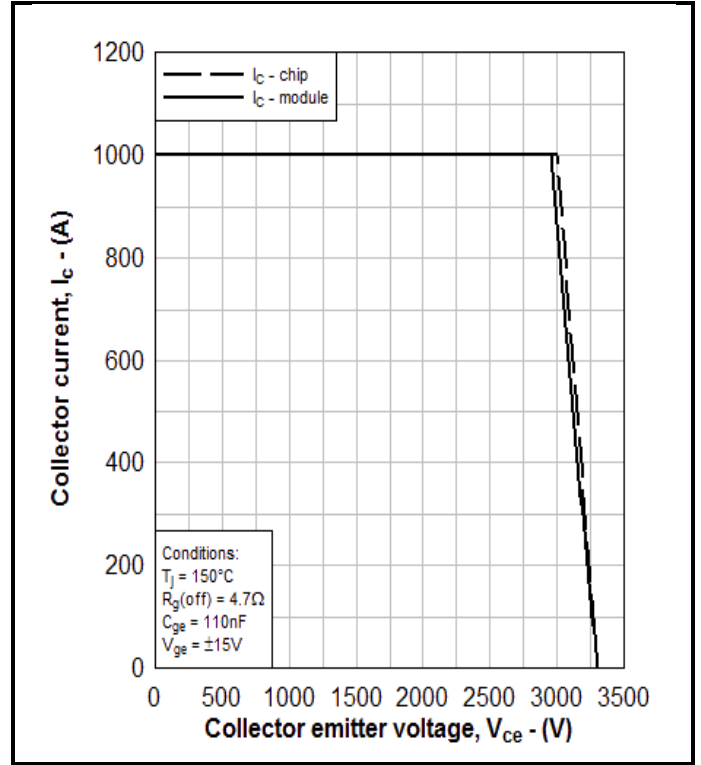


Fig. 8 Reverse bias safe operating area

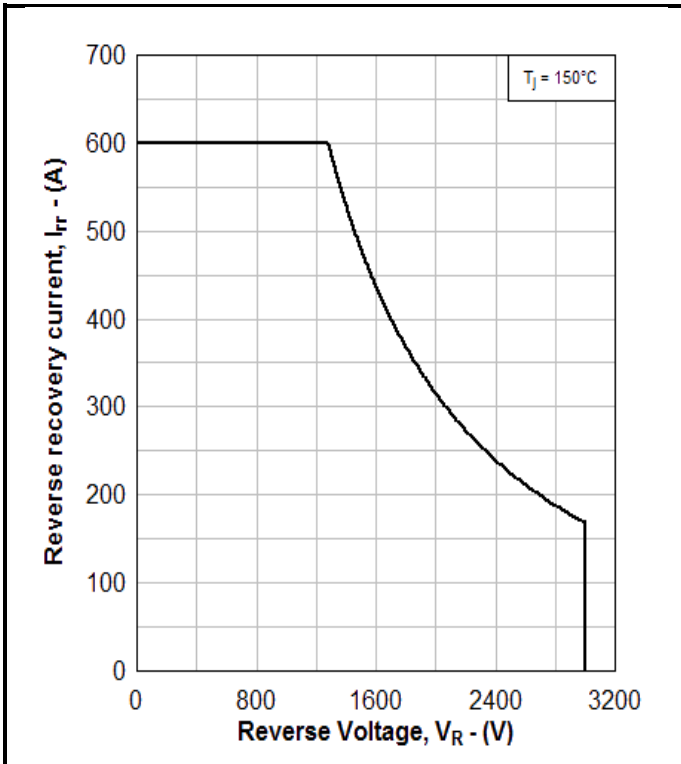


Fig. 9 Diode reverse bias safe operating area

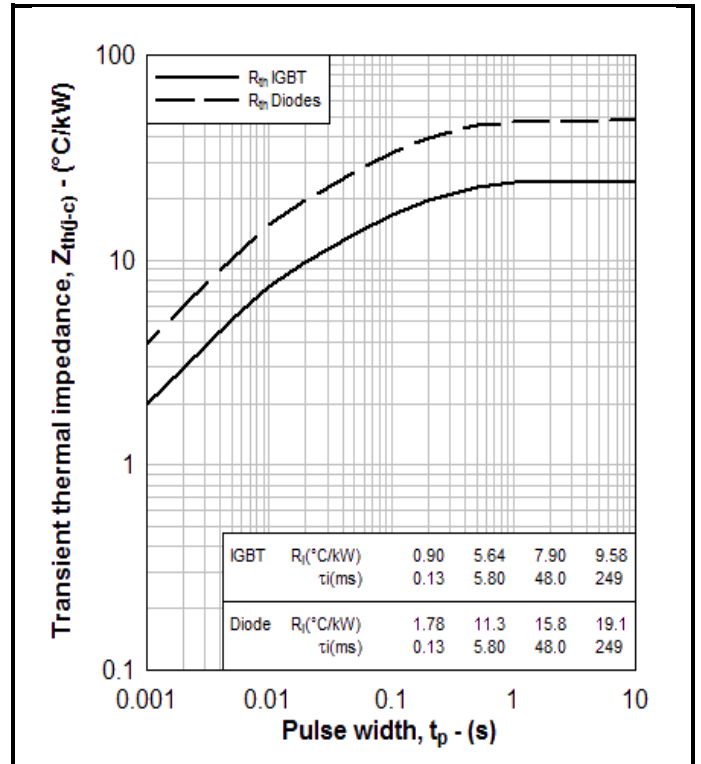
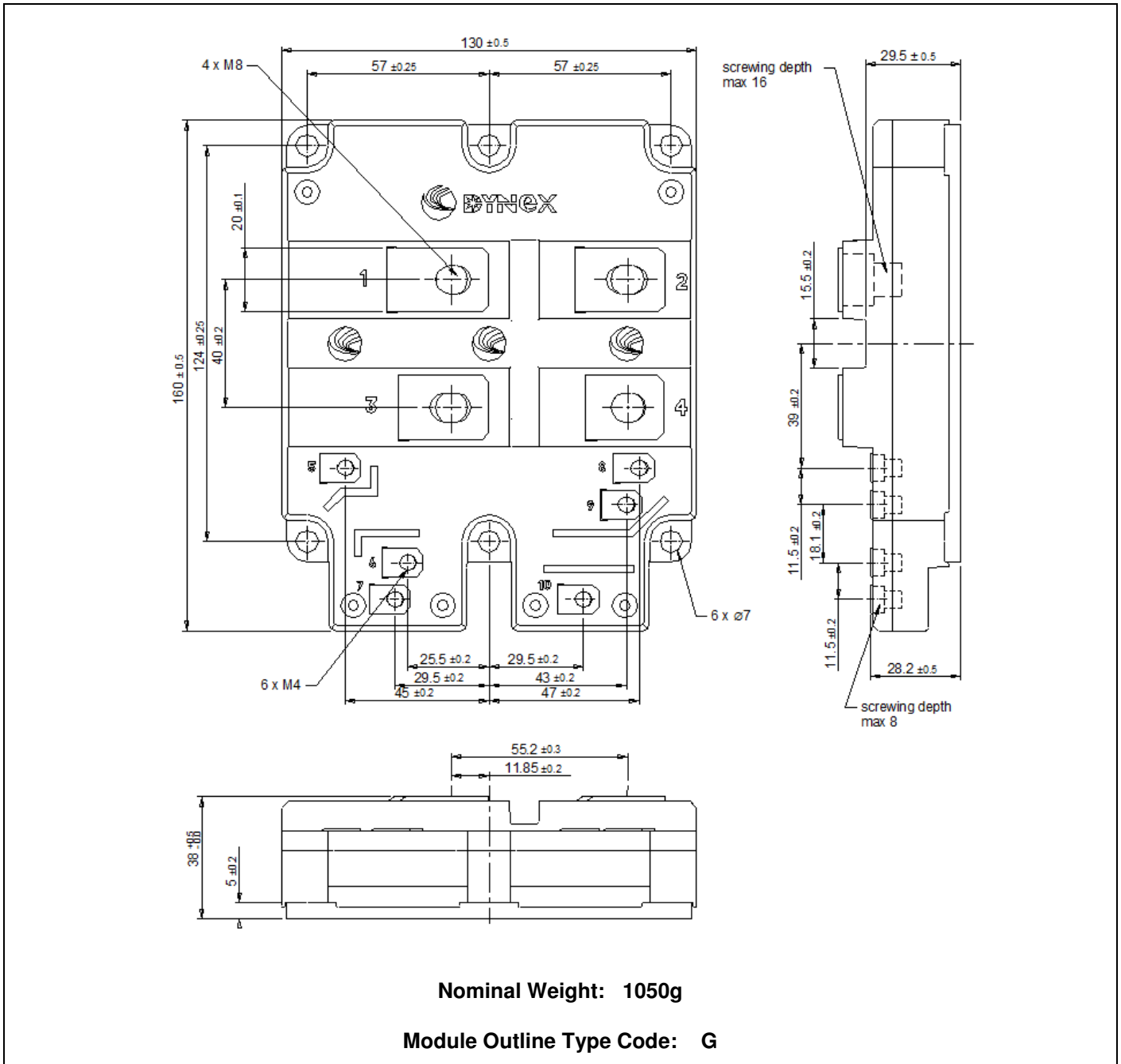


Fig. 10 Transient thermal impedance

**PACKAGE DETAILS**

For further package information, please visit our website or contact Customer Services.  
 All dimensions in mm, unless stated otherwise.  
**DO NOT SCALE.**


**Fig. 11 Module outline drawing**

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Extended exposure to conditions outside the product ratings may affect reliability leading to premature product failure. Use outside the product ratings is likely to cause permanent damage to the product. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture, a large current to flow or high voltage arcing, resulting in fire or explosion. Appropriate application design and safety precautions should always be followed to protect persons and property.

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