ACCUTEK
MICROCIRCUIT CORPORATION

AK61664Z
16,384 x 16 Bit CMOS/BiCMOS Static Random Access Memory

DESCRIPTION

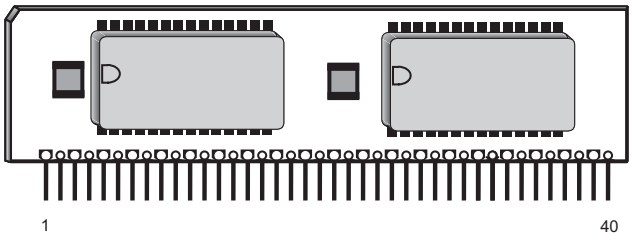
The Accutek AK61664 SRAM Module consists of fast high performance SRAMs mounted on a low profile 40 pin ZIP PCB. The module utilizes two 28 pin 64K x 4 SRAMs in 300 mil SOJ packages and two decoupling capacitor chips mounted on each side of a printed circuit board.

The SRAMs are interconnected on the module to have common I/O functions, single output enable functions and common chip select. The modules can be supplied in a variety of access time values from 8 nSEC to 45 nSEC in CMOS or BiCMOS technology.

The Accutek module is designed for the lowest height off the board, consistent with the availability of commonly available SRAM SOJ package configurations.

Front View

40-Pin ZIP



FEATURES

- 16,384 x 32 SRAM module in high density configuration
- 40 pin ZIP format
- Common I/O, single $\overline{OE}$ functions and common chip select $\overline{CE}$ Function
- Low height 0.500 inch ZIP maximum
- Fast Access Times range from 12 nSEC to 45 nSEC, CMOS or BiCMOS
- TTL compatible inputs and outputs
- Single 5 volt power supply - AK61664Z
- Single 3.3 volt power supply - AK61664Z/3.3
- Operating temperature range in free air, 0^0C to 70^0C
- Power (Typical)

CMOS	BiCMOS
3.2 Watt Active	4.8 Watt Active
1.2 Watt Standby	1.3 Watt Standby

PIN NOMENCLATURE

A_0 - A_{15}	Address Inputs
$\overline{WE}$	Write Enable
$\overline{OE}$	Output Enable
$\overline{CE}$	Chip Select
DQ_1 - DQ_{16}	Data In/Data Out
Vcc	5v Supply
Vss	Ground
NC	No Connect

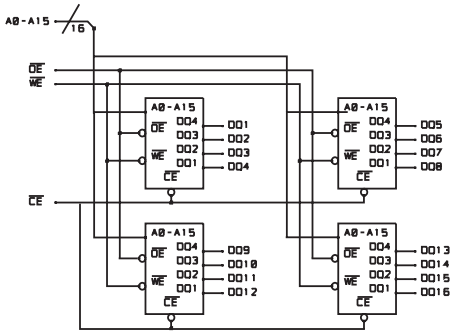
MODULE OPTIONS

Leaded ZIP: AK61664Z Single In-Line Package

PIN ASSIGNMENT

PIN #	SYMBOL	PIN #	SYMBOL
1	DQ_{16}	21	A_{11}
2	Vss	22	A_4
3	DQ_{15}	23	A_{10}
4	DQ_1	24	A_5
5	DQ_{14}	25	A_9
6	DQ_2	26	A_6
7	DQ_{13}	27	A_8
8	DQ_3	28	A_7
9	Vss	29	DQ_{12}
10	DQ_4	30	DQ_5
11	A_{15}	31	DQ_{11}
12	A_0	32	DQ_6
13	A_{14}	33	Vss
14	A_1	34	DQ_7
15	A_{13}	35	DQ_{10}
16	A_2	36	DQ_8
17	A_{12}	37	DQ_9
18	A_3	38	$\overline{OE}$
19	Vcc	39	$\overline{CE}$
20	Vss	40	$\overline{WE}$

FUNCTIONAL DIAGRAM



ORDERING INFORMATION

PART NUMBER CODING INTERPRETATION

Position	1	2	3	4	5	6	7	8

1 Product

AK = Accutek Memory

2 Type

4 = Dynamic RAM
5 = CMOS Dynamic RAM
6 = Static RAM

3 Organization/Word Width

1 = by 1 16 = by 16
4 = by 4 32 = by 32
8 = by 8 36 = by 36
9 = by 9

4 Size/Bits Depth

64	= 64K	4096	= 4 MEG
256	= 256K	8192	= 8 MEG
1024	= 1 MEG	16384	= 16 MEG

5 Package Type

G = Single In-Line Package (SIP)
S = Single In-Line Module (SIM)
D = Dual In-Line Package (DIP)
W = .050 inch Pitch Edge Connect
Z = Zig-Zag In-Line Package (ZIP)

6 Special Designation

P = Page Mode
N = Nibble Mode
K = Static Column Mode
W = Write Per Bit Mode
V = Video Ram

7 Separator

- = Commercial 0^0C to +70^0C
M = Military Equivalent Screened (-55^0C to +125^0C)
I = Industrial Temperature Tested (-45^0C to +85^0C)
X = Burned In

8 Speed (first two significant digits)

DRAMS			SRAMS		
50	=	50 nS	8	=	8 nS
60	=	60 nS	12	=	12 nS
70	=	70 nS	15	=	15 nS
80	=	80 nS	20	=	20 nS

The numbers and coding on this page do not include all variations available but are shown as examples of the most widely used variations. Contact Accutek if other information is required.

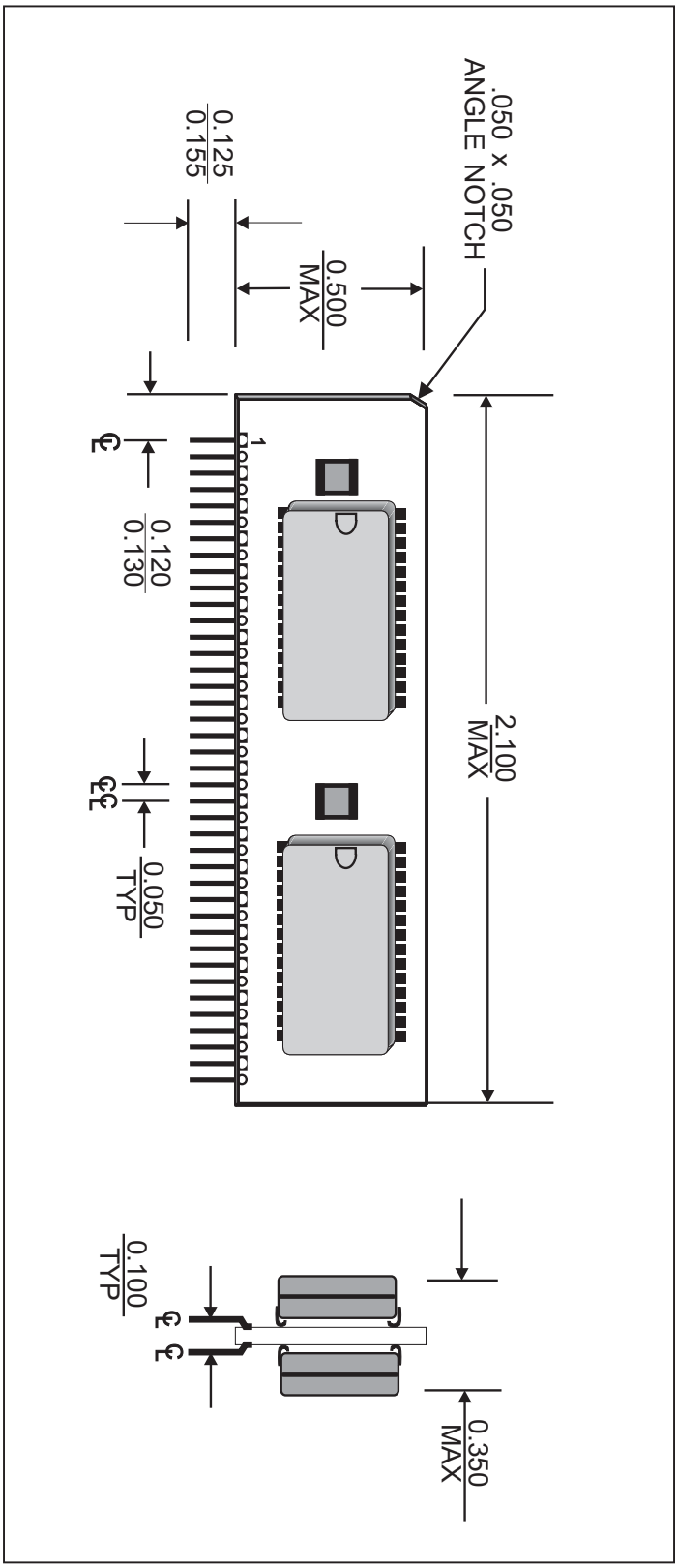
EXAMPLES:

AK61664Z-12

64K x 16, 12 nSEC SRAM ZIP Module

MECHANICAL DIMENSIONS

Inches



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ACCUTEK
MICROCIRCUIT CORPORATION

5 NEW PASTURE ROAD
NEWBURYPORT, MA 01950-4040
PHONE: 978-465-6200 FAX: 978-462-3396
Email: sales@accutekmicro.com
Internet: www.accutekmicro.com