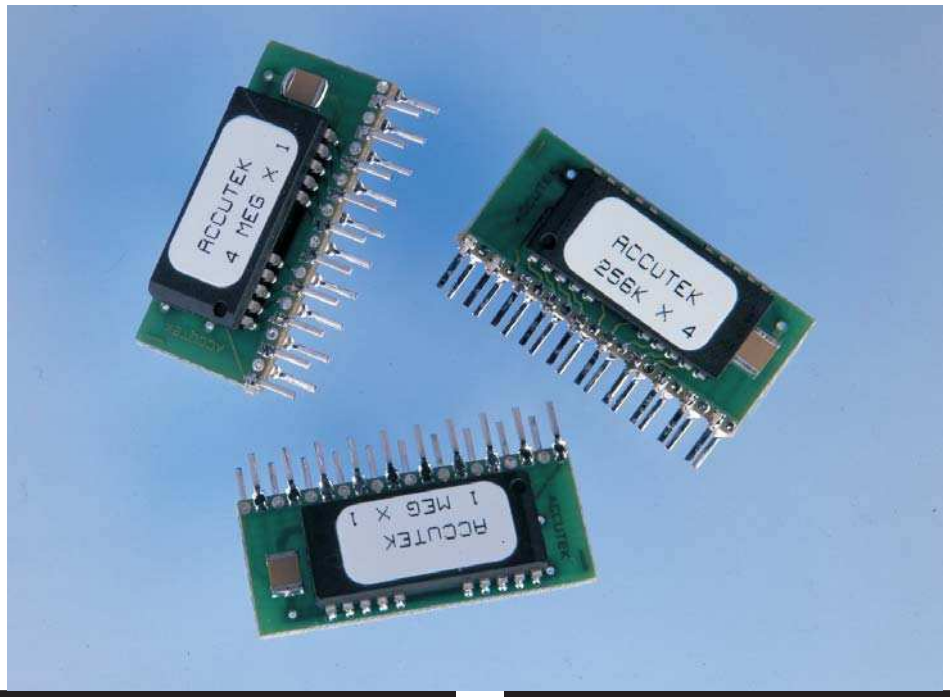
ACCUTEK
MICROCIRCUIT CORPORATION

DRAM Zip Adapter Modules
256K x 4 & 1 MEG x 4
1 MEG x 1 & 4 MEG x 1



PIN ASSIGNMENT

246K x 4 and 1 MEG x 4

PIN #	SYMBOL	PIN #	SYMBOL
1	$\overline{OE}$	11	A0
2	$\overline{CAS}$	12	A1
3	DQ3	13	A2
4	DQ4	14	A3
5	Vss	15	Vcc
6	DQ1	16	A4
7	DQ2	17	A5
8	$\overline{WE}$	18	A6
9	$\overline{RAS}$	19	A7
10	A9 (NC for 256K x 4)	20	A8

PIN ASSIGNMENT

1 MEG x 1 and 4 MEG x 1

PIN #	SYMBOL	PIN #	SYMBOL
1	A9	11	A0
2	$\overline{CAS}$	12	A1
3	Dout	13	A2
4	Vss	14	A3
5	Din	15	Vcc
6	$\overline{WE}$	16	A4
7	$\overline{RAS}$	17	A5
8	A10 (N/C for 1 Meg x 1)	18	A6
9	N/C	19	A7
10	N/C	20	A8

The Accutek family of AccuZIP DRAM adapter modules is designed to convert a DRAM in a surface mount SOJ package into a side-mounted ZIP package. This pin compatible module eliminates PC board retooling when monolithic ZIP packages become unavailable. These AccuZIP modules can be used to save space on complex circuit boards using thru-hole technology.

The family consists of a series of unique low-profile printed circuit boards designed to accept IC's in either 0.300" SOJ packages for the 256K x 4, 1 MEG x 4, 1 MEG x 1 and 4 MEG x 1 or 0.400" SOJ packages for the 512K x 8, 256K x 16, 256K x 18, 1 MEG x 16 and 1 MEG x 18.

FEATURES

- Saves costly redesign of current PC boards
- Allows for increased memory and density
- On board decoupling capacitor
- Low height of 0.425" above mother board
- 100% tested with life time warranty

PART NUMBERS

- AK54256Z-SS — 256K x 4
- AK541024Z-SS — 1 Meg x 4
- AK511024Z-SS — 1 Meg x 1
- AK514096Z-SS — 4 Meg x 1

SS - Speed Designator (60 nSEC, 70 nSEC, 80 nSEC)

Accutek reserves the right to make changes in specifications at any time and without notice. Accutek does not assume any responsibility for the use of any circuitry described; no circuit patent licenses are implied. Preliminary data sheets contain minimum and maximum limits based upon design objectives, which are subject to change upon full characterization over the specific operating conditions.

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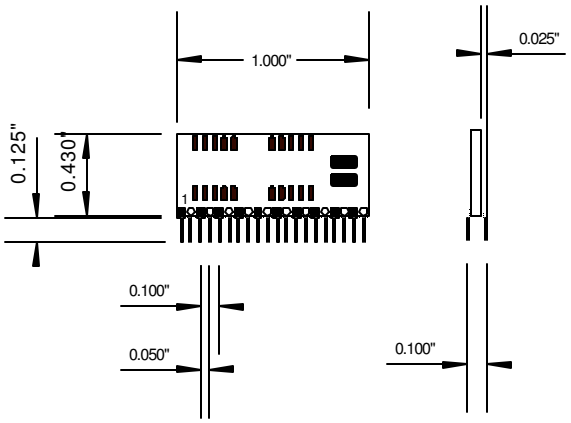
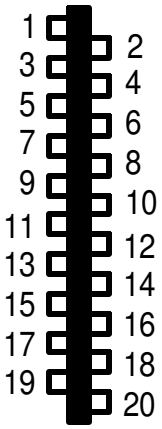
AK20Z-SOJ
(for 1Meg & 4 Meg DRAMs)



TOP VIEW