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STK404-140N-E

Thick-Film Hybrid IC

1ch class-AB Audio Power IC 180W

Overview

The STK404-140N-E is a hybrid IC for the audio power amplifier that mounts discrete components as the audio power amplifier circuit in small space using the original Insulated Metal Substrate Technology IMST. The compact package has been achieved by adopting the low thermal resistance substrate (our conventional model kind ratio).

Application

- Audio Power use

Features

- Pin-to-pin compatible outputs ranging from 60W to 180W
- Miniature package
- Output load impedance: $R_L = 6\Omega$ recommended.
- Allowable load shorted time: 0.3 second
- Allows the use of predesigned applications for standby, mute, and the load short protection circuit.

Selection Guide

	STK404-070N-E	STK404-120N-E	STK404-140N-E
Output1 (10%/1kHz)	60W × 1ch	120W × 1ch	180W × 1ch
Output2 (1%/20Hz to 20kHz)	40W × 1ch	80W × 1ch	120W × 1ch
Maximum rating V_{CC} max (no sig.)	±46V	±65V	±78V
Maximum rating V_{CC} (6Ω)	±39V	±59V	±73V
Recommended operating V_{CC} (6Ω)	±30V	±41V	±51V
Package size	44.0mm×25.6mm×8.5mm	46.6mm×25.5mm×8.5mm	59.2mm×25.5mm×8.5mm

Specifications

Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$, $T_c = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Conditions	Ratings	Unit
Power supply voltage 1	V_{CC} max1	Non- signal	±78	V
Power supply voltage 2	V_{CC} max2	Signal, $R_L = 6\Omega$	±73	V
Thermal detector maximum voltage	V_p	1-2pin	16	V
Thermal detector maximum current	I_p	1-2pin	30	mA
Thermal resistance	θ_{j-c}	Per one power transistor	1.1	$^\circ\text{C/W}$
Junction temperature	T_J max		150	$^\circ\text{C}$
Operating substrate temperature	T_c max		125	$^\circ\text{C}$
Storage temperature	T_{stg}		-30 to +125	$^\circ\text{C}$
Allowable time for load short-circuit *4	t_s	$V_{CC} = \pm 51\text{V}$, $R_L = 6\Omega$, $f = 50\text{Hz}$ $P_O = 120\text{W}$	0.3	s

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

ORDERING INFORMATION

See detailed ordering and shipping information on page 11 of this data sheet.

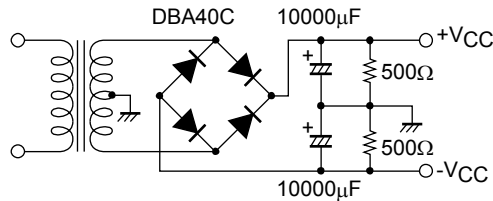
Operating Characteristics at $T_c = 25^\circ\text{C}$, $R_L = 6\Omega$ (Non-inductive load), $R_g = 600\Omega$, $V_G = 30\text{dB}$

Parameter	Symbol	Conditions					Ratings			Unit
		V_{CC} [V]	f [Hz]	P_O [W]	THD [%]		min	typ	max	
Output power	P_{O1}	± 51	20 to 20k		1.0		120			W
	P_{O2}	± 51	1k		10			180		
Frequency characteristics	f_L, f_H	± 51		1.0		+0 -3dB	20 to 20k			Hz
Input impedance	r_i	± 51	1k	1.0				55		$k\Omega$
Output noise voltage *3	V_{NO}	± 62				$R_g = 10k\Omega$		1.2		mVrms
Output neutral voltage	V_N	± 62					-100	0	+100	mV
Quiescent current	I_{CCO}	± 62				No load			60	mA
Thermal detector resistance *2	R_p	$T_p = 25^\circ\text{C}$, 1-2pin						470		Ω
Thermal detector operate temperature *2	T_p	$R_p = 4.7k\Omega$, 1-2pin						145		$^\circ\text{C}$

[Note]

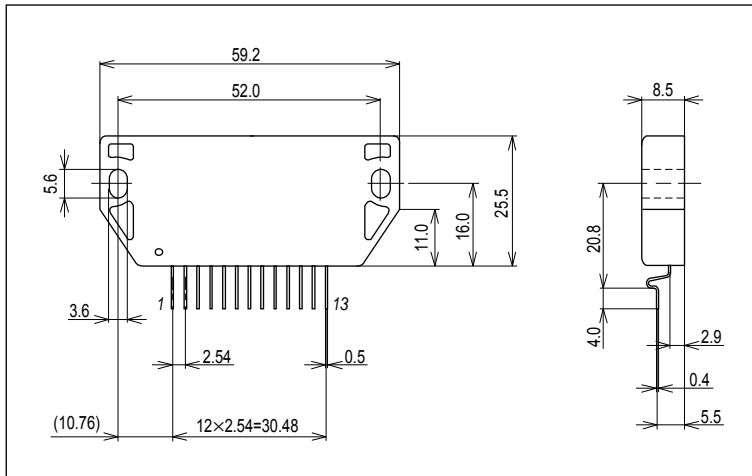
- *1. All tests are measured using a constant-voltage supply unless otherwise specified.
- *2. Thermal Detector temperature ($+145^\circ\text{C} \pm 5^\circ\text{C}$) indicates the value at unusual operation, therefore, does not indicate the guaranteed value at usual operation.
Thermal Detector is PRF18series (AS characteristic) manufactured by MURATA.
- *3. The output noise voltage is peak value of an average-reading meter with a rms value scale (VTVM).
A regulated AC supply (50Hz) should be used to eliminate the effects of AC primary line flicker noise.
- *4. Allowable time for load short-circuit and output noise voltage are measured using the specified transformer power supply. About the load short circuit, it is designed assuming protecting by cut-off within 0.3 second.
- *5. Weight of 1 HIC : 17.2g Outer carton dimensions (W×L×H) : 502mm×247mm×282mm

(Equivalent to MG-250)



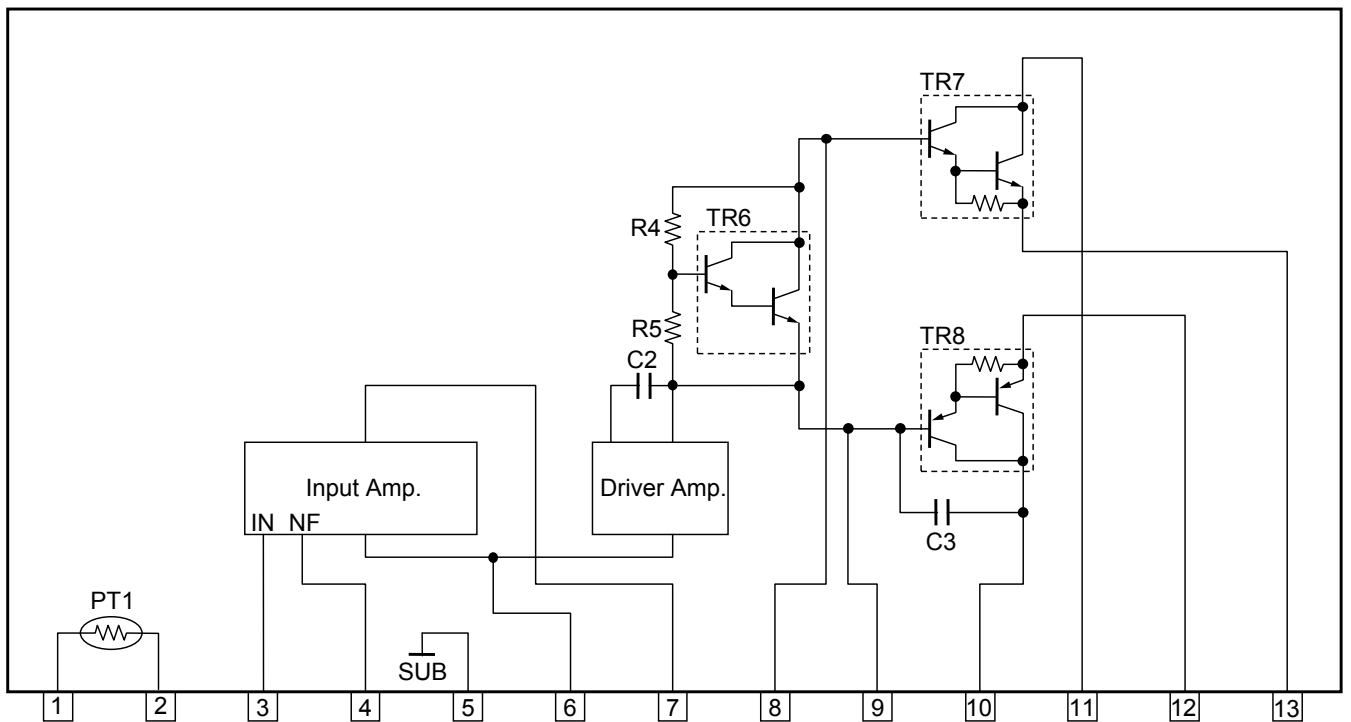
Package Dimensions

unit : mm (typ)

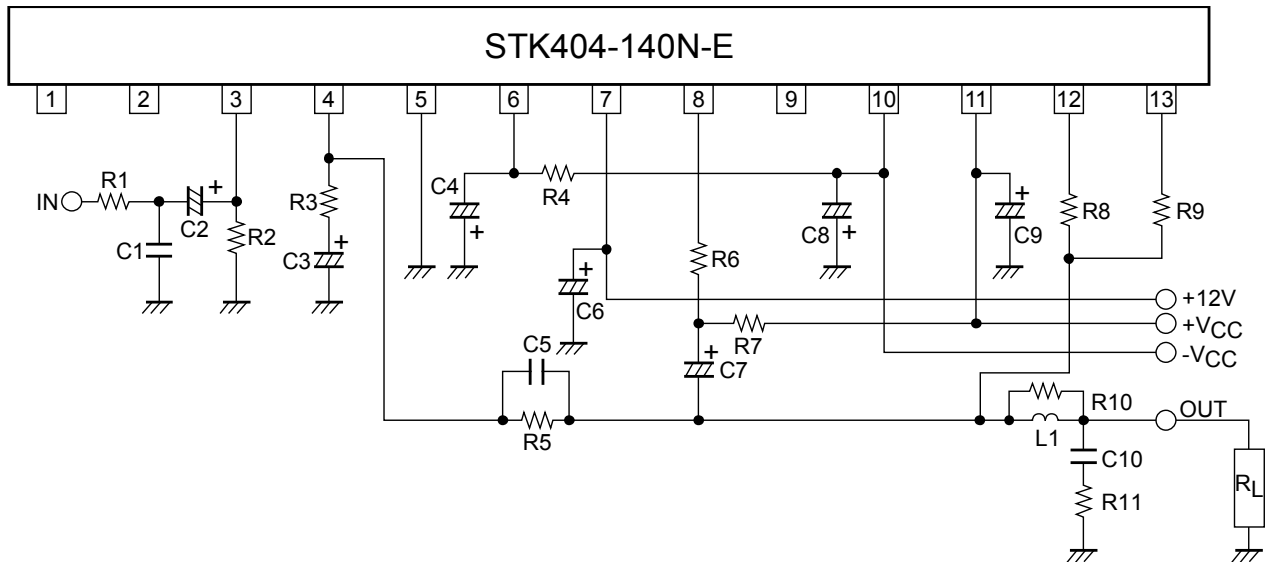


RoHS directive pass

Equivalent Circuit

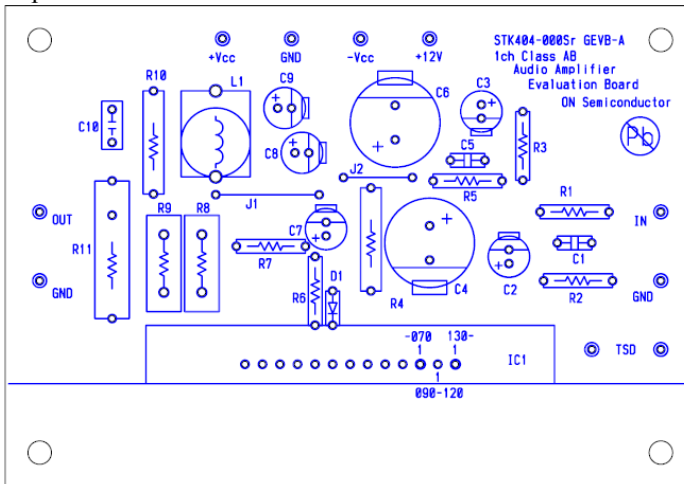


Test Circuit

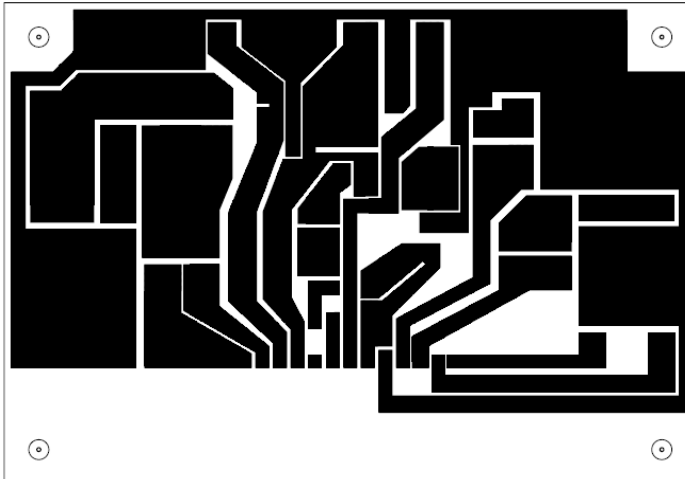


PCB Layout Example

Top view



Top view



STK404-140N-E

PCB Parts List

Type (IC1)	STK404-070N-E	STK404-120N-E	STK404-140N-E
Position of (1)pin	Third from the right end	Second from the right end	The right end
Location			
R1	1k Ω	←	←
R2	56k Ω	←	←
R3	1.8k Ω	←	←
R4	100 Ω /1W	←	←
R5	56k Ω	←	←
R6	10k Ω /1W	4.7k Ω /1W	5.1k Ω /1W
R7	10k Ω /1W	4.7k Ω /1W	5.1k Ω /1W
R8	0.22 Ω /5W	←	←
R9	-	0.22 Ω /5W	←
R10	4.7 Ω /1W	←	←
R11	4.7 Ω /1W	←	←
C1	470pF	←	←
C2	2.2 μ F/50V	←	←
C3	10 μ F/50V	←	←
C4	100 μ F/100V	←	←
C5	5pF	←	←
C6	100 μ F/50V	←	←
C7	47 μ F/100V	←	←
C8	10 μ F/100V	←	←
C9	10 μ F/100V	←	←
C10	0.1 μ F	←	←
D1	200V/0.5A	Short	Short
L1	2.2 μ H	←	←
J1	15mm	←	←
J2	10mm	←	←

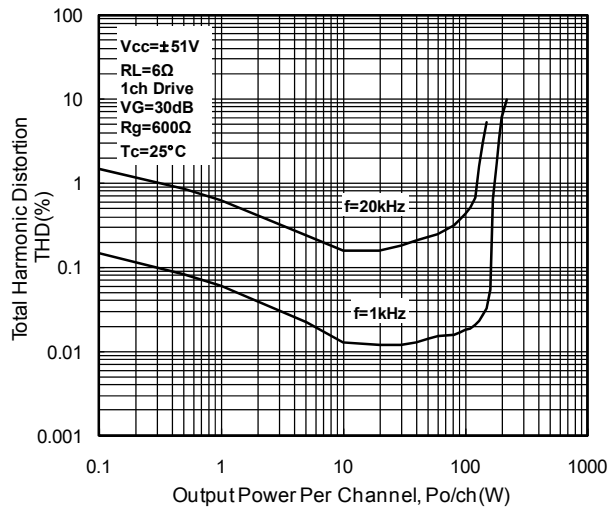
Pin Layout

[STK404-000Nsr Pin Layout]

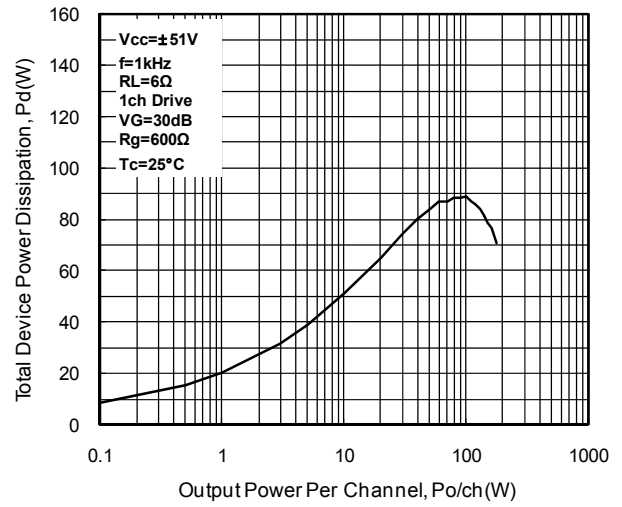
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Characteristic of Evaluation Board

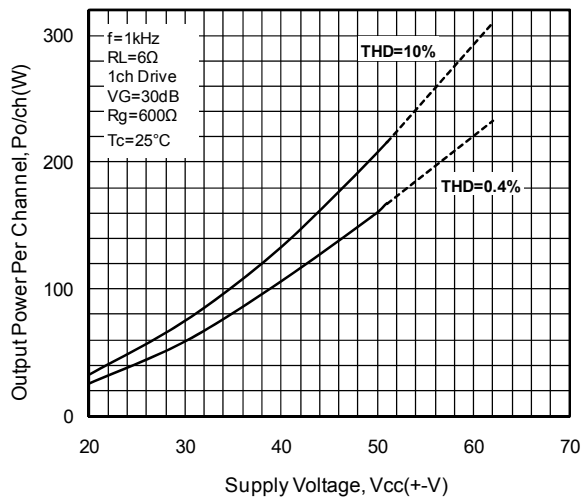
THD-Po
STK404-140N-E



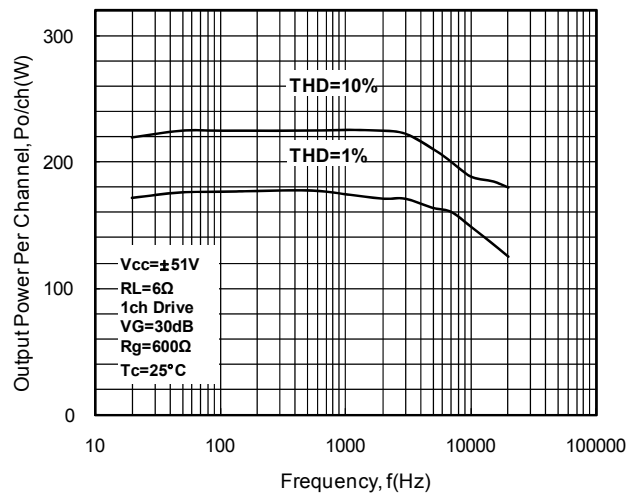
Pd-Po
STK404-140N-E



Po-Vcc
STK404-140N-E



Po-f
STK404-140N-E



A Thermal Design Tip For STK404-140N-E Amplifier**[Thermal Design Conditions]**

The thermal resistance (θ_{c-a}) of the heat-sink which manages the heat dissipation inside the Hybrid IC will be determined as follow:

(Condition 1) The case temperature (T_c) of the Hybrid IC should not exceed 125°C

$$P_d \times \theta_{c-a} + T_a < 125^\circ\text{C} \dots\dots\dots (1)$$

Where T_a : the ambient temperature for the system

(Condition 2) The junction temperature of each power transistor should not exceed 150°C

$$P_d \times \theta_{c-a} + P_d/N \times \theta_{j-c} + T_a < 150^\circ\text{C} \dots\dots\dots (2)$$

Where N : the number of transistors (two for 1 channel , ten for channel)

θ_{j-c} : the thermal resistance of each transistor (see specification)

Note that the power consumption of each power transistor is assumed to be equal to the total power dissipation (P_d) divided by the number of transistors (N).

From the formula (1) and (2), we will obtain:

$$\theta_{c-a} < (125 - T_a)/P_d \dots\dots\dots (1)'$$

$$\theta_{c-a} < (150 - T_a)/P_d - \theta_{j-c}/N \dots\dots\dots (2)'$$

The value which satisfies above formula (1)' and (2)' will be the thermal resistance for a desired heat-sink.

Note that all of the component except power transistors employed in the Hybrid IC comply with above conditions.

[Example of Thermal Design]

Generally, the power consumption of actual music signals are being estimated by the continuous signal of 1/8 $P_{O \text{ max}}$. (Note that the value of 1/8 $P_{O \text{ max}}$ may be varied from the country to country.)

(Sample of STK404-140N-E ; 120W×1ch)

If V_{CC} is $\pm 51\text{V}$, and R_L is 6Ω , then the total power dissipation (P_d) of inside Hybrid IC is as follow;

$$P_d = 57.2\text{W (at 15W output power, 1/8 of } P_{O \text{ max}})$$

There are four (2) transistors in Audio Section of this Hybrid IC, and thermal resistance (θ_{j-c}) of each transistor is 1.1°C/W. If the ambient temperature (T_a) is guaranteed for 50°C, then the thermal resistance (θ_{c-a}) of a desired heat-sink should be;

$$\begin{aligned} \text{From (1)'} \quad \theta_{c-a} &< (125 - 50)/57.2 \\ &< 1.31 \end{aligned}$$

$$\begin{aligned} \text{From (2)'} \quad \theta_{c-a} &< (150 - 50)/57.2 - 1.1/2 \\ &< 1.19 \end{aligned}$$

Therefore, in order to satisfy both (1)' and (2)', the thermal resistance of a desired Heat-sink will be 1.19°C/W.

[Note]

Above are reference only. The samples are operated with a constant power supply. Please verify the conditions when your system is actually implemented.

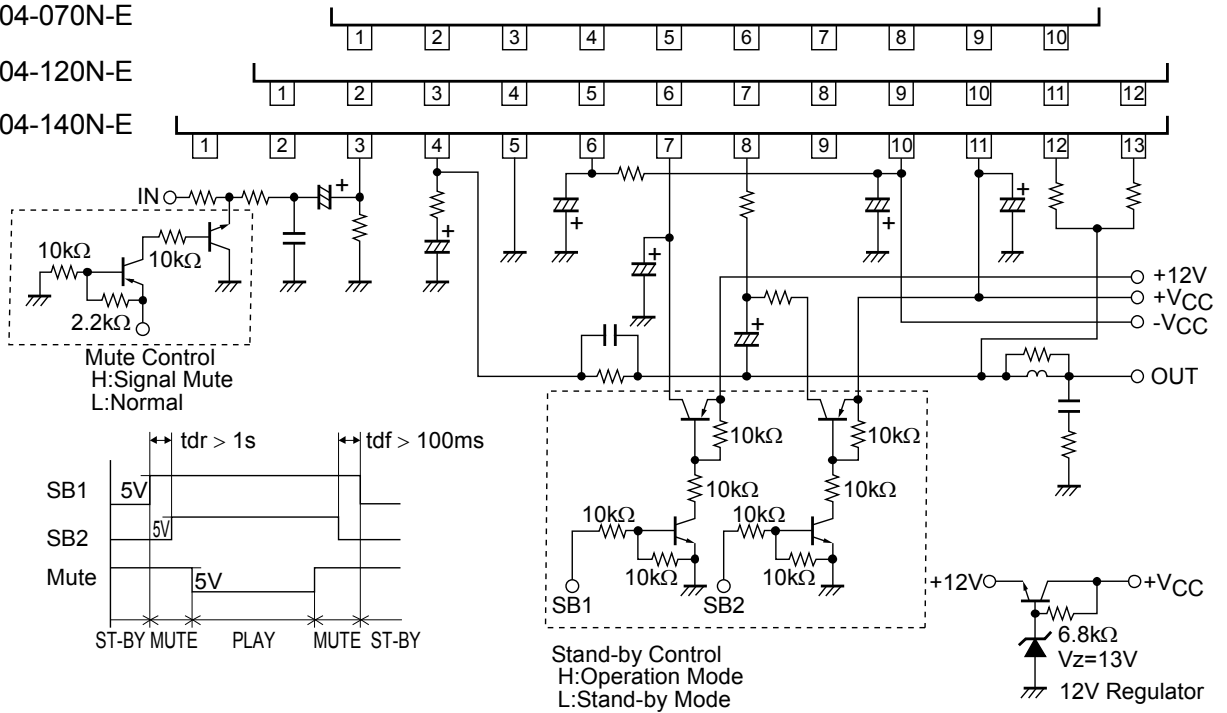
STK404-140N-E

STK404-000N-Esr Stand-by Control & Mute Control Application

STK404-070N-E

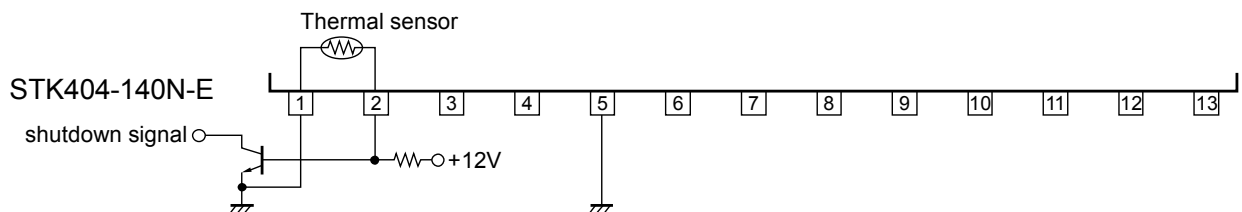
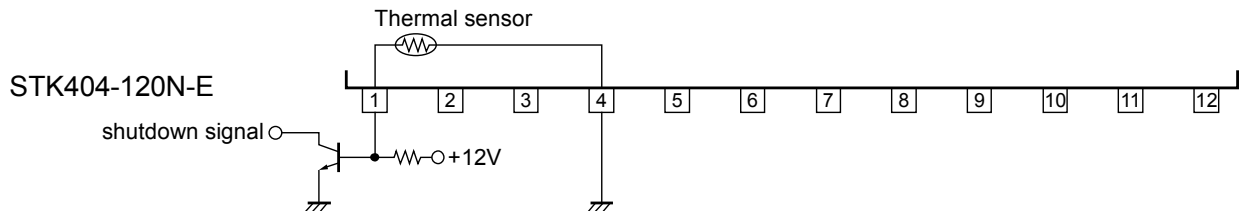
STK404-120N-E

STK404-140N-E

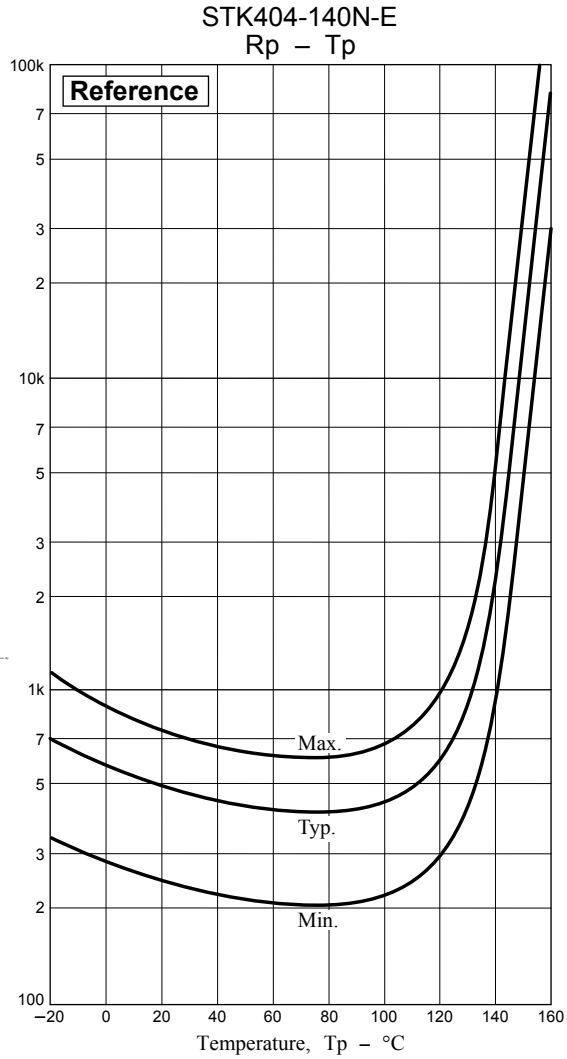


STK404-000N-Esr Thermal shut down Application

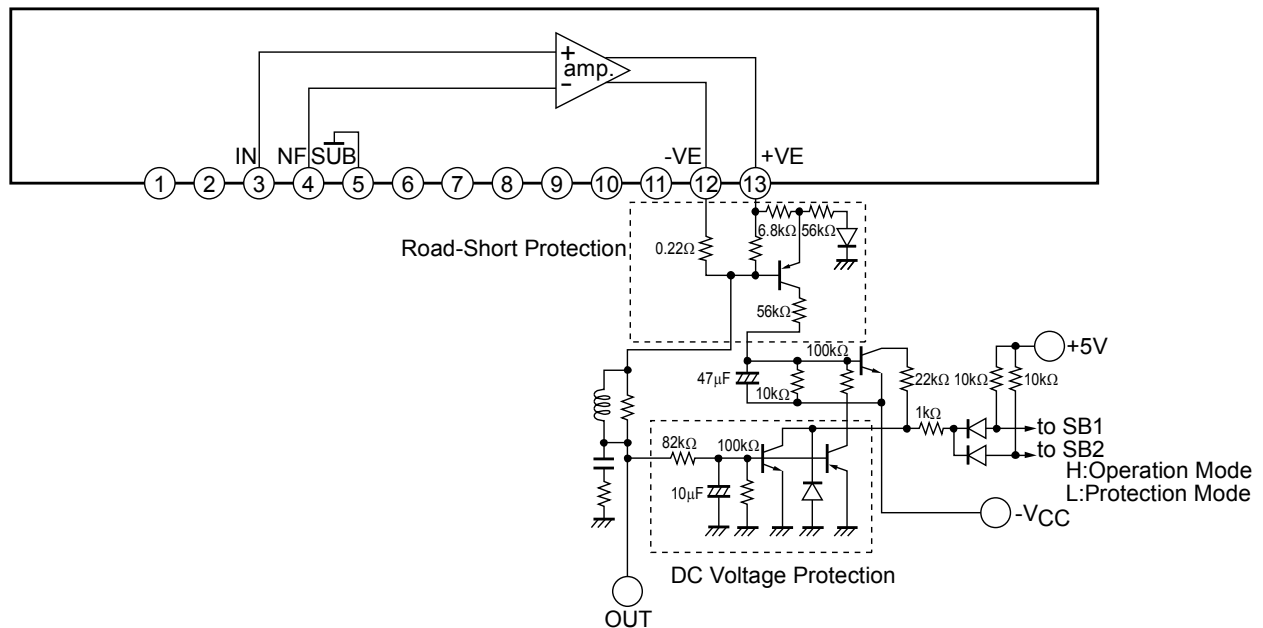
STK404-070N-E No thermal sensor



Thermal Sensor Characteristic



STK404-140N-E Road-Short & DC Voltage Protection Application



ORDERING INFORMATION

Device	Package	Shipping (Qty / Packing)
STK404-140N-E	SIP13 (Pb-Free)	25 / Bulk Box

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