

# SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

SBE808 — Schottky Barrier Diode 15V, 1A Rectifier

#### **Applications**

• High frequency rectification (switching regulators, converters, choppers)

#### **Features**

- · Small switching noise
- · Low leakage current and high reliability due to planar structure
- · Ultrasmall package permitting applied sets to be small and slim

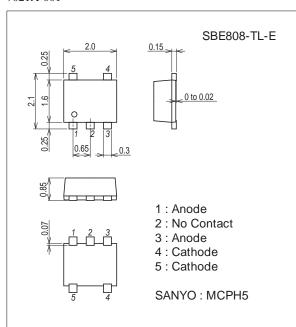
### **Specifications**

#### **Absolute Maximum Ratings** at Ta=25°C (Value per element)

Parameter	Symbol	Conditions	Ratings	Unit
Repetitive Peak Reverse Voltage	VRRM		15	V
Nonrepetitive Peak Reverse Surge Voltage	VRSM		17	V
Average Output Current	IO		1	Α
Surge Forward Current	IFSM	50Hz sine wave, 1 cycle	10	Α
Junction Temperature	Tj		-55 to +150	C
Storage Temperature	Tstg		-55 to +150	Ĉ

#### **Package Dimensions**

unit : mm (typ) 7021A-001



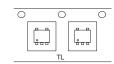
#### **Product & Package Information**

• Package : MCPH5

• JEITA, JEDEC : SC-88A, SC-70-5, SOT-353

• Minimum Packing Quantity : 3,000 pcs./reel

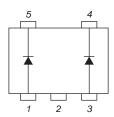
#### Packing Type: TL



#### Marking



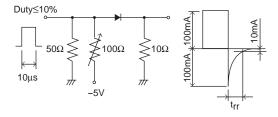
#### **Electrical Connection**



#### Electrical Characteristics at Ta=25°C (Value per element)

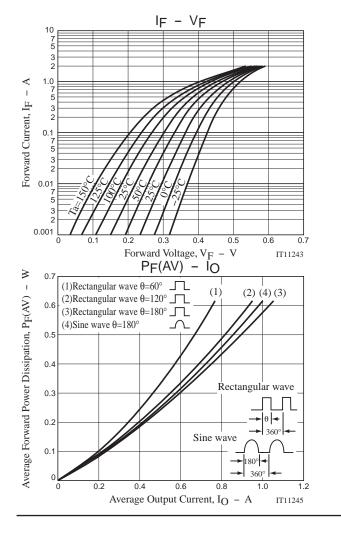
Parameter	Symbol	Conditions	Ratings			Unit
	Symbol	Conditions	min	typ	max	J OIIIL
Reverse Voltage	VR	I <sub>R</sub> =0.1mA	15			V
Forward Voltage	V <sub>F</sub> 1	I <sub>F</sub> =0.5A		0.43	0.48	V
	V <sub>F</sub> 2	I <sub>F</sub> =1A		0.49	0.54	V
Reverse Current	IR	V <sub>R</sub> =6V			3.0	μΑ
Interterminal Capacitance	С	V <sub>R</sub> =10V, f=1MHz		20		pF
Reverse Recovery Time	t <sub>rr</sub>	IF=IR=100mA, See specified Test Circuit.			10	ns
Thermal Resistance	Rth(j-a)	When mounted on ceramic substrate (600mm <sup>2</sup> ×0.8mm)		185		℃\M

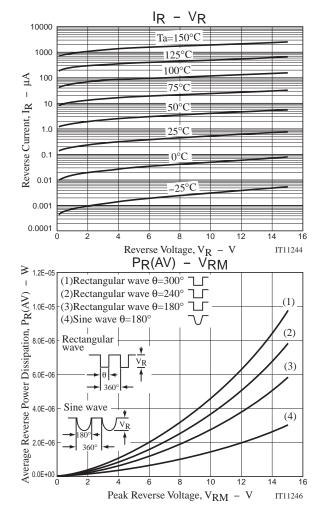
#### trr Test Circuit



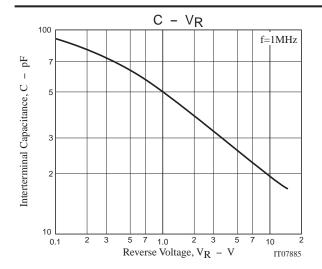
#### **Ordering Information**

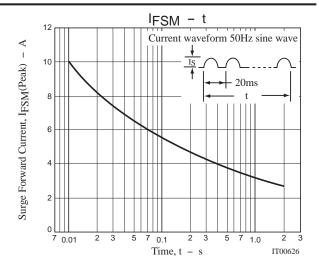
Device	Package	Shipping	memo	
SBE808-TL-E	MCPH5	3,000pcs./reel	Pb Free	





# **SBE808**



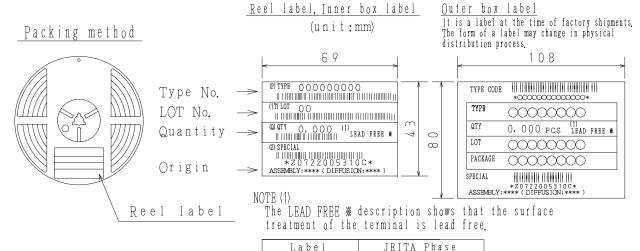


#### **Taping Specification**

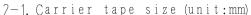
#### SBE808-TL-E

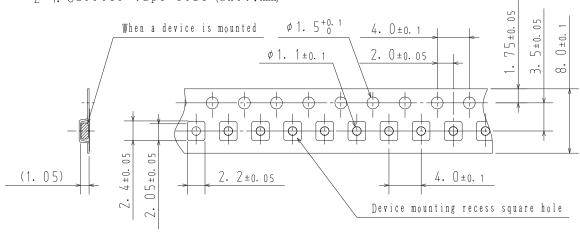
#### 1. Packing Format

Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing	ig format		
	Туре	Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)		
MCPH5	MCP4	3, 000	15, 000	90,000	5 reels contained	6 inner boxes contained		
					Dimensions:mm (external)	Dimensions:mm (external)		
					183×72×185	440×195×210		



## 7. Taping configuration



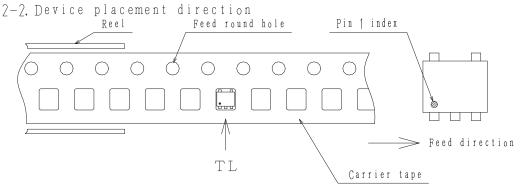


LEAD FREE

LEAD FREE 4

JEITA Phase 3A

JEITA Phase 3



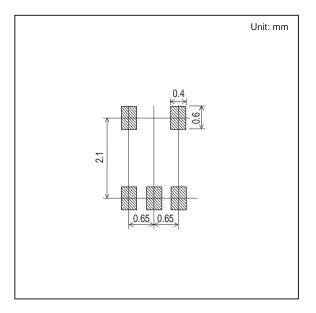
Those with two electrode terminal on the feed hole side·····TL

# **Outline Drawing**

SBE808-TL-E

# Mass (g) Unit 0.008 \*For reference mm 0. 15<sup>+0. 1</sup><sub>-0. 03</sub> 0. 25±0.06 2. O±0.06 5 4 0~0.02 2, 1±0.05 1.6±0.06 0 0. 3+0. 1 0. 1 M A 0. 65 PIN#1 0.85±0.05 0. 05 S \*1:Lot indication

# **Land Pattern Example**



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