

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

SFT1431 — General-Purpose Switching Device Applications

Features

- · Motor drive application
- · 4V drive

- · Low ON-resistance
- · Protection diode in

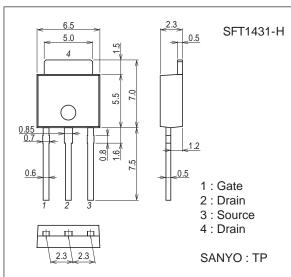
Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		35	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		11	Α
Drain Current (PW≤10μs)	IDP	PW≤10μs, duty cycle≤1%	44	Α
Allowable Dower Discinction	D-		1.0	W
Allowable Power Dissipation	PD	Tc=25°C	15	W
Channel Temperature	Tch		150	C
Storage Temperature	Tstg		-55 to +150	C

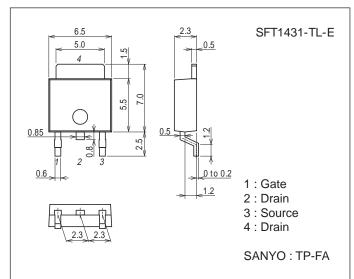
Package Dimensions unit: mm (typ)

7518-004



Package Dimensions unit: mm (typ)

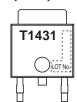
7003-004



Product & Package Information

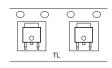
- Package: TP
- JEITA, JEDEC: SC-64, TO-251
- Minimum Packing Quantity: 500 pcs./bag

Marking (TP, TP-FA)

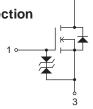


- Package: TP-FA
- JEITA, JEDEC : SC-63, TO-252
- Minimum Packing Quantity: 700 pcs./reel

Packing Type (TP-FA): TL



Electrical Connection



SANYO Semiconductor Co., Ltd.

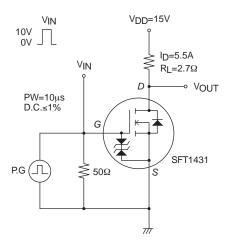
http://semicon.sanyo.com/en/network

SFT1431

Electrical Characteristics at Ta=25°C

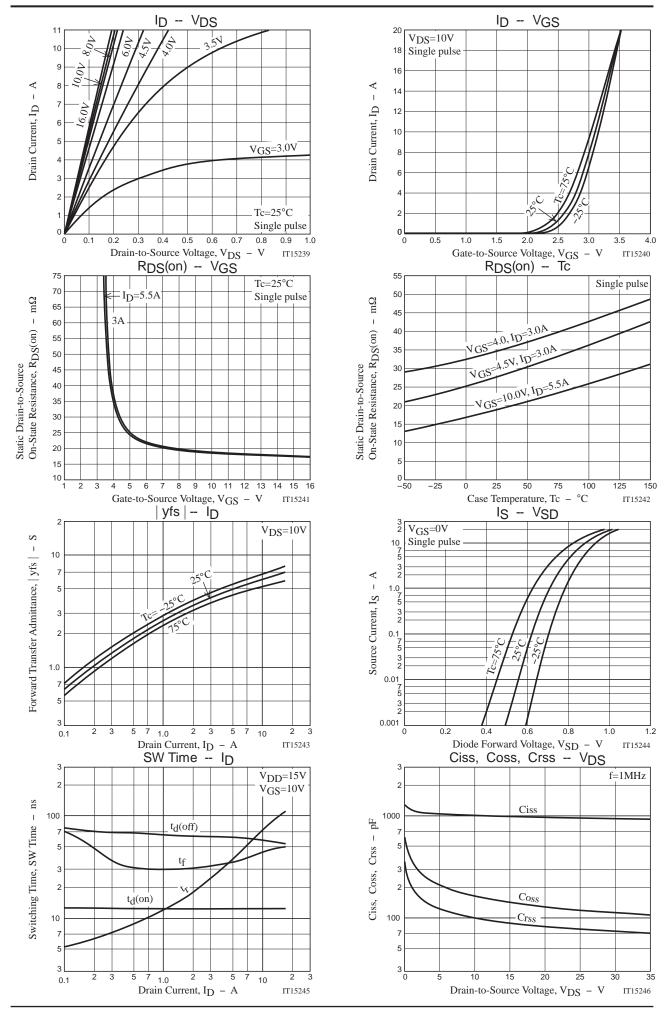
Parameter	Symbol	Conditions	Ratings			Unit	
Farameter	Syllibol	Conditions	min	typ	max	Offic	
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	35			V	
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =35V, V _{GS} =0V			1	μΑ	
Gate-to-Source Leakage Current	IGSS	VGS=±16V, VDS=0V			±10	μΑ	
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.2		2.6	V	
Forward Transfer Admittance	yfs	VDS=10V, ID=5.5A		5		S	
	R _{DS} (on)1	I _D =5.5A, V _G S=10V		19	25	mΩ	
Static Drain-to-Source On-State Resistance	R _{DS} (on)2	I _D =3A, V _G S=4.5V		28	39.5	mΩ	
	R _{DS} (on)3	I _D =3A, V _G S=4V		35	49	mΩ	
Input Capacitance	Ciss			960		pF	
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		130		pF	
Reverse Transfer Capacitance	Crss			84		pF	
Turn-ON Delay Time	t _d (on)			12		ns	
Rise Time	t _r	3		40		ns	
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		60		ns	
Fall Time	tf			36		ns	
Total Gate Charge	Qg			17.3		nC	
Gate-to-Source Charge	Qgs	V _{DS} =20V, V _{GS} =10V, I _D =11A		3.2		nC	
Gate-to-Drain "Miller" Charge	Qgd			3.6		nC	
Diode Forward Voltage	V _{SD}	I _S =11A, V _{GS} =0V		0.88	1.2	V	

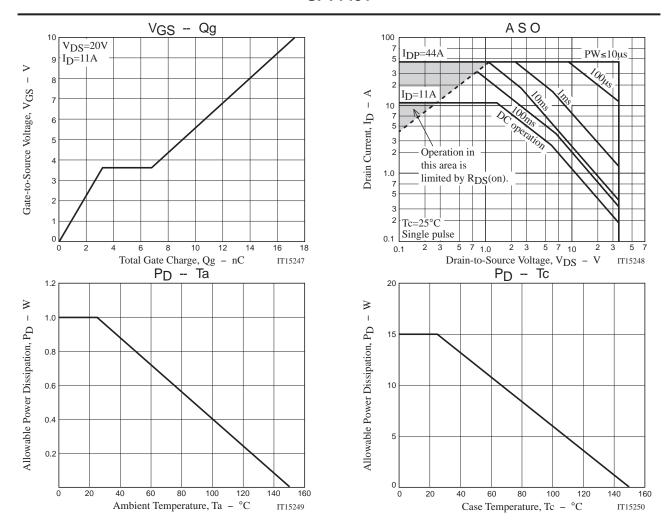
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
SFT1431-H	TP	500pcs./bag	Pb Free
SFT1431-TL-E	TP-FA	700pcs./reel	Pb Free



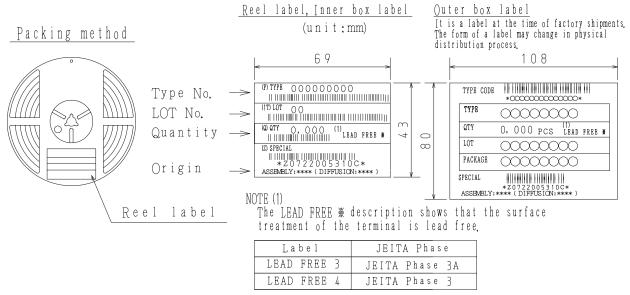


Taping Specification

SFT1431-TL-E

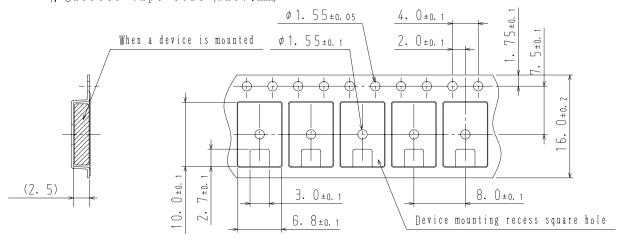
Packing Format

Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing	format
	Туре	Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
TP-FA	TP	700	2, 100	12, 600	3 reels contained	6 inner boxes contained
					Dimensions:mm (external)	Dimensions:mm (external)
					183×72×185	440×195×210

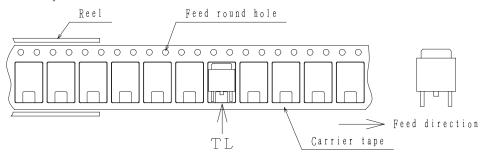


Taping configuration

1. Carrier tape size (unit:mm)



7. Device placement direction



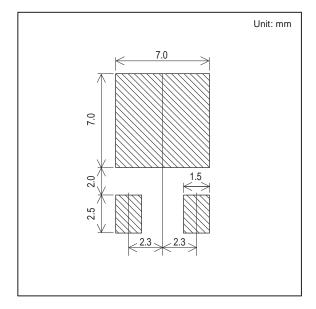
Those with one electrode terminal on the feed hole side·····TL

Outline Drawing

SFT1431-TL-E

Mass (g) Unit 0.282 *For reference mm 6. 5±0. 2 2. 3±0. 2 5. O±0. 2 1. 5±0. 2 0. 5±0. 1 [*1] 7. 0±0. 3 5. 5±0. 2 LOT No. 0. 5±0. 15 1. 2±0.3 0. 85±0. 2 2. 5±0. 3 0. 7±0.2 2 3 1. 2±0. 3 0. 6±0. 2 0~0.2 2. 3±0. 2 2. 3±0. 2 Pin 2 is idle pin with electrical designation only carried. *1:Lot indication

Land Pattern Example



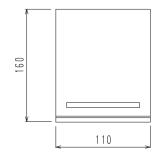
Bag Packing Specification

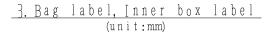
SFT1431-H

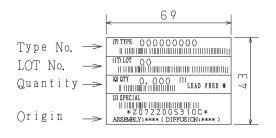
1. Packing Format

Package Name		Maximum Number of devices contained (pcs)					
I donago Mamo	Bag	Inner box	Outer box				
TP		B-1	A-1	A-2			
1 Γ	500	10,000	50,000	30,000			
		Packing fo	ormat (Dimensions:mm (external))				
		Inner box	Outer box				
		B-1	A-1	A-2			
		445×225×55	470×250×300	470×250×190			

2. Bag dimensions (unit:mm)





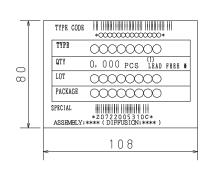


4. Outer box label (unit:mm)

It is a label at the time of factory shipments, The form of a label may change in physical distribution process,

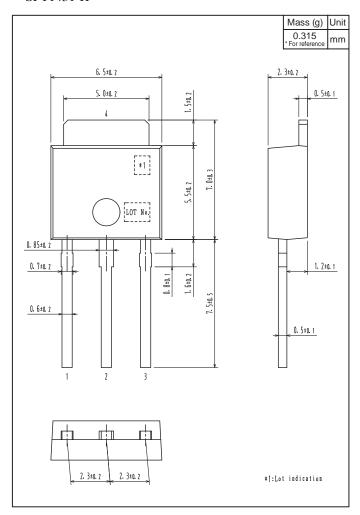


Label			JEITA Phase
LEAD F	REE	3	JEITA Phase 3A
LEAD F	REE	4	JEITA Phase 3



Outline Drawing

SFT1431-H



Note on usage: Since the SFT1431 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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