



SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

FW216A — General-Purpose Switching Device Applications

Features

- ON-resistance $N_{ch} : R_{DS(on)} = 49m\Omega$ (typ.)
- 4.0V drive
- Halogen free compliance

Specifications

Absolute Maximum Ratings at $T_a = 25^\circ C$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DS}		35	V
Gate-to-Source Voltage	V_{GS}		± 20	V
Drain Current (DC)	I_D		4.5	A
Drain Current ($PW \leq 10\mu s$)	I_{DP}	Duty cycle $\leq 1\%$	18	A
Allowable Power Dissipation	P_D	When mounted on ceramic substrate ($2000mm^2 \times 0.8mm$) 1unit, $PW \leq 10s$	1.6	W
Total Dissipation	P_T	When mounted on ceramic substrate ($2000mm^2 \times 0.8mm$), $PW \leq 10s$	2.2	W
Channel Temperature	T_{ch}		150	$^\circ C$
Storage Temperature	T_{stg}		-55 to +150	$^\circ C$

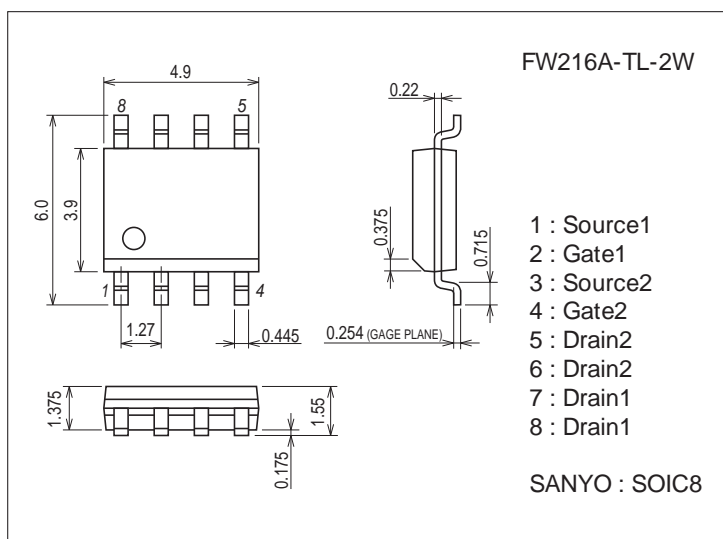
This product is designed to "ESD immunity < 200V**", so please take care when handling.

* Machine Model

Package Dimensions

unit : mm (typ)

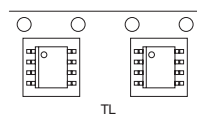
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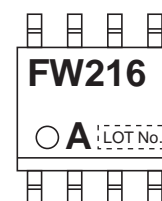
Product & Package Information

- Package : SOIC8
- JEITA, JEDEC : SC-87, SOT-96
- Minimum Packing Quantity : 2,500 pcs./reel

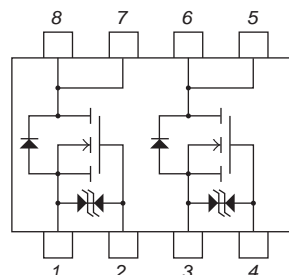
Packing Type : TL



Marking



Electrical Connection



SANYO Semiconductor Co., Ltd.

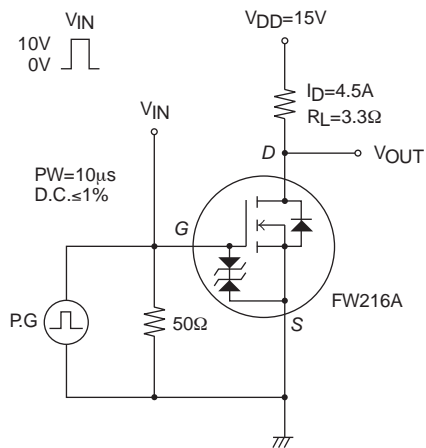
<http://semicon.sanyo.com/en/network>

FW216A

Electrical Characteristics at Ta=25°C

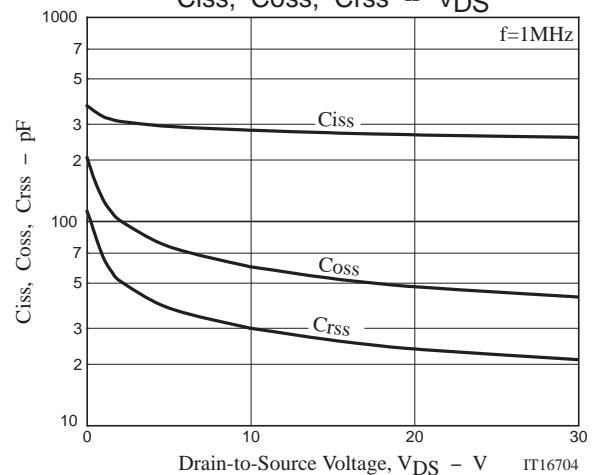
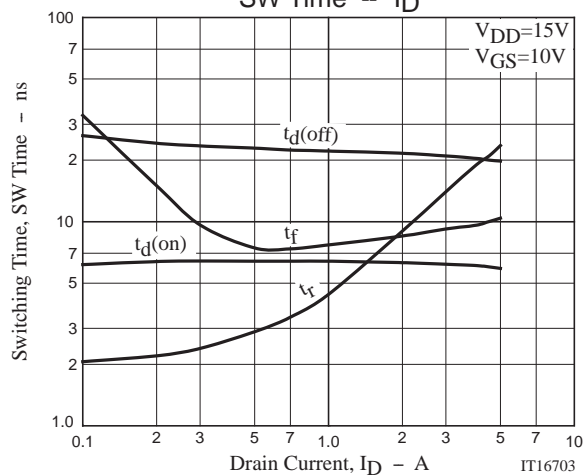
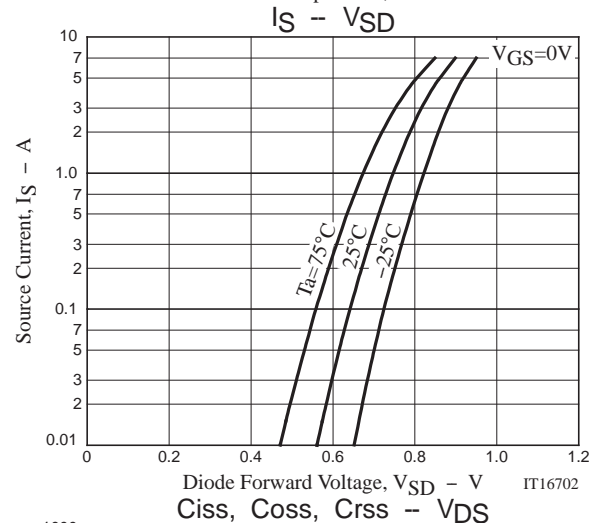
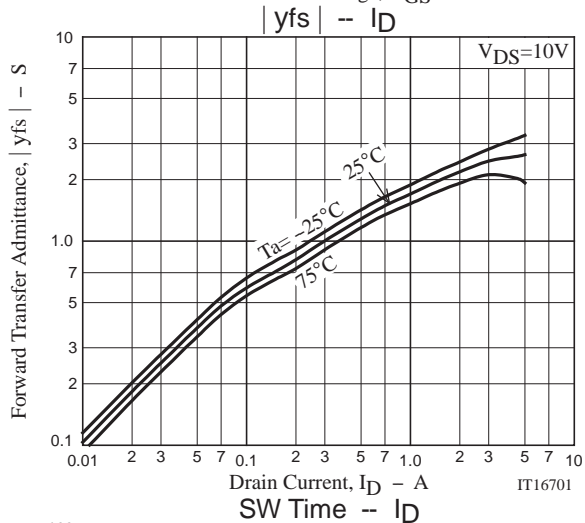
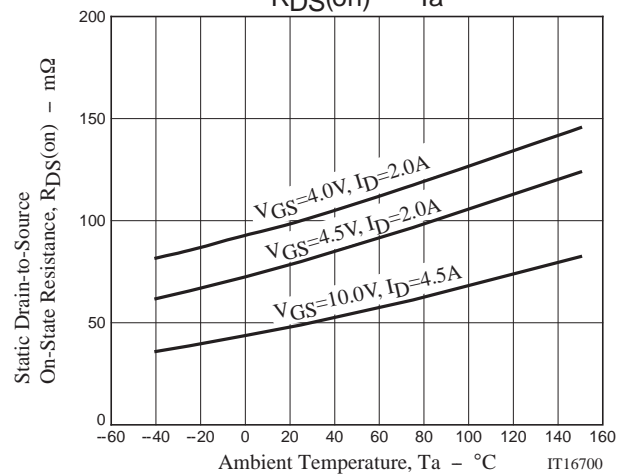
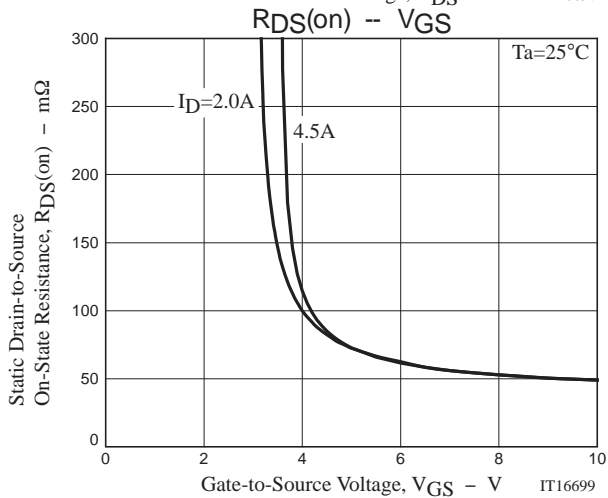
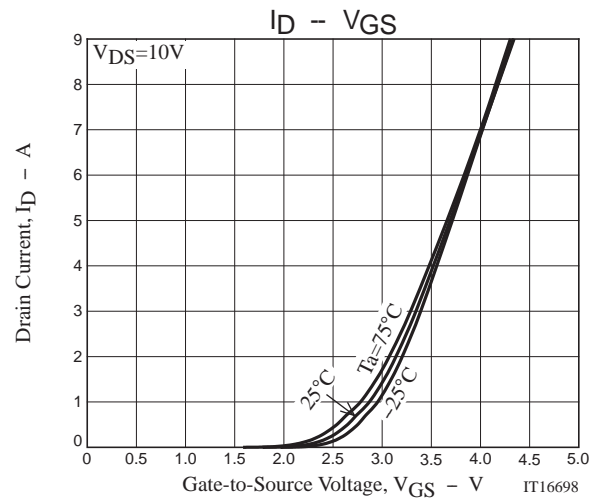
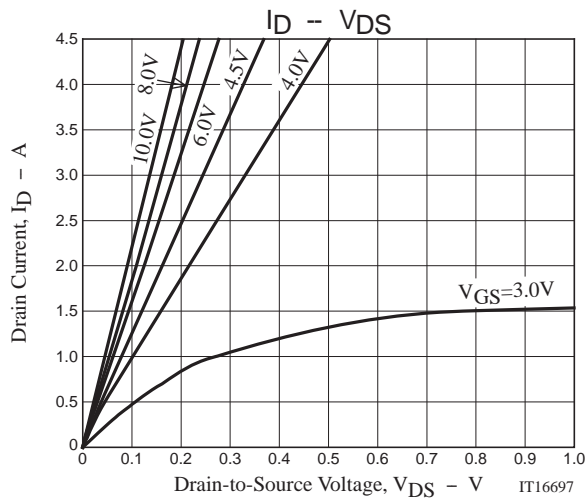
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=1mA, V_{GS}=0V$	35			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=35V, V_{GS}=0V$			1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 16V, V_{DS}=0V$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10V, I_D=1mA$	1.5		2.5	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10V, I_D=4.5A$		2.6		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=4.5A, V_{GS}=10V$		49	64	$m\Omega$
	$R_{DS(on)2}$	$I_D=2A, V_{GS}=4.5V$		80	112	$m\Omega$
	$R_{DS(on)3}$	$I_D=2A, V_{GS}=4.0V$		100	140	$m\Omega$
Input Capacitance	C_{iss}	$V_{DS}=10V, f=1MHz$		280		pF
Output Capacitance	C_{oss}			60		pF
Reverse Transfer Capacitance	C_{rss}			30		pF
Turn-ON Delay Time	$t_d(on)$			6		ns
Rise Time	t_r	See specified Test Circuit.		21		ns
Turn-OFF Delay Time	$t_d(off)$			20		ns
Fall Time	t_f			10		ns
Total Gate Charge	Q_g	$V_{DS}=10V, V_{GS}=10V, I_D=4.5A$		5.6		nC
Gate-to-Source Charge	Q_{gs}			1.2		nC
Gate-to-Drain "Miller" Charge	Q_{gd}			0.8		nC
Diode Forward Voltage	V_{SD}	$I_S=4.5A, V_{GS}=0V$		0.85	1.2	V

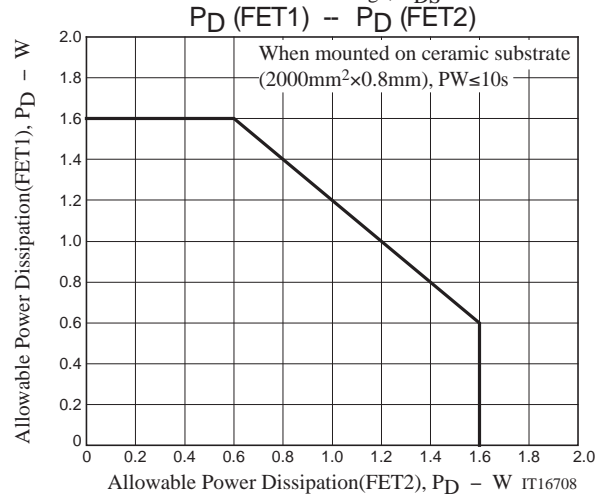
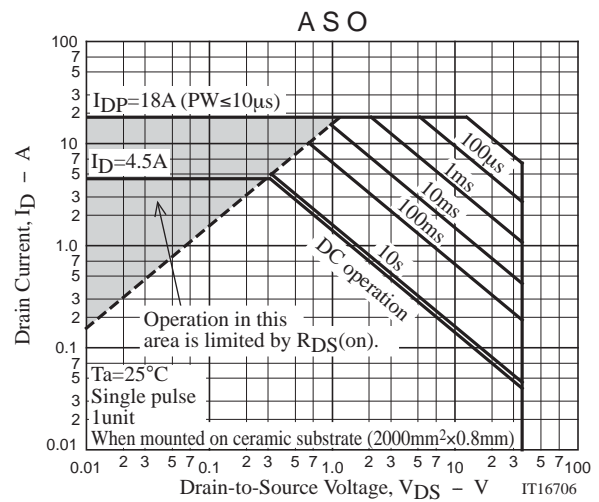
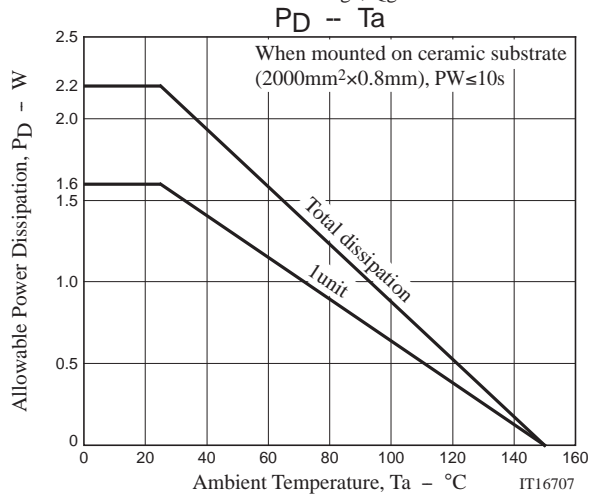
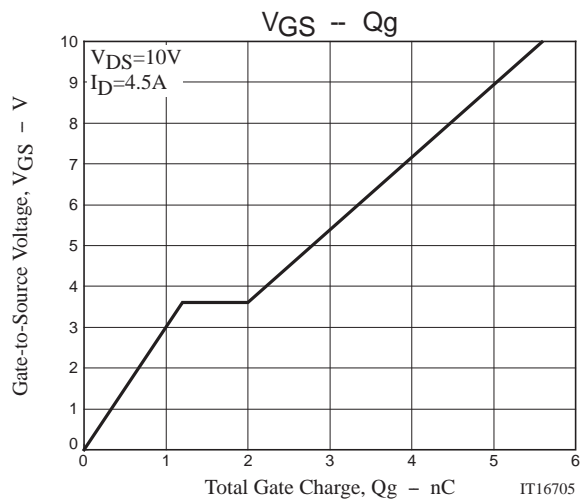
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
FW216A-TL-2W	SOIC8	2,500pcs./reel	Pb Free and Halogen Free





Taping Specification

FW216A-TL-2W

1. Packing Format

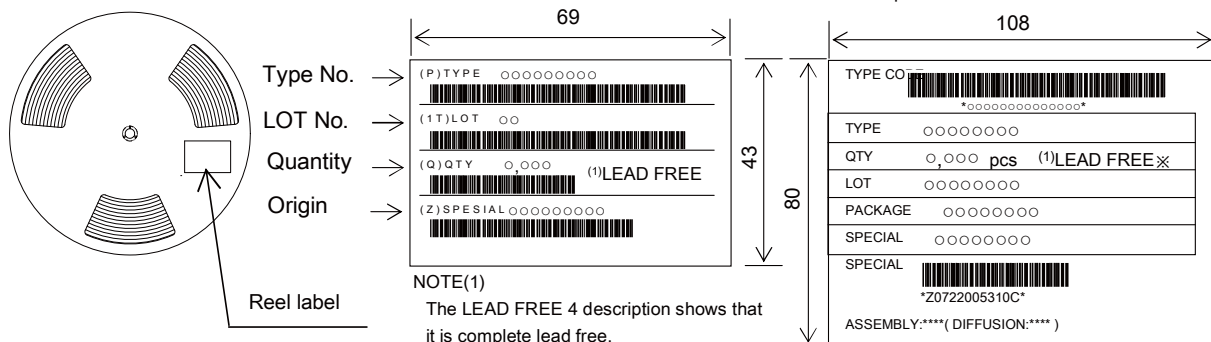
Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX W206-112	Outer BOX W207-124
SOIC8	B202-101	2,500	12,500	25,000	5 reels contained Dimensions :mm(external) 340×95×340	2 inner boxes contained Dimensions :mm(external) 360×210×375

Packing method

Reel label, Inner box label (unit: mm)

Outer box label

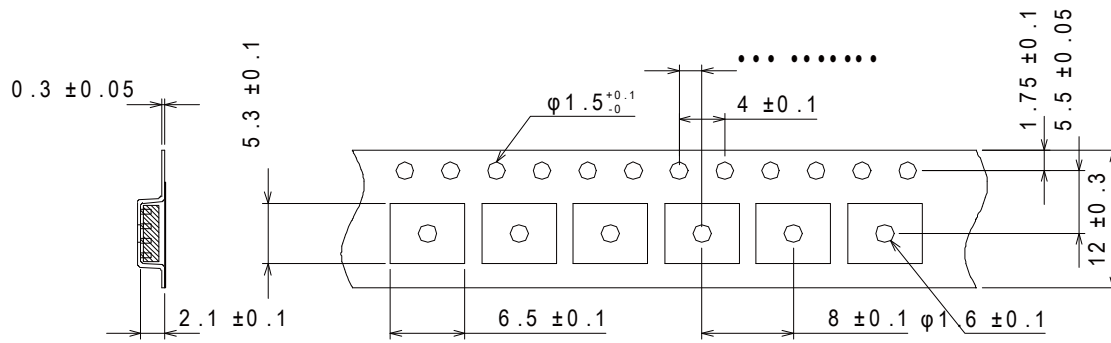
It is a label at the time of factory shipments.
The form of a label may change in physical distribution process.



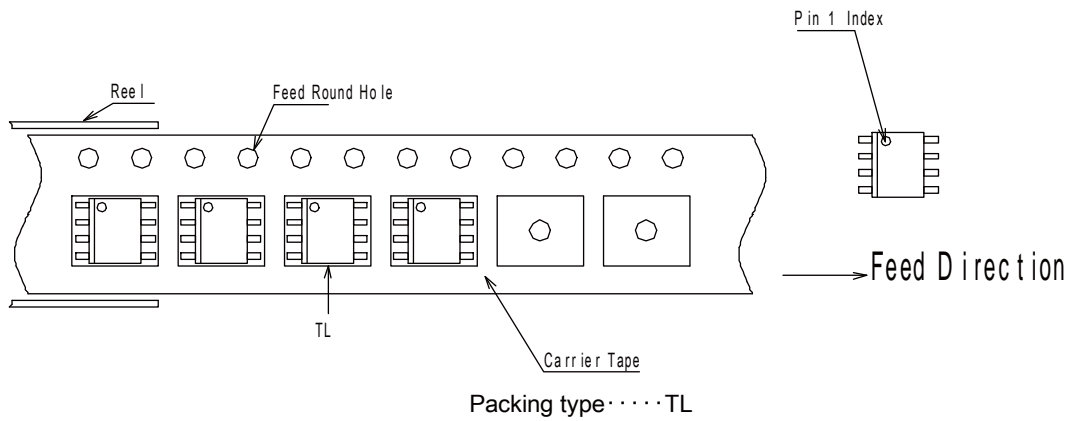
Label	JEITA Phase
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

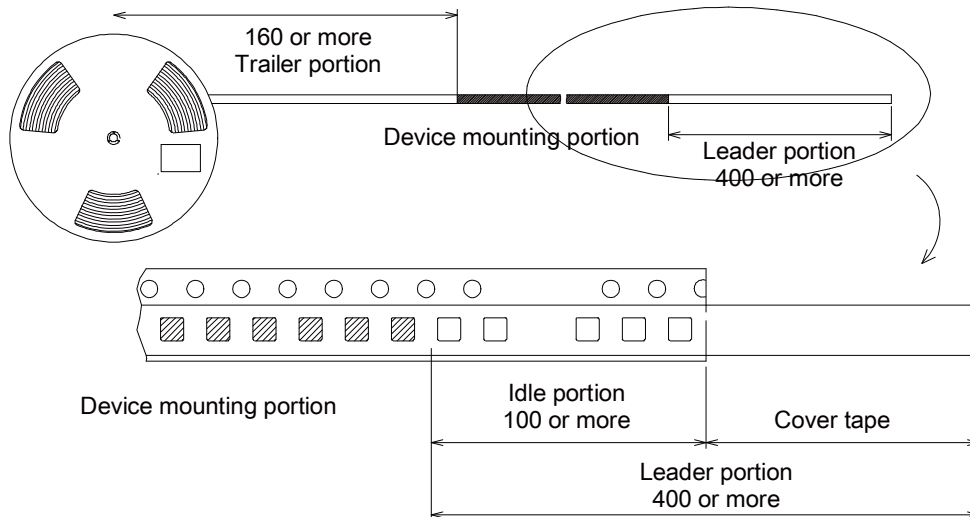
2-1. Carrier tape size (unit: mm)



2-2. Device placement direction



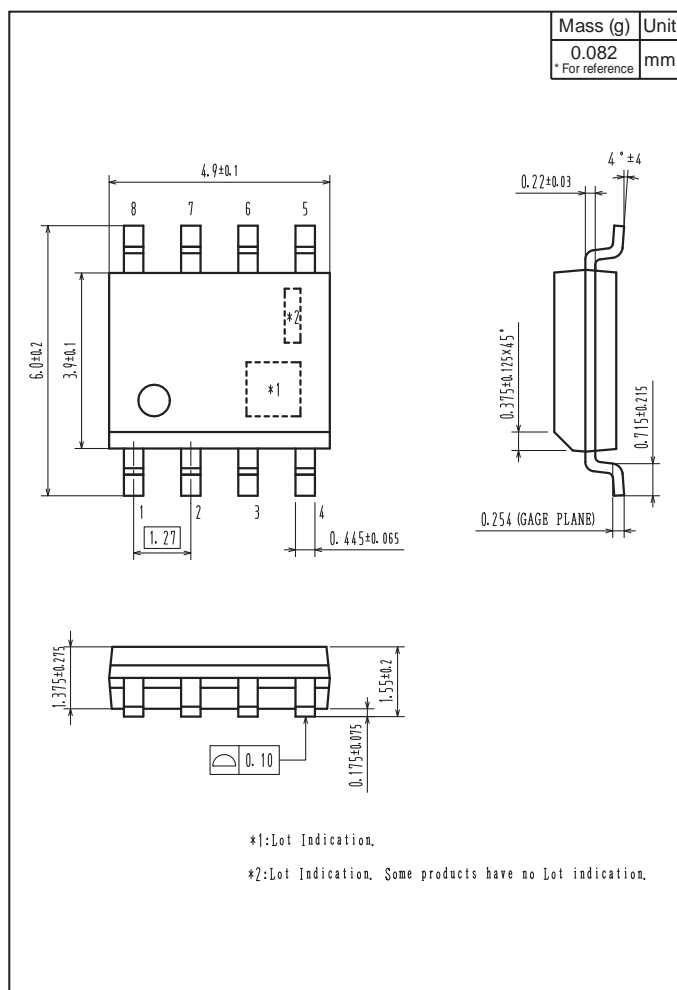
2-3. Leader portion and trailer portion (unit: mm)



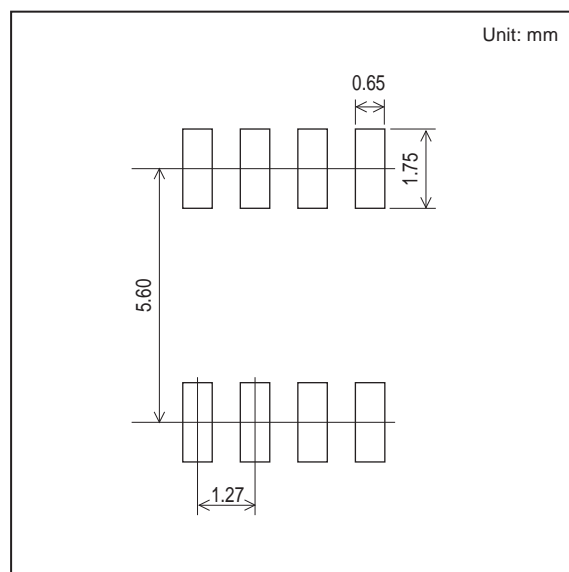
FW216A

Outline Drawing

FW216A-TL-2W



Land Pattern Example



Note on usage : Since the FW216A is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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