Ordering number : ENA1185A

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

ECH8664R — General-Purpose Switching Device Applications

Features

- Low ON-resistance
- 2.5V drive
- Common-drain type
- Protection diode in
- Built-in gate protection resistor
- Best suited for LiB charging and discharging switch
- Halogen free compliance

Specifications

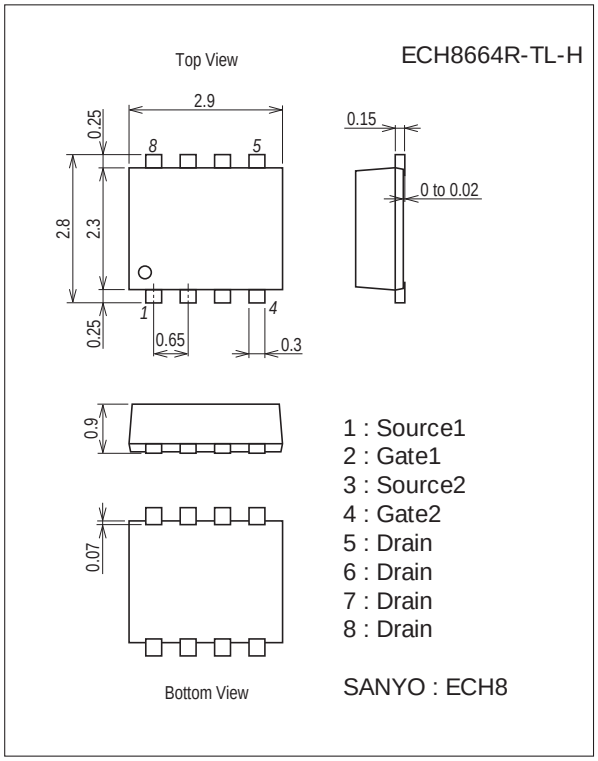
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		30	V
Gate-to-Source Voltage	V_{GSS}		±12	V
Drain Current (DC)	I_D		7	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	60	A
Allowable Power Dissipation	P_D	When mounted on ceramic substrate (900mm²×0.8mm) 1unit	1.3	W
Total Power Dissipation	P_T	When mounted on ceramic substrate (900mm²×0.8mm)	1.4	W
Channel Temperature	Tch		150	℃
Storage Temperature	Tstg		−55 to +150	℃

Package Dimensions

unit : mm (typ)

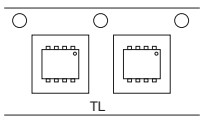
7011A-003



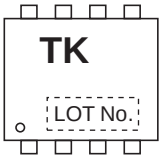
Product & Package Information

- Package : ECH8
- JEITA, JEDEC : -
- Minimum Packing Quantity : 3,000 pcs./reel

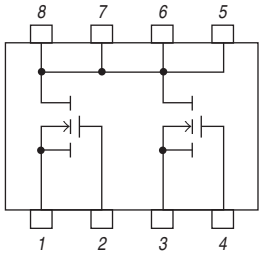
Packing Type : TL



Marking



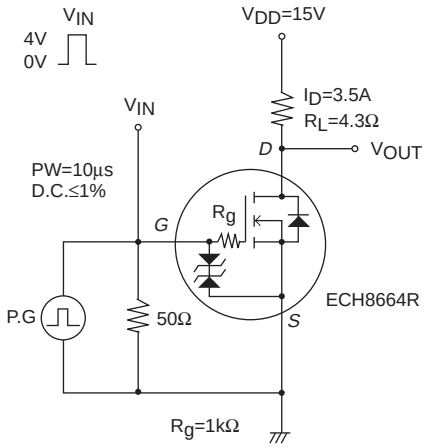
Electrical Connection



Electrical Characteristics at Ta=25°C

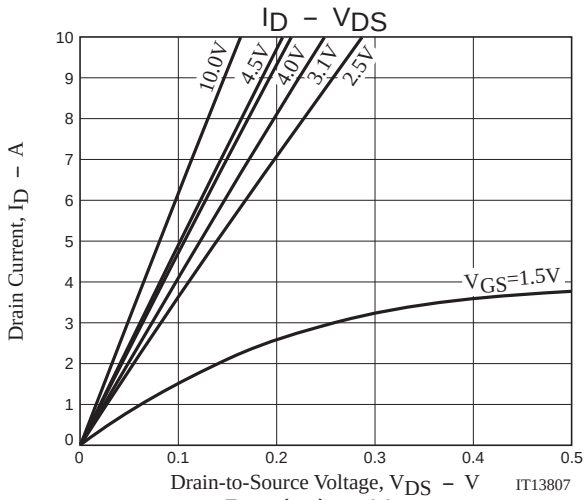
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=1mA, V_{GS}=0V	30			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=30V, V_{GS}=0V			1	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±8V, V_{DS}=0V			±10	μA
Cutoff Voltage	V_{GS}(off)	V_{DS}=10V, I_D=1mA	0.5		1.3	V
Forward Transfer Admittance	| yfs |	V_{DS}=10V, I_D=3.5A	4.5	7.5		S
Static Drain-to-Source On-State Resistance	R_{DS}(on)1	I_D=3.5A, V_{GS}=4.5V	12.5	18	23.5	mΩ
	R_{DS}(on)2	I_D=3.5A, V_{GS}=4.0V	13	19	25	mΩ
	R_{DS}(on)3	I_D=2A, V_{GS}=3.1V	14.5	21	27.3	mΩ
	R_{DS}(on)4	I_D=2A, V_{GS}=2.5V	14.5	24	34	mΩ
Turn-ON Delay Time	t_d(on)	See specified Test Circuit.		270		ns
Rise Time	t_r			850		ns
Turn-OFF Delay Time	t_d(off)			3300		ns
Fall Time	t_f			1700		ns
Total Gate Charge	Qg	V_{DS}=10V, V_{GS}=4.5V, I_D=7A		10		nC
Gate-to-Source Charge	Qgs			2.1		nC
Gate-to-Drain “Miller” Charge	Qgd			2.0		nC
Diode Forward Voltage	V_{SD}	I_S=7A, V_{GS}=0V		0.75	1.2	V

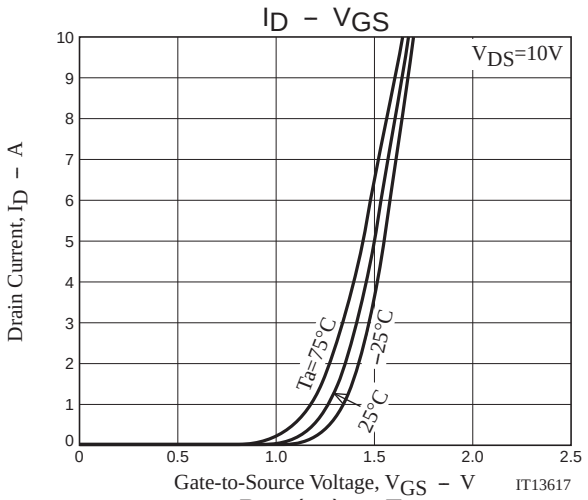
Switching Time Test Circuit

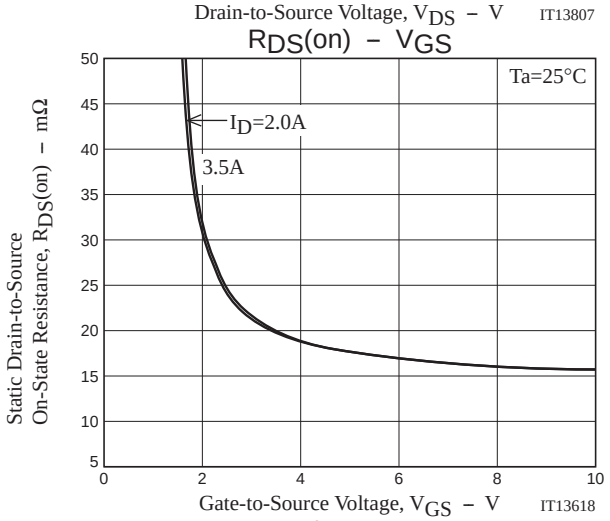


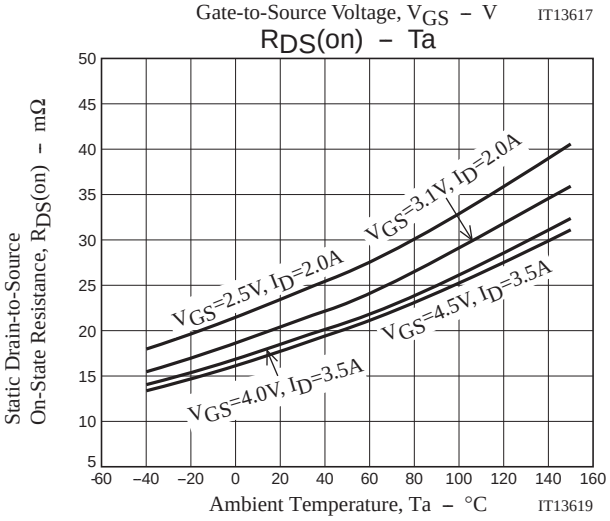
Ordering Information

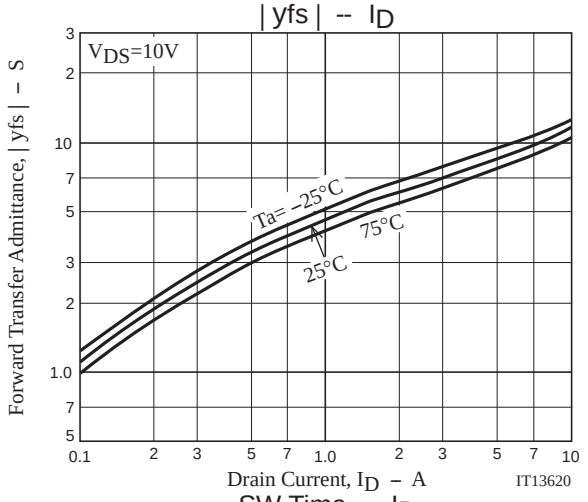
Device	Package	Shipping	memo
ECH8664R-TL-H	ECH8	3,000pcs./reel	Pb Free and Halogen Free

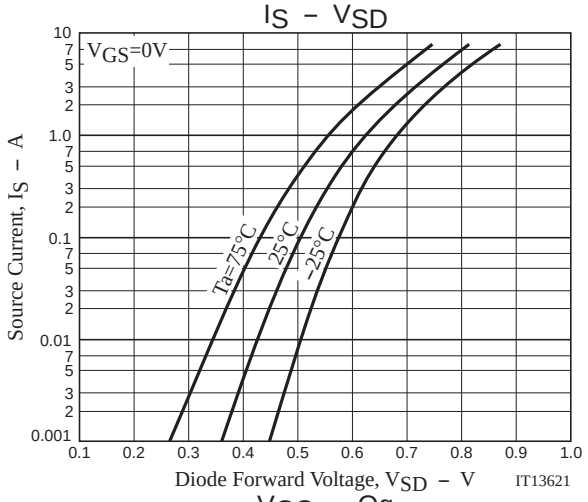

ID – VDS
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
10.0V
4.5V
4.0V
3.1V
2.5V
VGS=1.5V
IT13807

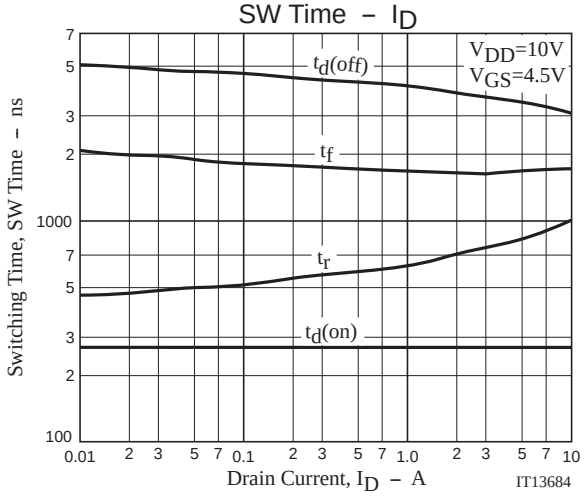

ID – VGS
Drain Current, ID – A
Gate-to-Source Voltage, VGS – V
VDS=10V
Ta=75°C
25°C
–25°C
IT13617

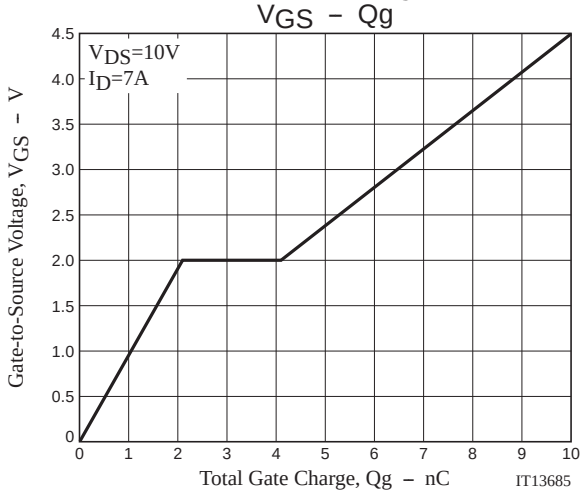

RDS(on) – VGS
Static Drain-to-Source On-State Resistance, RDS(on) – mΩ
Gate-to-Source Voltage, VGS – V
Ta=25°C
ID=2.0A
3.5A
IT13618

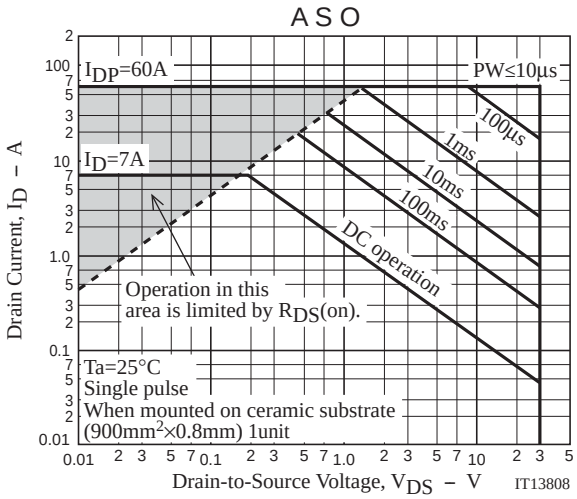

RDS(on) – Ta
Static Drain-to-Source On-State Resistance, RDS(on) – mΩ
Ambient Temperature, Ta – °C
VGS=3.1V, ID=2.0A
VGS=2.5V, ID=2.0A
VGS=4.5V, ID=3.5A
VGS=4.0V, ID=3.5A
IT13619

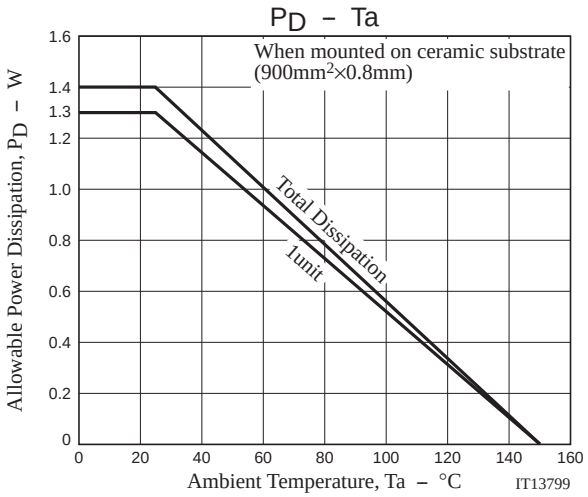

| yfs | -- ID
Forward Transfer Admittance, | yfs | – S
Drain Current, ID – A
VDS=10V
Ta= –25°C
75°C
25°C
IT13620


IS – VSD
Source Current, IS – A
Diode Forward Voltage, VSD – V
VGS=0V
Ta=75°C
25°C
–25°C
IT13621


SW Time – ID
Switching Time, SW Time – ns
Drain Current, ID – A
VDD=10V
VGS=4.5V
td(off)
tf
tr
td(on)
IT13684


VGS – Qg
Gate-to-Source Voltage, VGS – V
Total Gate Charge, Qg – nC
VDS=10V
ID=7A
IT13685


A S O
I_DP=60A
PW≤10μs
100μs
1ms
10ms
100ms
DC operation
I_D=7A
Operation in this
area is limited by R_DS(on).
Ta=25°C
Single pulse
When mounted on ceramic substrate
(900mm²×0.8mm) 1unit
Drain Current, I_D – A
Drain-to-Source Voltage, V_DS – V
IT13808


P_D – Ta
When mounted on ceramic substrate
(900mm²×0.8mm)
Total Dissipation
1unit
Allowable Power Dissipation, P_D – W
Ambient Temperature, Ta – °C
IT13799

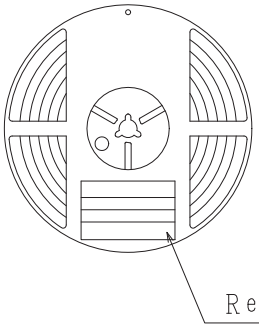
Embossed Taping Specification

ECH8664R-TL-H

1. Packing Format

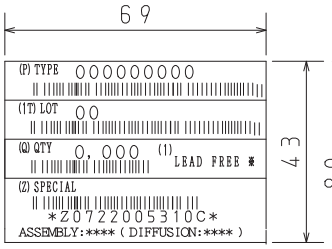
Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
ECH8	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm(external) 183×72×185	6 inner boxes contained Dimensions:mm(external) 440×195×210

Packing method



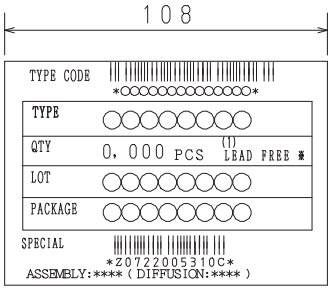
Reel label, Inner box label

(unit:mm)



Outer box label

It is a label at the time of factory shipments. The form of a label may change in physical distribution process.



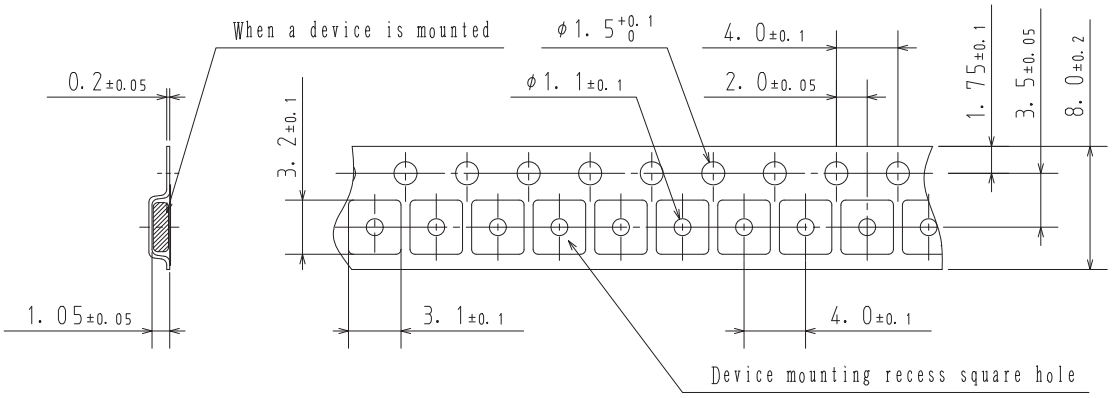
NOTE (1)

The LEAD FREE ※ description shows that the surface treatment of the terminal is lead free.

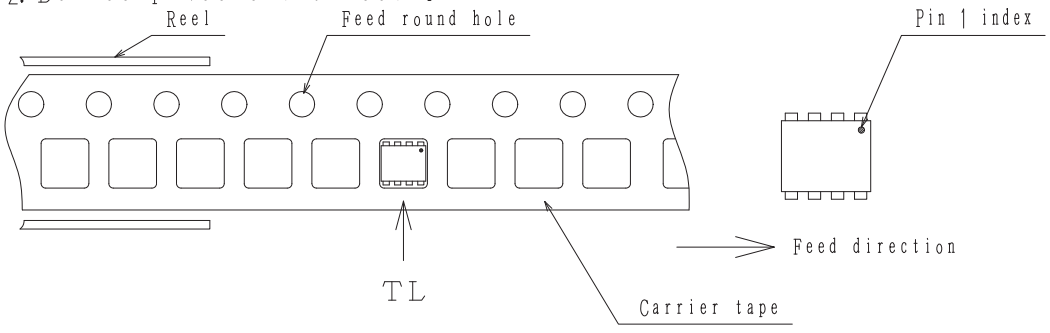
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



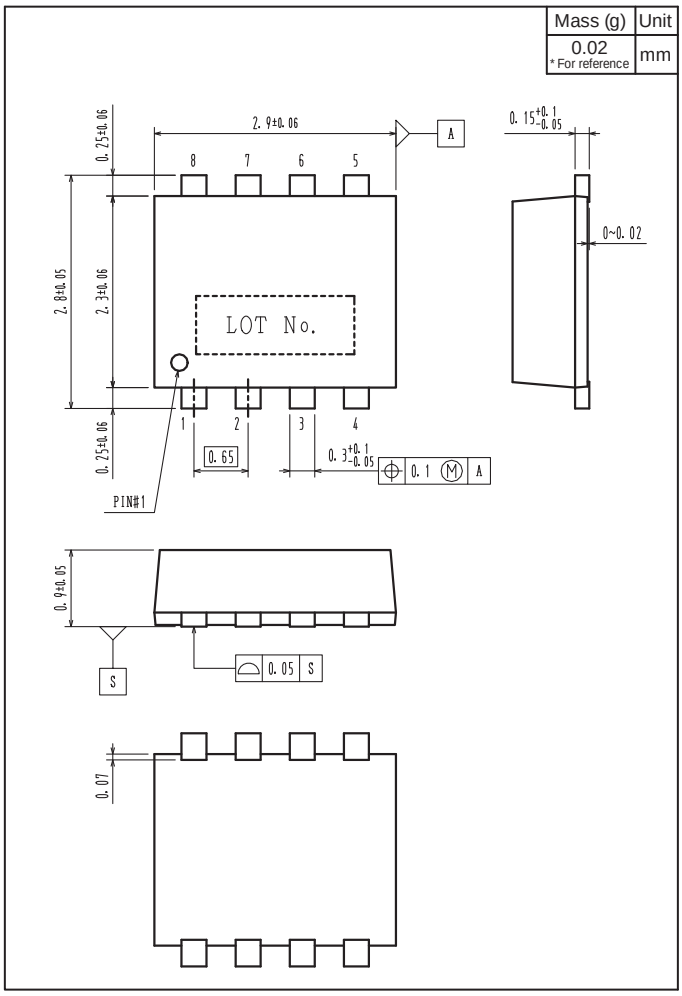
2-2. Device placement direction



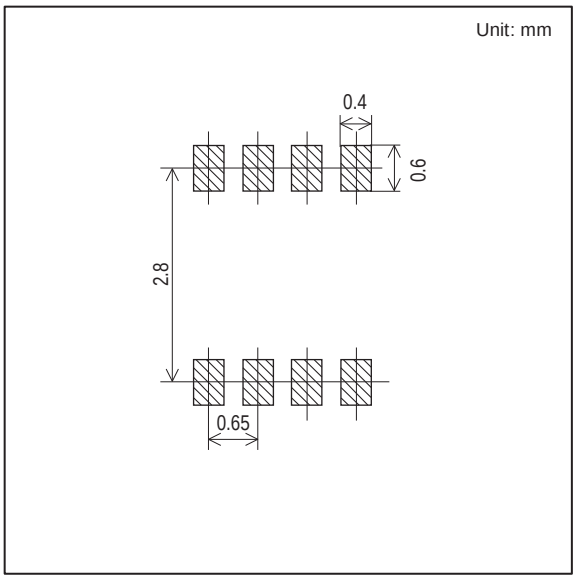
Those with pin 1 index on the feed hole side·····TL

Outline Drawing

ECH8664R-TL-H



Land Pattern Example



Note on usage : Since the ECH8664R is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

This catalog provides information as of June, 2012. Specifications and information herein are subject to change without notice.