



SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

P-Channel Silicon MOSFET

CPH6337 — General-Purpose Switching Device Applications

Features

- Ultrahigh-speed switching
- 1.8V drive

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DS}		-12	V
Gate-to-Source Voltage	V_{GS}		±10	V
Drain Current (DC)	I_D		-3.5	A
Drain Current (Pulse)	I_{DP}	$PW \leq 10\mu s$, duty cycle $\leq 1\%$	-14	A
Allowable Power Dissipation	P_D	When mounted on ceramic substrate (1200mm ² ×0.8mm)	1.6	W
Channel Temperature	T_{ch}		150	°C
Storage Temperature	T_{stg}		-55 to +150	°C

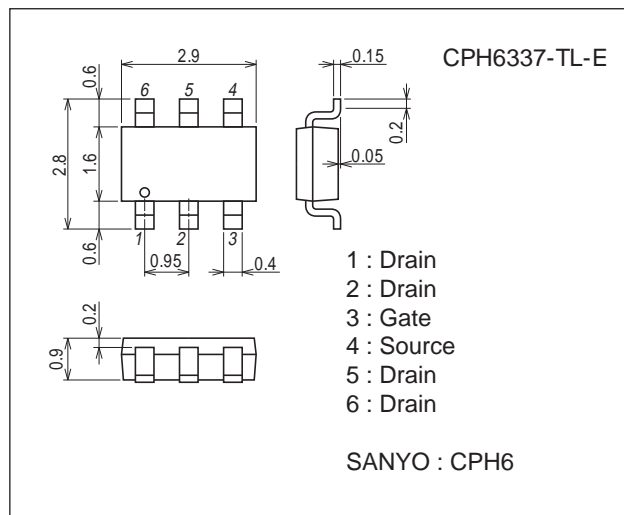
This product is designed to "ESD immunity < 200V**", so please take care when handling.

* Machine Model

Package Dimensions

unit : mm (typ)

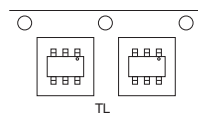
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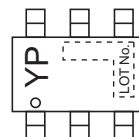
Product & Package Information

- Package : CPH6
- JEITA, JEDEC : SC-74, SOT-26, SOT-457
- Minimum Packing Quantity : 3,000 pcs./reel

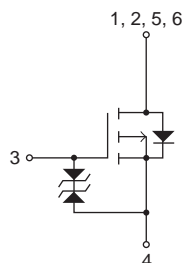
Packing Type: TL



Marking



Electrical Connection

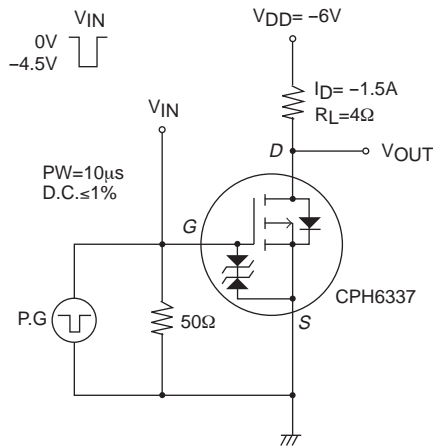


CPH6337

Electrical Characteristics at Ta=25°C

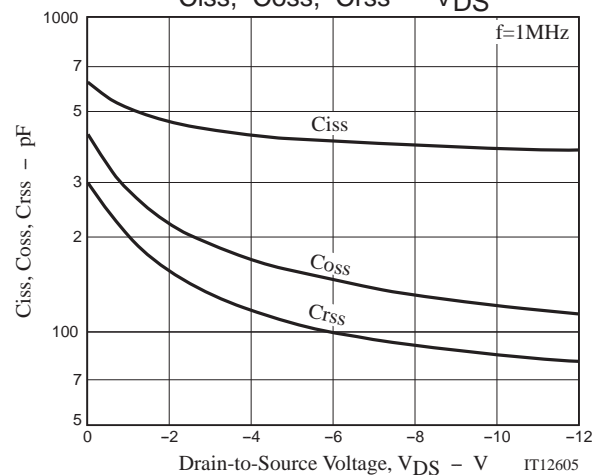
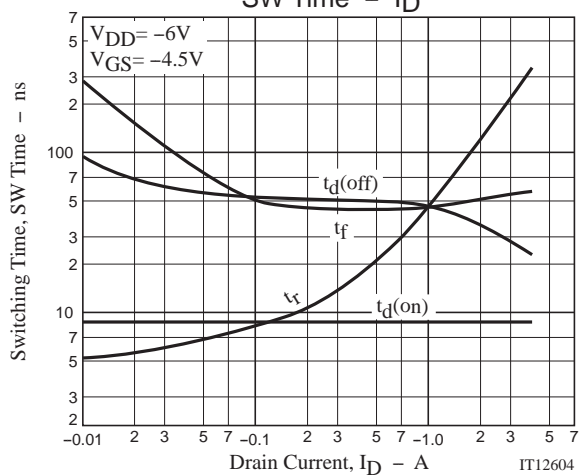
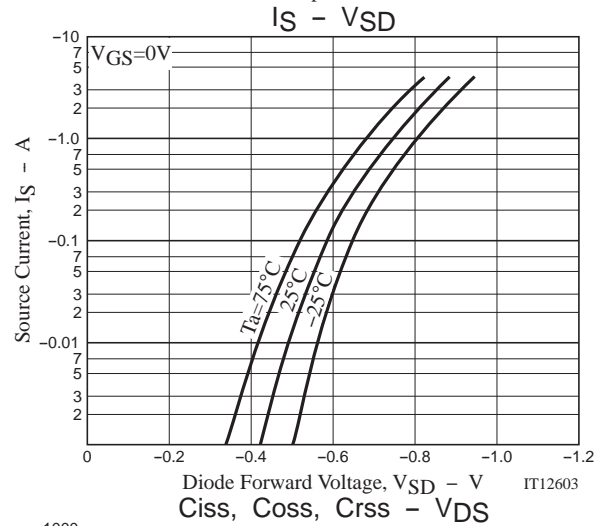
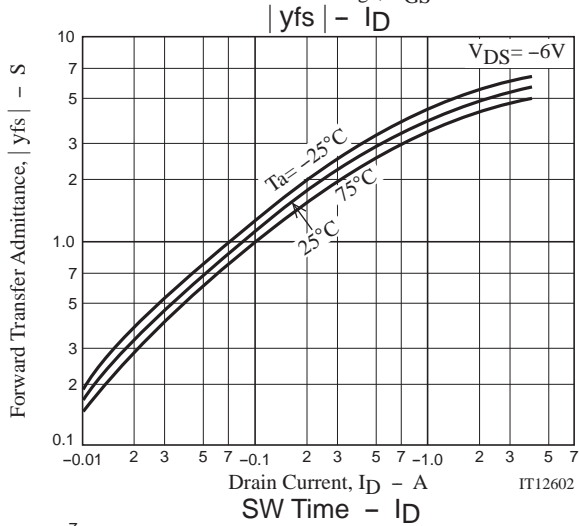
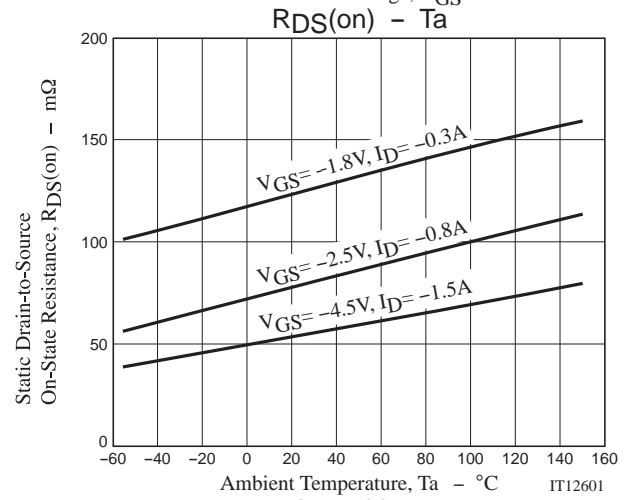
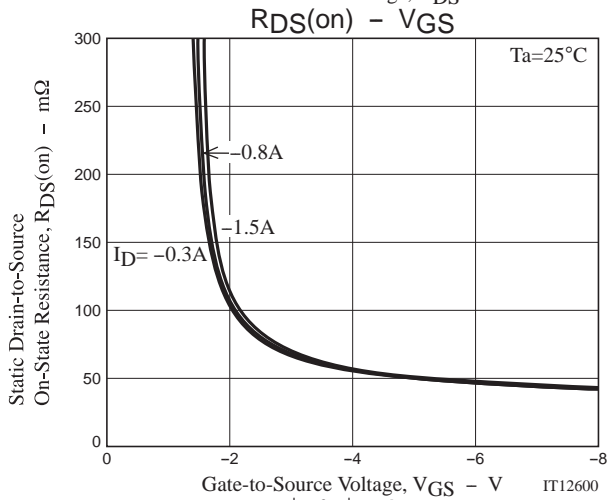
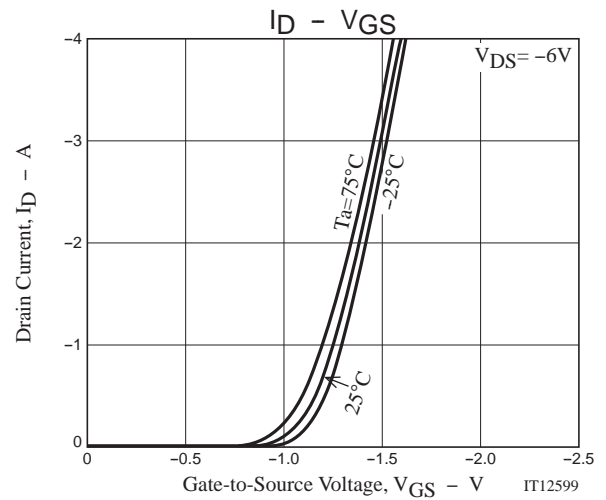
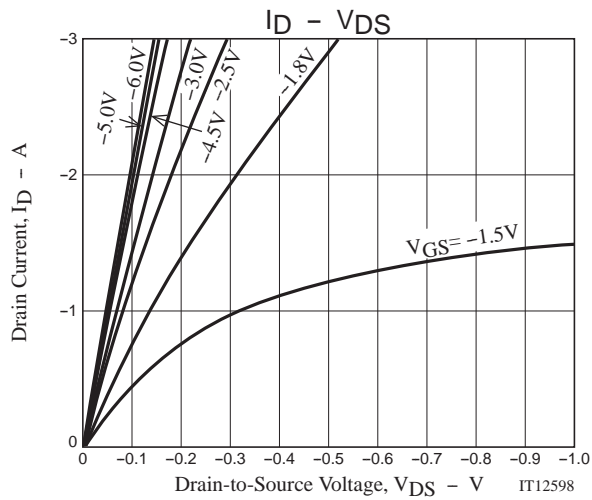
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1mA, V_{GS} = 0V$	-12			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -12V, V_{GS} = 0V$			-10	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS} = \pm 8V, V_{DS} = 0V$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -6V, I_D = -1mA$	-0.4		-1.4	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -6V, I_D = -1.5A$	2.7	4.5		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D = -1.5A, V_{GS} = -4.5V$		54	70	m Ω
	$R_{DS(on)2}$	$I_D = -0.8A, V_{GS} = -2.5V$		80	115	m Ω
	$R_{DS(on)3}$	$I_D = -0.3A, V_{GS} = -1.8V$		125	215	m Ω
Input Capacitance	C_{iss}	$V_{DS} = -6V, f = 1MHz$		405		pF
Output Capacitance	C_{oss}			145		pF
Reverse Transfer Capacitance	C_{rss}			100		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		8.8		ns
Rise Time	t_r			80		ns
Turn-OFF Delay Time	$t_d(off)$			41		ns
Fall Time	t_f			50		ns
Total Gate Charge	Q_g	$V_{DS} = -6V, V_{GS} = -4.5V, I_D = -3.5A$		5.6		nC
Gate-to-Source Charge	Q_{gs}			0.7		nC
Gate-to-Drain "Miller" Charge	Q_{gd}			1.6		nC
Diode Forward Voltage	V_{SD}	$I_S = -3.5A, V_{GS} = 0V$		-0.86	-1.5	V

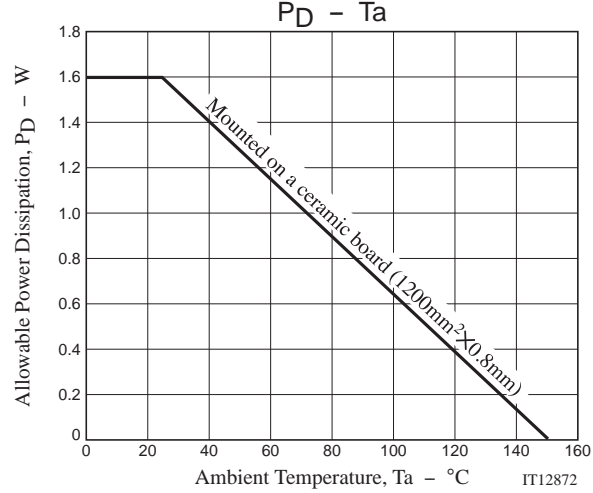
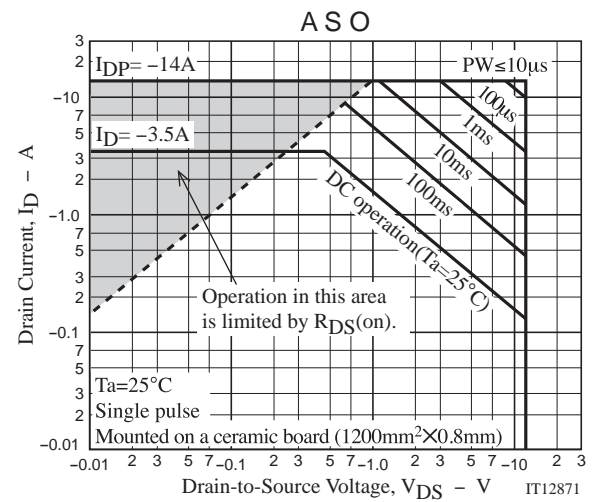
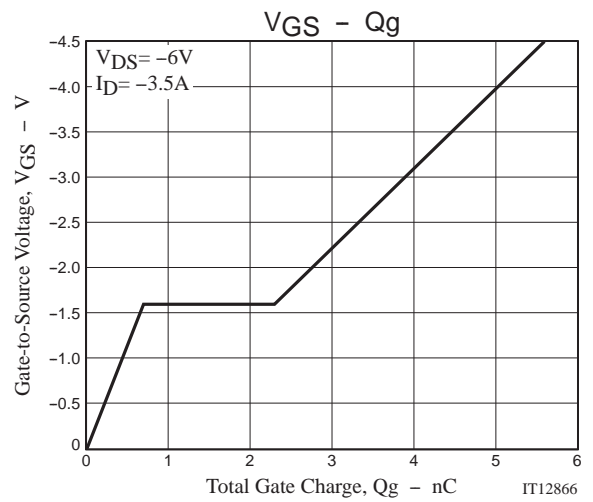
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
CPH6337-TL-E	CPH6	3,000pcs./reel	Pb Free





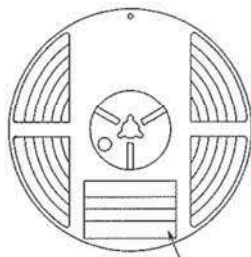
Embossed Taping Specification

CPH6337-TL-E

1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
CPH6	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Packing method



Reel label

Type No.
LOT No.
Quantity
Origin

Reel label, Inner box label
(unit:mm)

69	
TYPE 000000000	
LOT 00	
QTY 0,000 (1) LEAD FREE #	
SPECIAL	
20722005310C	
ASSEMBLY:**** (DIFFUSION:****)	
43	80

Outer box label

It is a label at the time of factory shipments.
The form of a label may change in physical
distribution process.

108	
TYPE CODE	
TYPE	00000000
QTY	0,000 PCS (1) LEAD FREE #
LOT	00000000
PACKAGE	00000000
SPECIAL	
20722005310C	
ASSEMBLY:**** (DIFFUSION:****)	

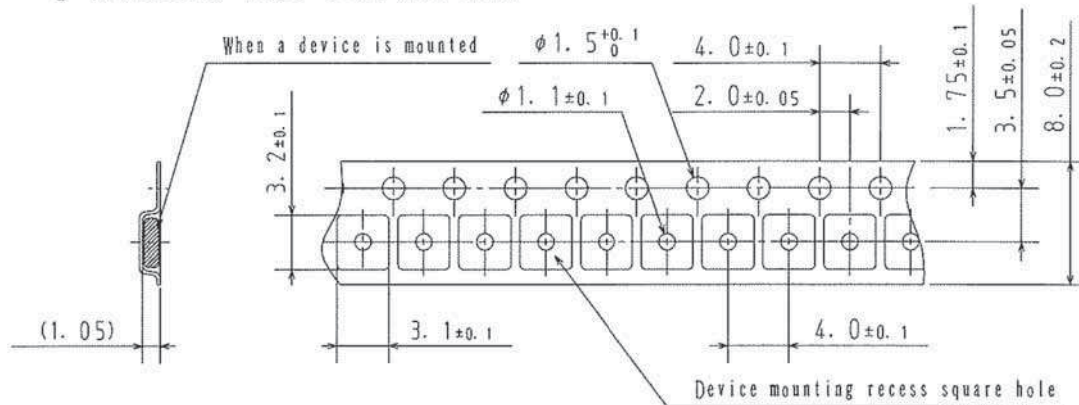
NOTE (1)

The LEAD FREE # description shows that the surface
treatment of the terminal is lead free.

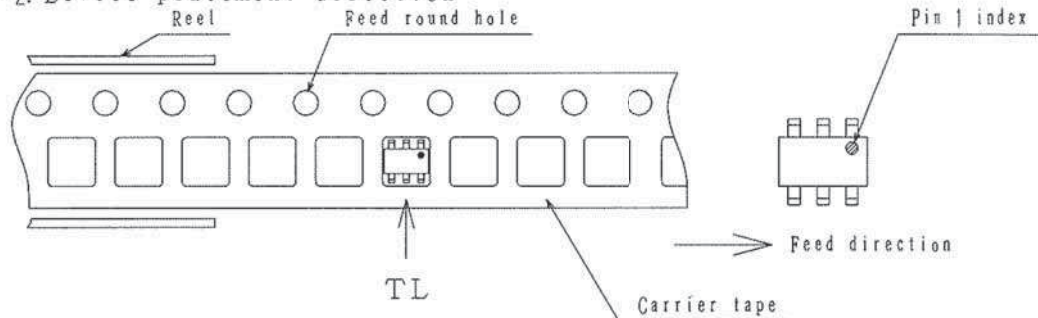
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)

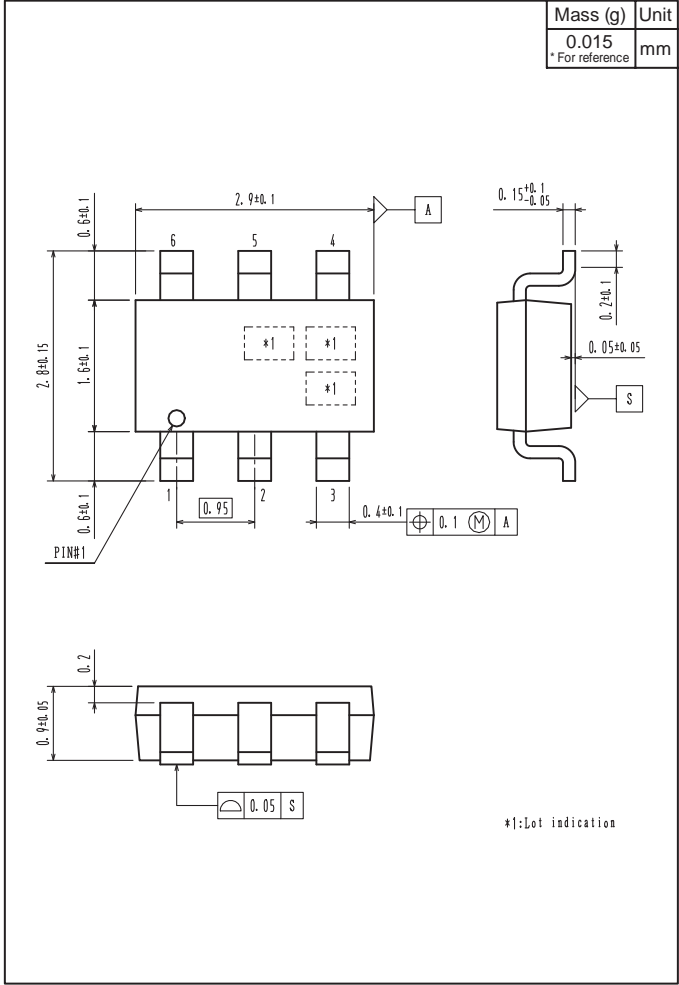


2-2. Device placement direction

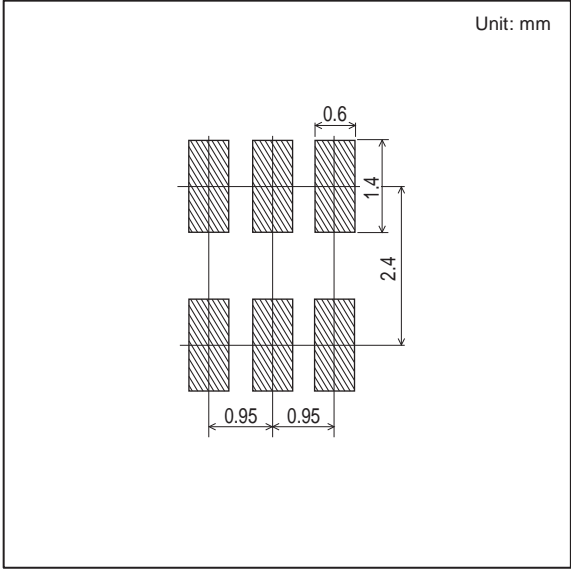


Those with pin 1 index on the feed hole side.....TL

Outline Drawing
CPH6337-TL-E



Land Pattern Example



Note on usage : Since the CPH6337 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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