

SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

CPH3457 — General-Purpose Switching Device Applications

Features

- ON-resistance RDS(on)1=73m Ω (typ.)
- · 1.8V drive
- · Halogen free compliance
- · Protection diode in

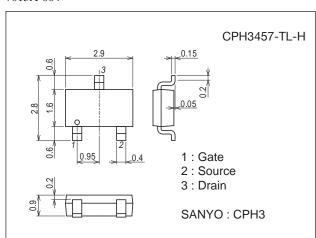
Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		30	V
Gate-to-Source Voltage	VGSS		±12	V
Drain Current (DC)	ID		3	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	12	Α
Allowable Power Dissipation	PD	When mounted on ceramic substrate (900mm ² ×0.8mm)	1.0	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Package Dimensions

unit : mm (typ) 7015A-004



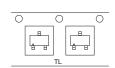
Product & Package Information

• Package : CPH3

• JEITA, JEDEC : SC-59, TO-236, SOT-23

• Minimum Packing Quantity : 3,000 pcs./reel

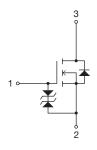
Packing Type: TL



Marking



Electrical Connection

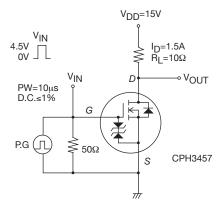


CPH3457

Electrical Characteristics at Ta=25°C

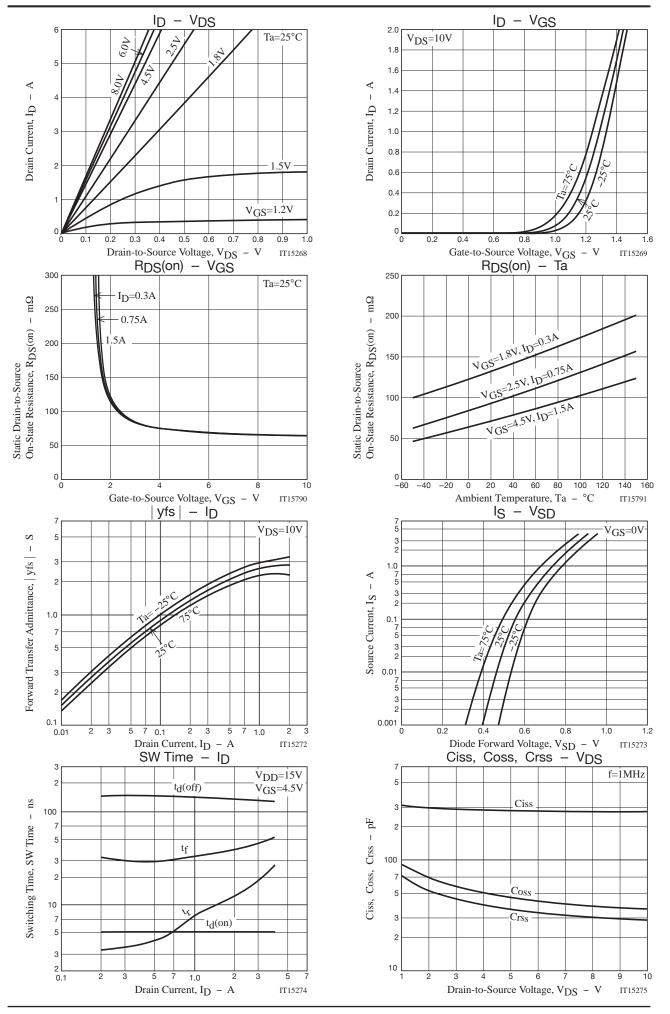
Parameter	Symbol	Conditions	Ratings			Unit	
Farameter	Syllibol	Conditions	min	typ	max	Onit	
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	30			V	
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0V			1	μΑ	
Gate-to-Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0V			±10	μΑ	
Cutoff Voltage	V _{GS} (off)	V _{DS} =10V, I _D =1mA	0.4		1.3	V	
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =1.5A		2.7		S	
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =1.5A, V _G S=4.5V		73	95	mΩ	
	R _{DS} (on)2	I _D =0.75A, V _{GS} =2.5V		95	133	mΩ	
	R _{DS} (on)2	I _D =0.3A, V _{GS} =1.8V		135	203	mΩ	
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		265		рF	
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		35		рF	
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz		28		рF	
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		5.1		ns	
Rise Time	t _r	See specified Test Circuit.		10		ns	
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.				ns	
Fall Time	tf	See specified Test Circuit.		36		ns	
Total Gate Charge	Qg	V _{DS} =15V, V _{GS} =4.5V, I _D =3A		3.5		nC	
Gate-to-Source Charge	Qgs	V _{DS} =15V, V _{GS} =4.5V, I _D =3A		0.57		nC	
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =15V, V _{GS} =4.5V, I _D =3A		0.93		nC	
Diode Forward Voltage	V _{SD}	I _S =3A, V _{GS} =0V		0.87	1.2	V	

Switching Time Test Circuit

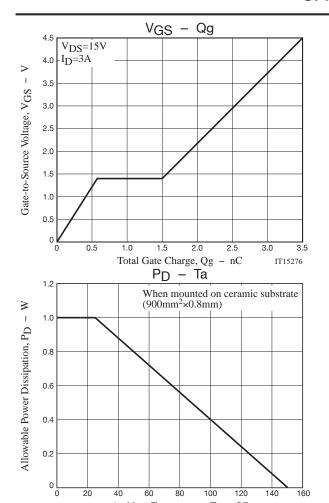


Ordering Information

Device	Device Package		memo	
CPH3457-TL-H	CPH3	3,000pcs./reel	Pb Free and Halogen Free	

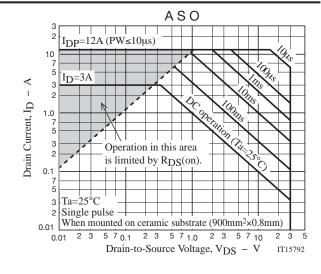


CPH3457



Ambient Temperature, Ta - °C

IT15789

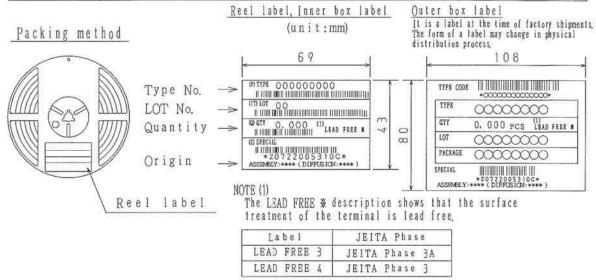


Embossed Taping Specification

CPH3457-TL-H

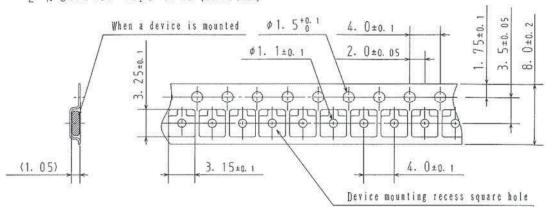
1. Packing Format

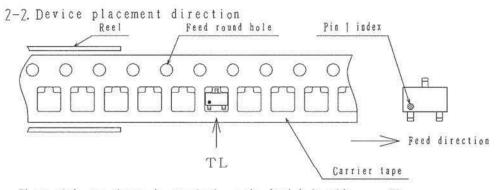
Package Name Carrier Tape Type	Carrier Tape	Maximum Number of devices contained (pcs)			Packing format		
	Type	Reel	loner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)	
СРНЗ	СРНЗ	3, 000	15, 000	90, 000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210	



2. Taping configuration

2-1. Carrier tape size (unit:mm)

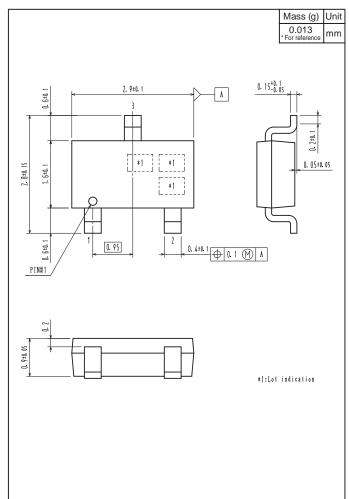




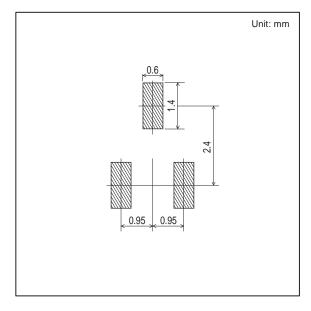
Those with one electrode terminal on the feed hole side·····TL

Outline Drawing

СРН3457-ТL-Н



Land Pattern Example



Note on usage: Since the CPH3457 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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