

SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

2SK3746 — High-Voltage, High-Speed Switching Applications

Features

- · Low ON-resistance, low input capacitance, ultrahigh-speed switching
- · High reliability (Adoption of HVP process)
- · Avalanche resistance guarantee

Specifications

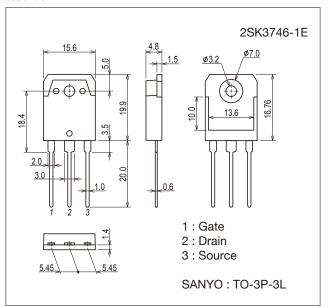
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		1500	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		2	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	4	Α
All II B B' ' '			2.5	W
Allowable Power Dissipation	PD	Tc=25°C	110	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *1	EAS		41	mJ
Avalanche Current *2	lav		2	Α

^{*1} VDD=50V, L=20mH, IAV=2A (Fig.1)

Package Dimensions

unit : mm (typ) 7539-002

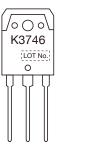


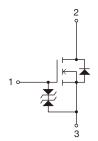
Product & Package Information

• Package : TO-3P-3L

• JEITA, JEDEC: SC-65, TO-247, SOT-199
• Minimum Packing Quantity: 30 pcs./magazine

Marking





Electrical Connection

^{*2} L≤20mH, single pulse

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
Farameter	Symbol	Conditions	min	typ	max	Offit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	1500			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =1200V, V _{GS} =0V			100	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =16V, V _{DS} =0V			±10	μΑ
Cutoff Voltage	V _{GS} (off)	V _{DS} =10V, I _D =1mA	2.5		3.5	V
Forward Transfer Admittance	yfs	V _{DS} =20V, I _D =1A	0.7	1.4		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)	I _D =1A, V _{GS} =10V		10	13	Ω
Input Capacitance	Ciss			380		pF
Output Capacitance	Coss	V _{DS} =30V, f=1MHz		70		рF
Reverse Transfer Capacitance	Crss			40		рF
Turn-ON Delay Time	t _d (on)			12		ns
Rise Time	t _r	See Fig.2		37		ns
Turn-OFF Delay Time	t _d (off)			152		ns
Fall Time	tf			59		ns
Total Gate Charge	Qg			37.5		nC
Gate-to-Source Charge	Qgs	V_{DS} =200V, V_{GS} =10V, I_{D} =2A		2.7		nC
Gate-to-Drain "Miller" Charge	Qgd			20		nC
Diode Forward Voltage	V _{SD}	I _S =2A, V _{GS} =0V		0.88	1.2	V

Fig.1 Avalanche Resistance Test Circuit

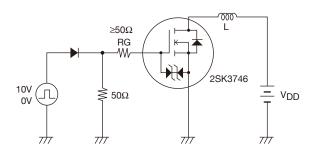
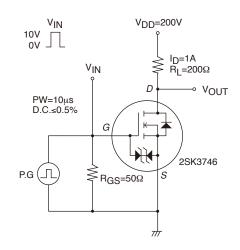
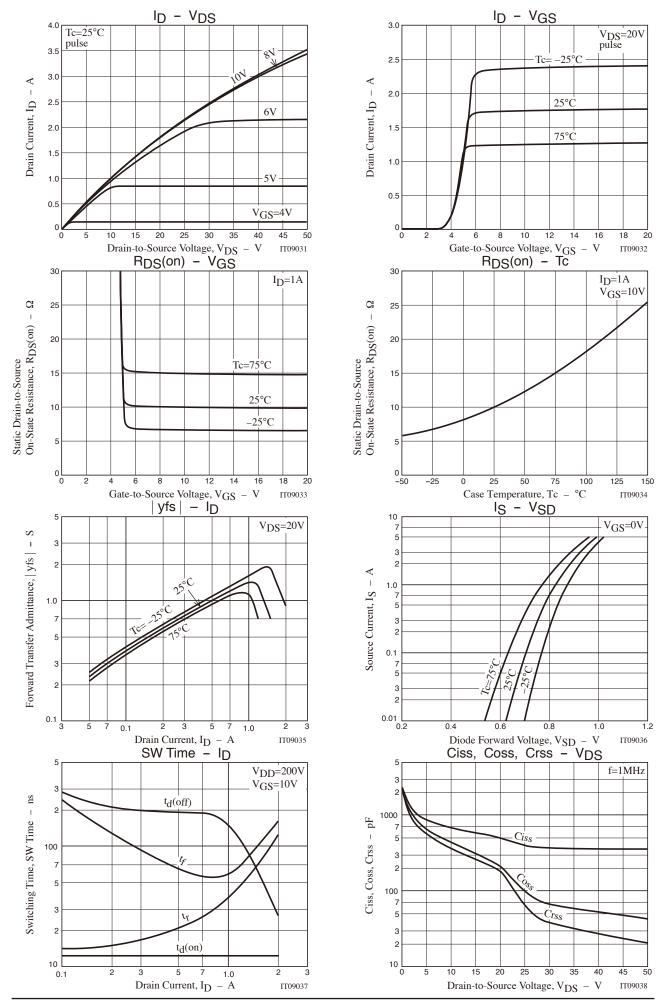


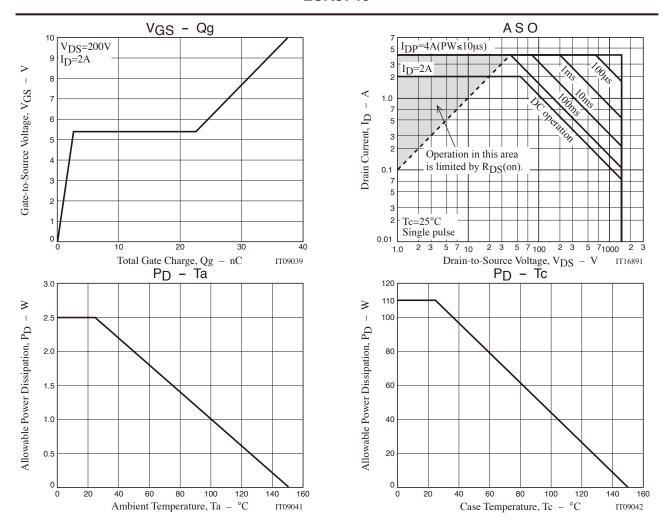
Fig.2 Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo	
2SK3746-1E	TO-3P-3L	30pcs./magazine	Pb Free	



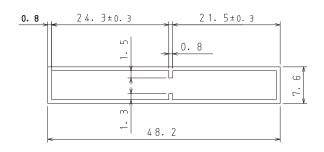


Magazine Specification

2SK3746-1E

1. Packing Format

Package Name	Maximum Number of devices contained (pcs)			Packing format		
1404480 14440	Magazine	Inner box	Outer box	Inner BOX	Outer BOX	
TO-3P-3L	30	450	1800	SPD-0V0001 15 magazines contained Dimensions:mm(external) 568×150×55	SPD-LV0010 4 inner boxes contained Dimensions:mm(external) 590x225x178	



Tolerance=±0.2mm
Thickness=0.8±0.2mm
Length =508.0±1mm
Material =PVC or PET
(Antistatic treatment)

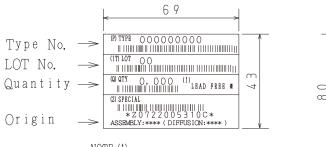
3. Storage method to magazine

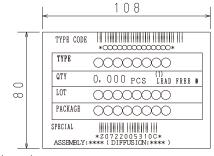


4. Inner box label (unit:mm)



It is a label at the time of factory shipments. The form of a label may change in physical distribution process.



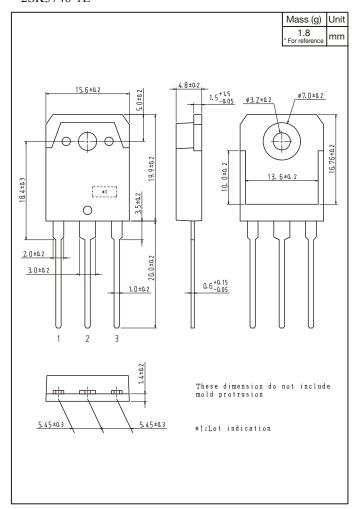


The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

Label		JEITA Phase			
LEAD FREE	3	JEITA Phase 3A			

Outline Drawing

2SK3746-1E



Note on usage: Since the 2SK3746 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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