



SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

LC79431KNE

CMOS LSI

Dot-Matrix LCD Drivers

Overview

The LC79431KNE is a large-scale dot matrix LCD common driver LSI. The LC79431KNE contains an 80-bit bidirectional shift register and is equipped with a 4-level LCD driver. The input/output pins for cascade connection can be used to further increase the IC's number of bits. The LC79431KNE can be used in conjunction with segment driver LC79401KNE (QIP100E) to drive a wide-screen LCD panel.

Features

- On-chip LCD drive circuit (80 bits)
- Display duty selection ranging from 1/64 to 1/256
- On-chip input/output pins support a further increases in bit number
- Supports externally supplied bias voltage
- Operating power supply voltage/operating temperature include
 VDD (Logic section) : 2.7 to 5.5V/-20 to +85°C
 VDD-VEE (LCD section) : 12 to 32V/-20 to +85°C
- CMOS process
- 100-pin flat plastic package (QIP100E)

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Specifications

Absolute Maximum Ratings at $T_a = 25 \pm 2^\circ C$, $V_{SS} = 0V$

Parameter	Symbol	Conditions	Ratings	unit
Maximum supply voltage (Logic)	V_{DD} max		-0.3 to +7.0	V
Maximum supply voltage (LCD)	$V_{DD}-V_{EE}$ max	*1	0 to 35	V
Maximum input voltage	V_I max		-0.3 to $V_{DD}+0.3$	V
Storage temperature	T_{STG}		-40 to +125	$^\circ C$

Note *1 The following relations between elements should be maintained: $V_{DD} \geq V_1 > V_2 > V_5 > V_{EE}$, $V_{DD} - V_2 \leq 7V$, $V_5 - V_{EE} \leq 7V$

Allowable Operating Ranges at $T_a = -20$ to $+85^\circ C$, $V_{SS} = 0V$

Parameter	Symbol	Conditions	min	typ	max	unit
Supply voltage (Logic)	V_{DD}		2.7		5.5	V
Supply voltage (LCD)	$V_{DD}-V_{EE}$	*2, 3	12		32	V
Input high level voltage	V_{IH}	DIO1, DIO80, CP, M, RS/LS, <u>DISPOFF</u>	0.8 V_{DD}			V
Input low level voltage	V_{IL}	DIO1, DIO80, CP, M, RS/LS, <u>DISPOFF</u>			0.2 V_{DD}	V
CP Shift clock	f_{CP}	CP			1	MHz
CP pulse width	t_{WC}	CP	63			ns
Setup time	t_{SETUP}	DIO1 \rightarrow CP, DIO80 \rightarrow CP	100			ns
Hold time	t_{HOLD}	DIO1 \rightarrow CP, DIO80 \rightarrow CP	100			ns
CP rise time	t_R	CP			50	ns
CP fall time	t_F	CP			50	ns

Note *2 The following relations between elements should be maintained: $V_{DD} \geq V_1 > V_2 > V_5 > V_{EE}$, $V_{DD} - V_2 \leq 7V$, $V_5 - V_{EE} \leq 7V$

*3 When the power supply is turned on, power to the LCD driver is turned on after or simultaneously with the turning on of the logic section's power supply. When the power supply is turned off, the logic power supply is turned off after or at the same time the LCD driver power supply is turned off.

Electrical Characteristics at $T_a = 25 \pm 2^\circ C$, $V_{DD} = 2.7$ to $5.5V$

Parameter	Symbol	Conditions	min	typ	max	unit
Input high level current	I_{IH}	$V_{IN}=V_{DD}$, $V_{DD}=5.5V$, DIO1, DIO80, CP, M, RS/LS, <u>DISPOFF</u>			1	μA
Input low level current	I_{IL}	$V_{IN}=V_{SS}$, $V_{DD}=5.5V$, DIO1, DIO80, CP, M, RS/LS, <u>DISPOFF</u>	-1			μA
Output high level voltage	V_{OH}	$I_{OH}=-0.4mA$, DIO1, DIO80	$V_{DD}-0.4$			V
Output low level voltage	V_{OL}	$I_{OL}=0.4mA$, DIO1, DIO80			0.4	V
Driver on resistance	$R_{ON}(1)$	$V_{DD}-V_{EE}=30V$, $ V_{DE}-V_O =0.5V$ $V_{DD}=4.5V$, O1 to O80 *4			1.0	$k\Omega$
	$R_{ON}(2)$	$V_{DD}-V_{EE}=20V$, $ V_{DE}-V_O =0.5V$ $V_{DD}=4.5V$, O1 to O80 *4			1.0	$k\Omega$
Consumable current drain (1)	I_{SS}	$V_{DD}-V_{EE}=30V$, CP=14kHz no-load, $V_{DD}=5.5V$; V_{SS}			100	μA
Consumable current drain (2)	I_{EE}	$V_{DD}-V_{EE}=30V$, CP=14kHz no-load, $V_{DD}=5.5V$; V_{EE}			100	μA
Input capacitance	C_I	$f=1MHz$; CP		6		pF

Note *4 $V_{DE} = V_1$ or V_2 or V_5 or V_{EE} , $V_1 = V_{DD}$, $V_2 = 16/17 (V_{DD}-V_{EE})$, $V_5 = 1/17 (V_{DD}-V_{EE})$

Switching Characteristics at $T_a = 25 \pm 2^\circ C$, $V_{SS} = 0V$, $V_{DD} = 2.7$ to $5.5V$

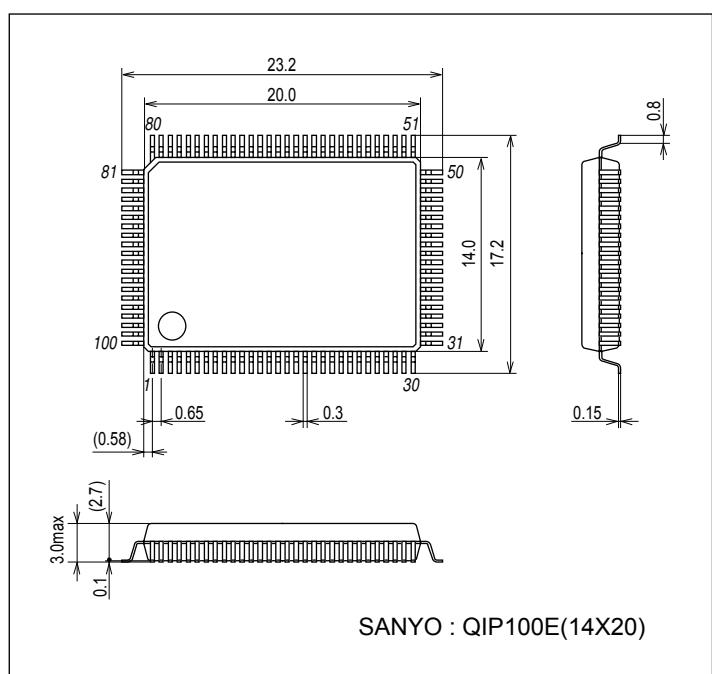
Parameter	Symbol	Conditions	min	typ	max	unit
Output delay time	t_{PLH}	$CL=15pF$; CP \rightarrow DIO1, CP \rightarrow DIO80			250	ns
	t_{PHL}	$CL=15pF$; CP \rightarrow DIO1, CP \rightarrow DIO80			250	ns

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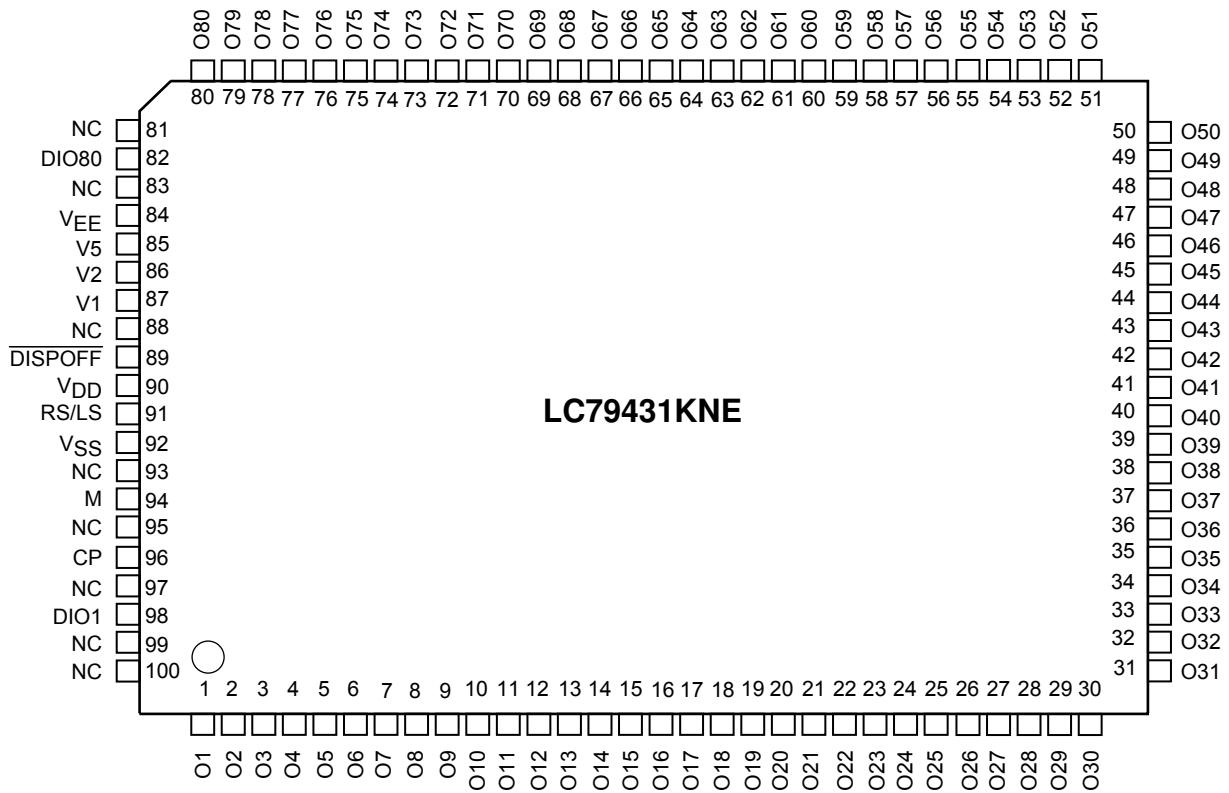
Package Dimensions

unit:mm (typ)

3151A

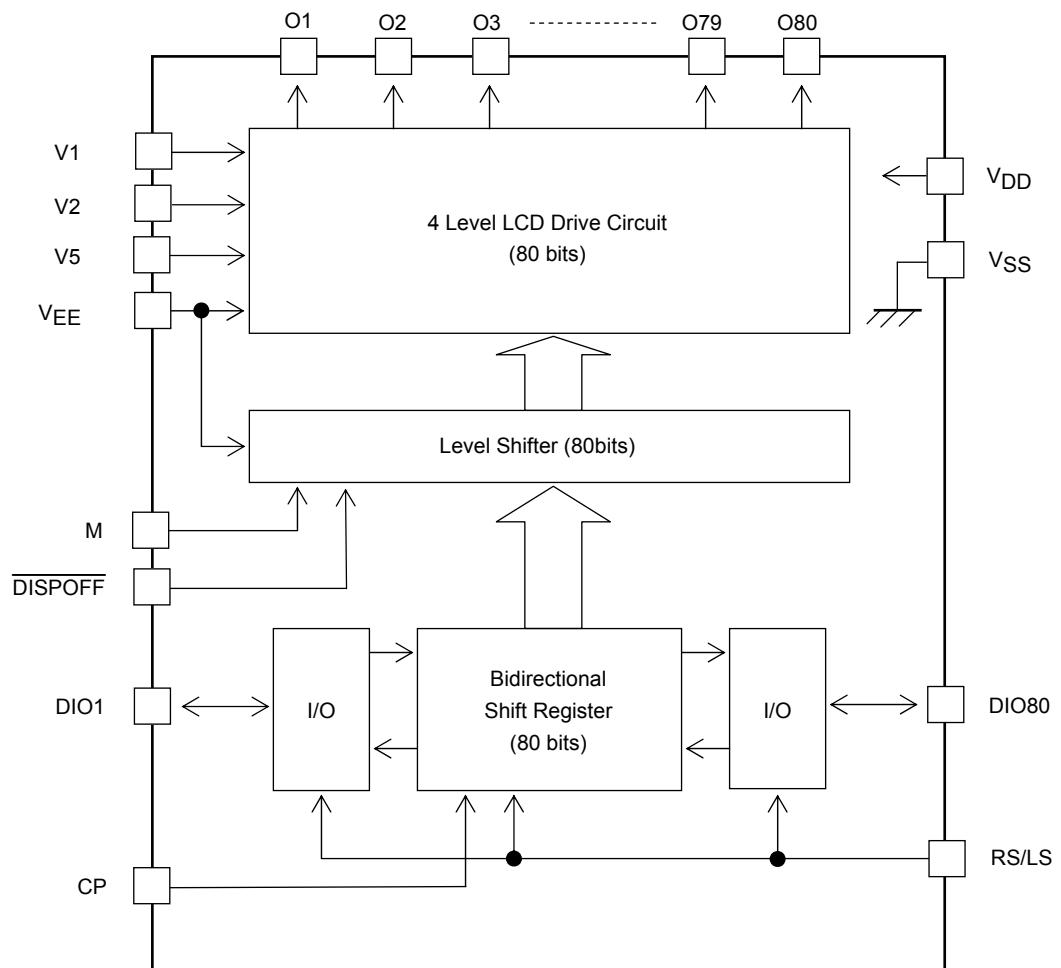


Pin Assignment



Top view

Equivalent Circuit Block Diagram



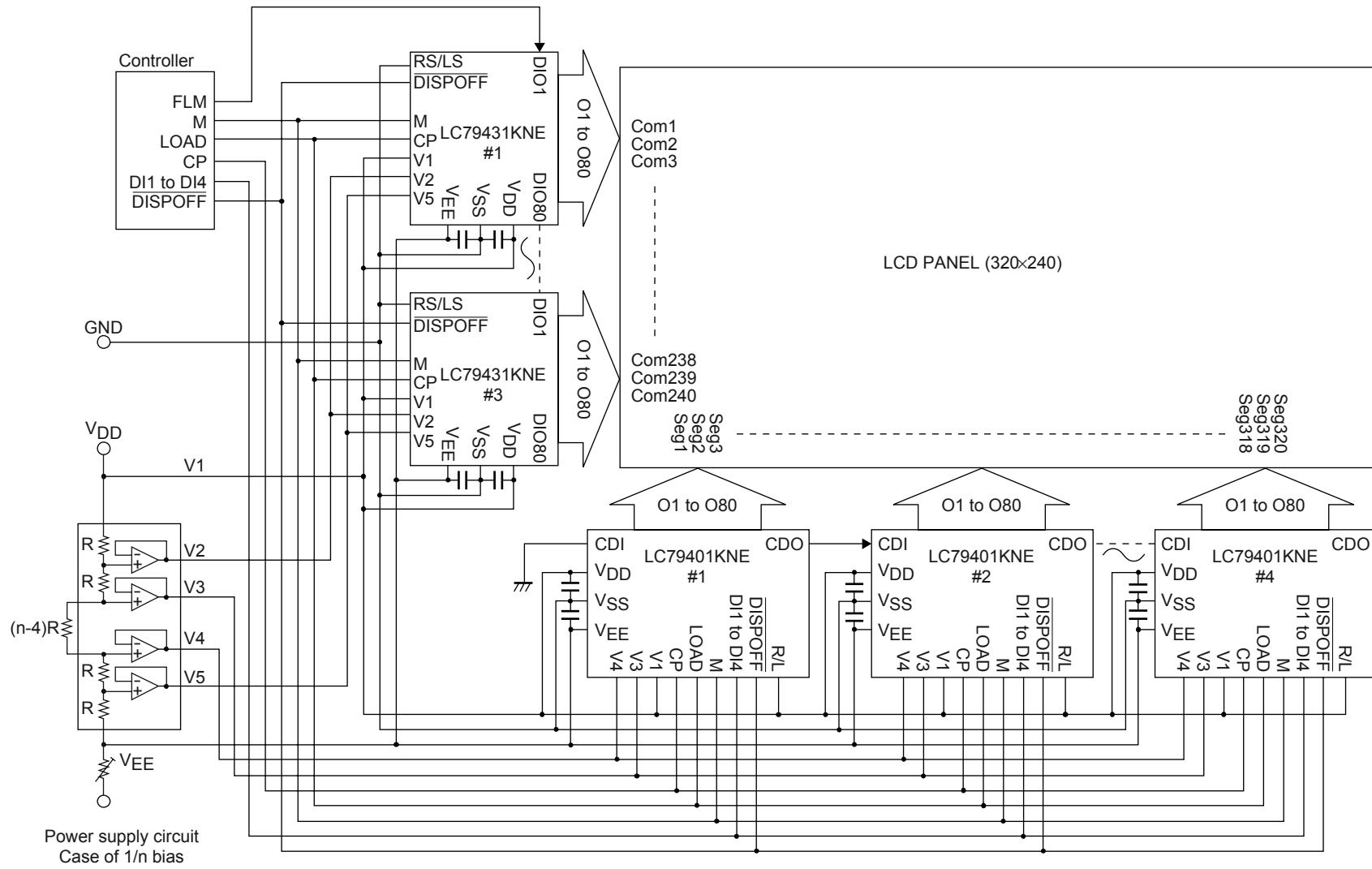
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Pin Function

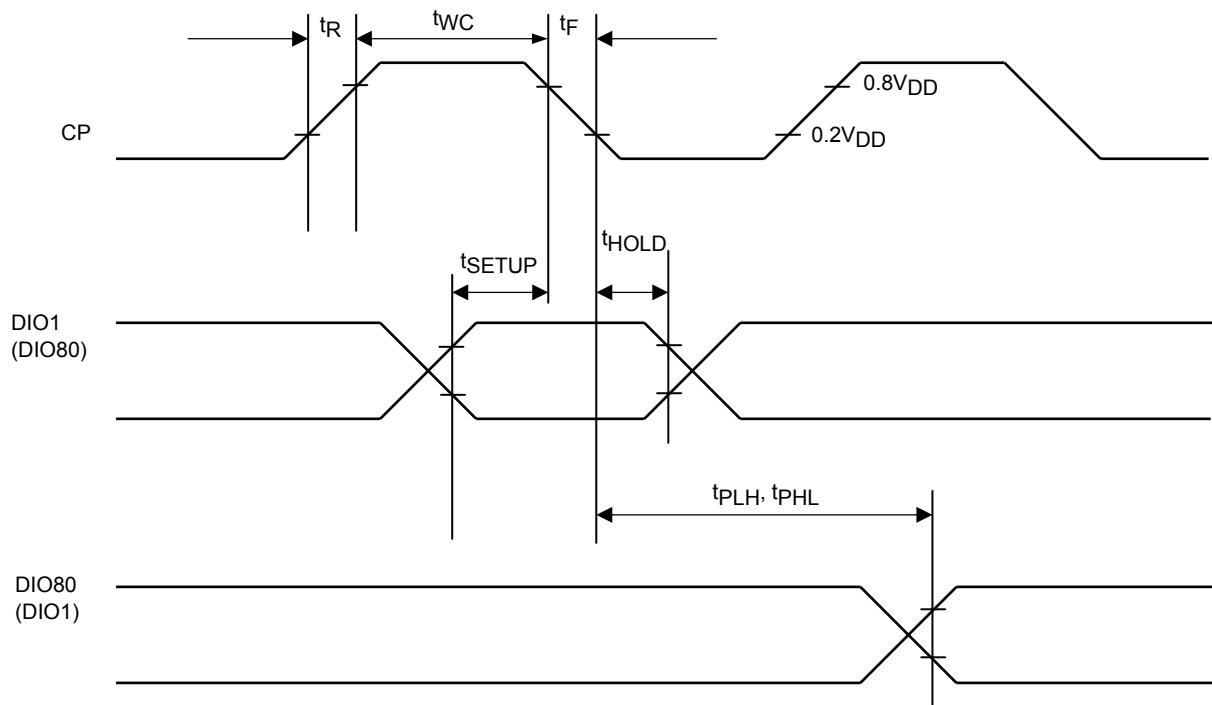
Pin No	Symbol	I/O	Function																								
90	V _{DD}	Supply	V _{DD} -V _{SS} : Logic power supply V _{DD} -V _{EE} : LCD drive circuit power supply																								
92	V _{SS}																										
84	V _{EE}																										
87	V ₁	Supply	LCD drive level power supply																								
86	V ₂		V ₁ , V _{EE} : Selected level																								
85	V ₅		V ₂ , V ₅ : Unselected level																								
96	CP	I	Bidirectional shift register shift clock (falling edge trigger)																								
98	DIO1 DIO80	I/O I/O I	<table border="1"> <thead> <tr> <th>RS/LS</th> <th>Data Transfer Direction</th> <th>DIO1</th> <th>DIO80</th> </tr> </thead> <tbody> <tr> <td>L (Shift right)</td> <td>O₁ → O₈₀</td> <td>IN</td> <td>OUT</td> </tr> <tr> <td>H (Shift left)</td> <td>O₈₀ → O₁</td> <td>OUT</td> <td>IN</td> </tr> </tbody> </table>	RS/LS	Data Transfer Direction	DIO1	DIO80	L (Shift right)	O ₁ → O ₈₀	IN	OUT	H (Shift left)	O ₈₀ → O ₁	OUT	IN												
RS/LS	Data Transfer Direction	DIO1	DIO80																								
L (Shift right)	O ₁ → O ₈₀	IN	OUT																								
H (Shift left)	O ₈₀ → O ₁	OUT	IN																								
82																											
91	RS/LS																										
94	M	I	LCD drive output alternation signal																								
89	DISPOFF	I	O ₁ to O ₈₀ output controlling input pins.																								
1	O ₁ O ₈₀	O	<p>LCD drive outputs The output levels are determined by the combination of the output the data, The M signal, and the DISPOFF pin as shown in the table.</p> <table border="1"> <thead> <tr> <th>M</th> <th>Data</th> <th>DISPOFF</th> <th>Output</th> </tr> </thead> <tbody> <tr> <td>L</td> <td>L</td> <td>H</td> <td>V₂</td> </tr> <tr> <td>L</td> <td>H</td> <td>H</td> <td>V_{EE}</td> </tr> <tr> <td>H</td> <td>L</td> <td>H</td> <td>V₅</td> </tr> <tr> <td>H</td> <td>H</td> <td>H</td> <td>V₁</td> </tr> <tr> <td>*</td> <td>*</td> <td>L</td> <td>V₁</td> </tr> </tbody> </table> <p>* Don't care (May be set to either "H" or "L")</p>	M	Data	DISPOFF	Output	L	L	H	V ₂	L	H	H	V _{EE}	H	L	H	V ₅	H	H	H	V ₁	*	*	L	V ₁
M	Data	DISPOFF	Output																								
L	L	H	V ₂																								
L	H	H	V _{EE}																								
H	L	H	V ₅																								
H	H	H	V ₁																								
*	*	L	V ₁																								
80																											
81	NC	-	Must be left open.																								
83																											
88																											
93																											
95																											
97																											
99																											
100																											

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Application Example (LC79401KNE/LC79431KNE)



Switching Characteristics Diagram



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