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LA6584JA

Monolithic Linear IC

Single-phase Full-wave Driver For Fan Motor

Overview

The LA6584JA is Single-phase bipolar fan motor is put into silent driving by means of BTL output linear drive, offering high efficiency and power saving by suppressing the reactive current. Lock protection and rotation signal (FG, RD) circuits are built in, ensuring optimum application to small fans for notebook PC, consumer equipment power supply, car audio system, CPU cooler, etc. that require high reliability and low noise.

Functions and Applications

- Single-phase full-wave driver for fan motor

Specifications

Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Output voltage	$V_{CC\text{ max}}$		15	V
Output current	$I_{OUT\text{ max}}$		1.2	A
Output withstand voltage	$V_{OUT\text{ max}}$		15	V
RD/FG output pin output Withstand voltage	$V_{RD/FG\text{ max}}$		15	V
RD/FG output current	$I_{RD/FG\text{ max}}$		5	mA
HB output voltage	$I_B\text{ max}$		10	mA
Allowable dissipation	$P_d\text{ max}$	Mounted on a specified board*	0.8	W
Operating temperature range	T_{opr}		-30 to +90	$^\circ\text{C}$
Storage temperature range	T_{stg}		-55 to +150	$^\circ\text{C}$

* Mounted on a specified board (114.3mm × 76.1mm × 1.6mm, Glass epoxy)

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

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Recommended Operating Range at Ta = 25°C

Parameter	Conditions	Conditions	Ratings	Unit
Supply voltage	V _{CC}		2.8 to 14.0	V
Common-phase input voltage range of Hall input	V _{ICM}		0 to V _{CC} -1.5	V

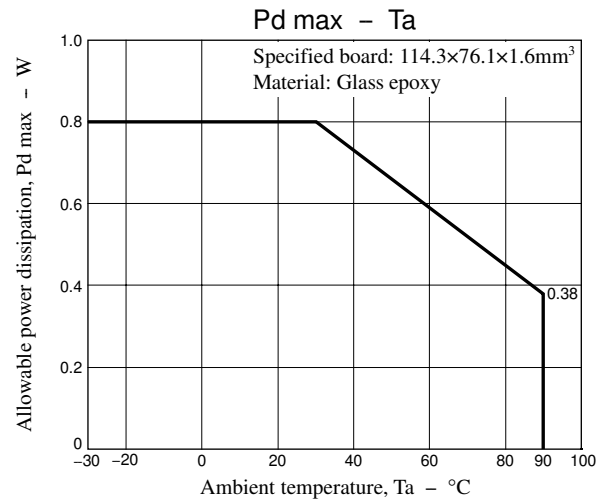
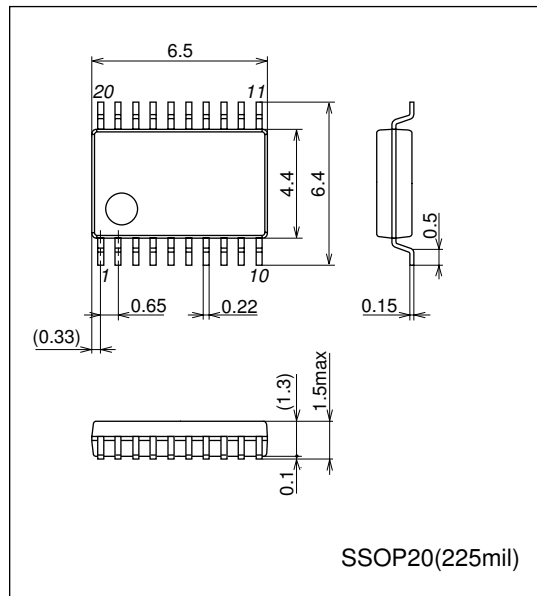
Electrical Characteristics at Ta = 25°C V_{CC} = 12V, unless otherwise specified.

Parameter	Conditions	Conditions	Ratings			Unit
			min	typ	max	
Circuit current	I _{CC1}	During drive (CT = L)	4	6	9	mA
	I _{CC2}	During lock protection (CT = H)	2	4	6	mA
Lock detection capacitor charge current	I _{CT1}		2.0	2.8	3.5	μA
Capacitor discharge current	I _{CT2}		0.15	0.23	0.30	μA
Capacitor charge and discharge current ratio	R _{CT}	R _{CD} = I _{CT1} /I _{CT2}	9	12	15	-
CT charge voltage	V _{CT1}		1.6	1.7	1.8	V
CT discharge voltage	V _{CT2}		0.6	0.7	0.8	V
OUT output L saturation voltage	V _{OL}	I _O = 200mA		0.2	0.3	V
OUT output H saturation voltage	V _{OH}	I _O = 200mA		0.9	1.2	V
Hall input sensitivity	V _{HN}	Zero peak value (including offset and hysteresis)		7	15	mV
RD/FG output pin L voltage	V _{RD/FG}	I _{RD/FG} = 5mA		0.1	0.2	V
RD/FG output pin leak current	I _{RD/FGL}	V _{RD/FG} = 15V		1	30	μA
HB output L voltage	V _{HBL}	I _{HB} = 5mA	1.3	1.5	1.7	V

Package Dimensions

unit : mm (typ)

3179C

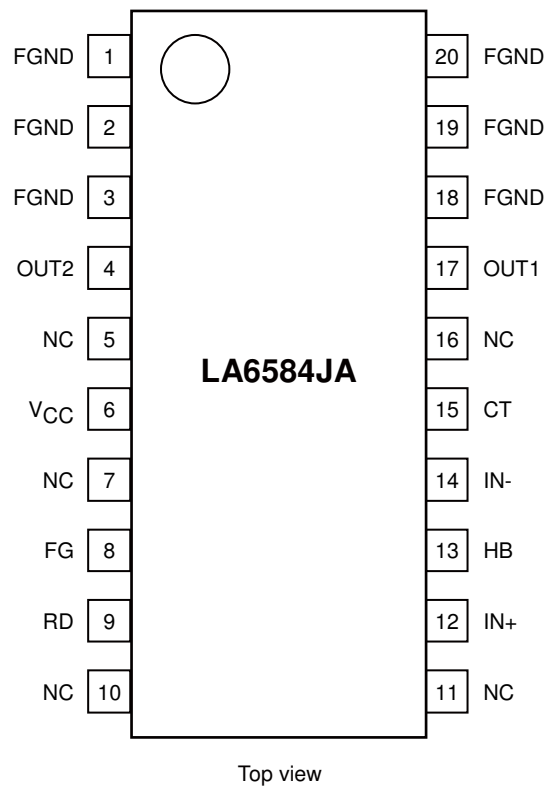


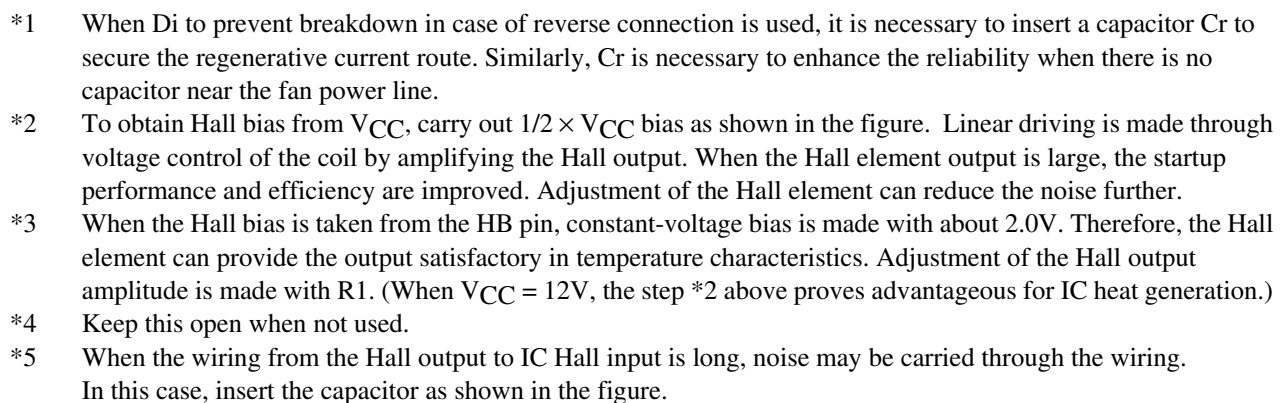
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Truth Table

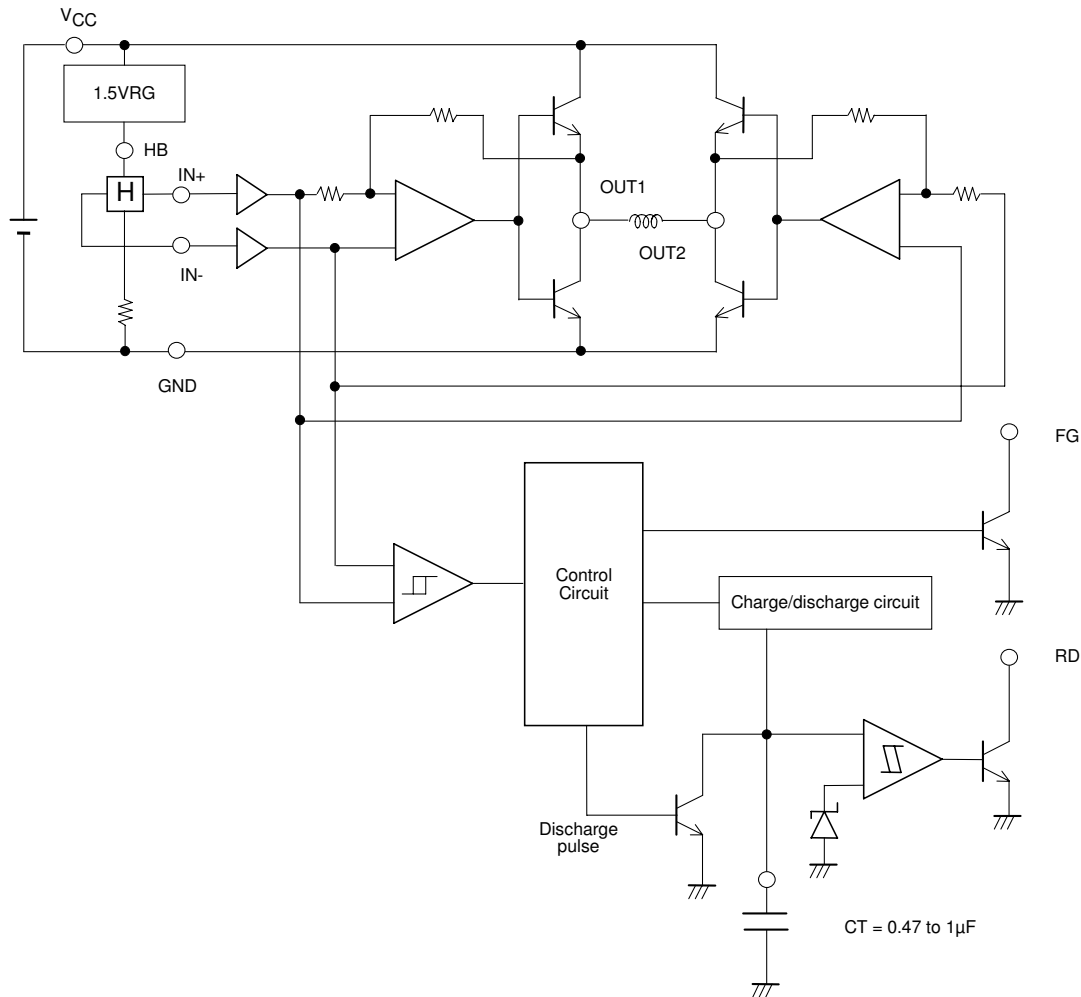
IN-	IN+	CT	OUT1	OUT2	FG	RD	Mode
H	L	L	H	L	L	L	During rotation
L	H		L	H	H		
-	-	H	OFF	OFF	-	H	Lock protection

Pin Assignment





Internal Equivalent Circuit



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