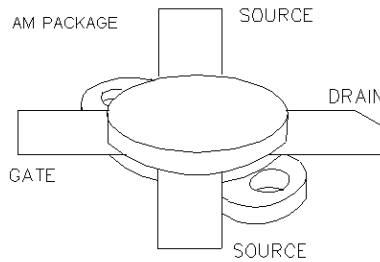




**General Description**

Silicon VDMOS and LDMOS transistors designed specifically for broadband RF applications. Suitable for Military Radios, Cellular and Paging Amplifier Base Stations, Broadcast FM/AM, MRI, Laser Driver and others.

"Polyfet"™ process features gold metal for greatly extended lifetime. Low output capacitance and high  $F_T$  enhance broadband performance



**PATENTED GOLD METALIZED SILICON GATE ENHANCEMENT MODE RF POWER VDMOS TRANSISTOR**

**150 Watts Single Ended**

**Package Style AM**

**HIGH EFFICIENCY, LINEAR, HIGH GAIN, LOW NOISE**

**ABSOLUTE MAXIMUM RATINGS (TC = 25 °C)**

Total Device Dissipation	Junction to Case Thermal Resistance	Maximum Junction Temperature	Storage Temperature	DC Drain Current	Drain to Gate Voltage	Drain to Source Voltage	Gate to Source Voltage
250 Watts	0.8 °C/W	200 °C	-65 °C to 150 °C	10 A	150 V	150 V	30V

**RF CHARACTERISTICS ( 150WATTS OUTPUT )**

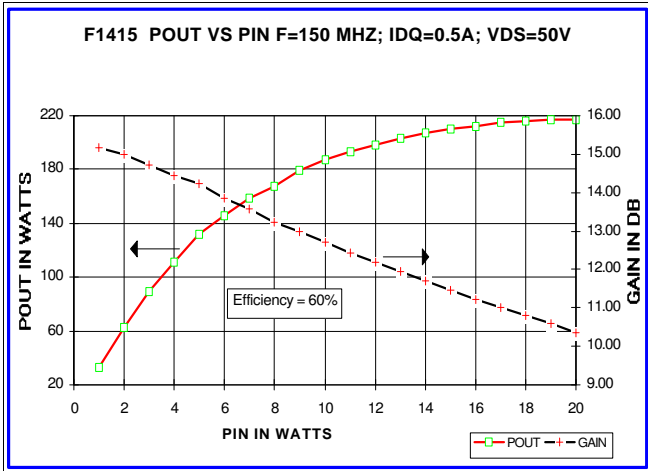
SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
Gps	Common Source Power Gain	13			dB	$I_{dq} = 0.6 A, V_{ds} = 50.0V, F = 150 MHz$
$\eta$	Drain Efficiency		65		%	$I_{dq} = 0.6 A, V_{ds} = 50.0V, F = 150 MHz$
VSWR	Load Mismatch Tolerance			20:1	Relative	$I_{dq} = 0.6 A, V_{ds} = 50.0V, F = 150 MHz$

**ELECTRICAL CHARACTERISTICS (EACH SIDE)**

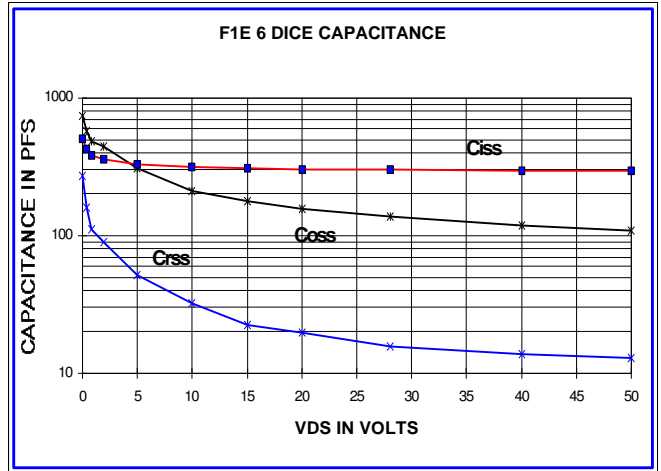
SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
Bvdss	Drain Breakdown Voltage	125			V	$I_{ds} = 0.1 A, V_{gs} = 0V$
Idss	Zero Bias Drain Current			12	mA	$V_{ds} = 50.0 V, V_{gs} = 0V$
Igss	Gate Leakage Current			1	uA	$V_{ds} = 0 V, V_{gs} = 30V$
Vgs	Gate Bias for Drain Current	1		7	V	$I_{ds} = 0.15 A, V_{gs} = V_{ds}$
gM	Forward Transconductance		4.8		Mho	$V_{ds} = 10V, V_{gs} = 5V$
Rdson	Saturation Resistance		0.25		Ohm	$V_{gs} = 20V, I_{ds} = 10A$
Idsat	Saturation Current		28.8		Amp	$V_{gs} = 20V, V_{ds} = 10V$
Ciss	Common Source Input Capacitance		270		pF	$V_{ds} = 50.0 V, V_{gs} = 0V, F = 1 MHz$
Crss	Common Source Feedback Capacitance		13.2		pF	$V_{ds} = 50.0 V, V_{gs} = 0V, F = 1 MHz$
Coss	Common Source Output Capacitance		120		pF	$V_{ds} = 50.0 V, V_{gs} = 0V, F = 1 MHz$

# F1415

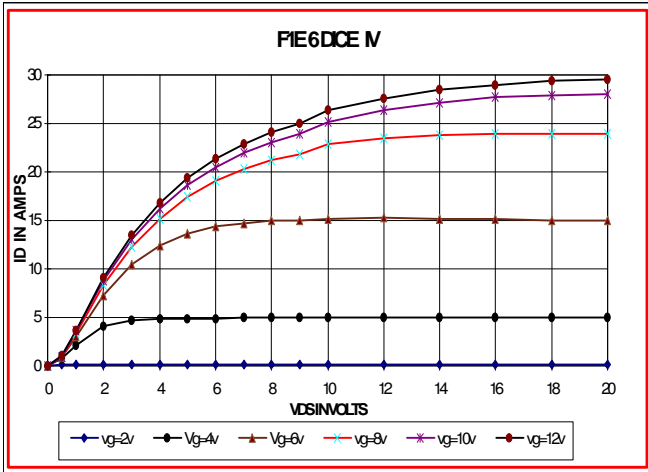
POUT VS PIN GRAPH



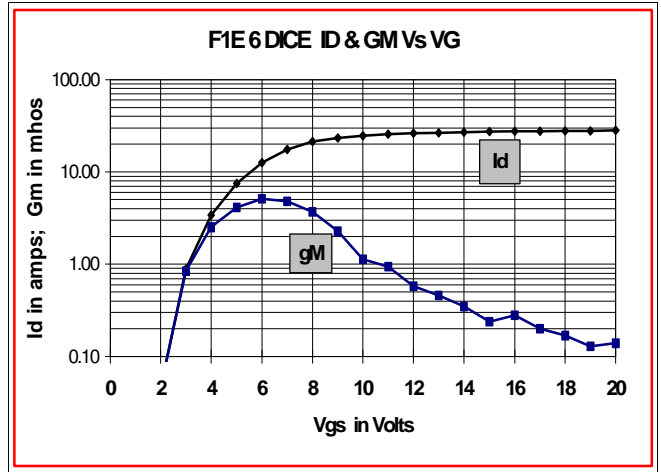
CAPACITANCE VS VOLTAGE



IV CURVE



ID AND GM VS VGS



S11 AND S22 SMITH CHART

PACKAGE DIMENSIONS IN INCHES

