



Micro Commercial Components

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 20736 Marilla Street Chatsworth
 CA 91311
 Phone: (818) 701-4933
 Fax: (818) 701-4939

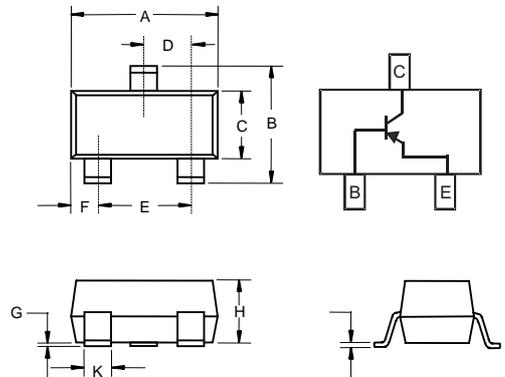
MMBTA93

Features

- Surface Mount SOT-23 Package
- Capable of 300mWatts of Power Dissipation
- Lead Free Finish/RoHS Compliant ("P" Suffix designates RoHS Compliant. See ordering information)
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1
- Marking: YW
- Halogen free available upon request by adding suffix "-HF"

PNP Silicon High Voltage Transistor

SOT-23



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	.110	.120	2.80	3.04	
B	.083	.104	2.10	2.64	
C	.047	.055	1.20	1.40	
D	.035	.041	.89	1.03	
E	.070	.081	1.78	2.05	
F	.018	.024	.45	.60	
G	.0005	.0039	.013	.100	
H	.035	.044	.89	1.12	
J	.003	.007	.085	.180	
K	.015	.020	.37	.51	

Electrical Characteristics @ 25°C Unless Otherwise Specified

Symbol	Parameter	Min	Max	Units
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OFF CHARACTERISTICS

$V_{(BR)CEO}$	Collector-Emmitter Breakdown Voltage ($I_C=-1.0mA_{dc}$, $I_B=0$)	-200		Vdc
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage ($I_C=-100\mu A_{dc}$, $I_E=0$)	-200		Vdc
$V_{(BR)EBO}$	Emmitter-Base Breakdown Voltage ($I_E=-100\mu A_{dc}$, $I_C=0$)	-5		Vdc
I_C	Collector Current-Continuous	-500		mA _{dc}
I_{CBO}	Collector Cutoff Current ($V_{CB}=-200V_{dc}$, $I_E=0$)		-250	nA _{dc}
I_{EBO}	Emmitter Cutoff Current ($V_{EB}=-5V_{dc}$, $I_C=0$)		-100	nA _{dc}

ON CHARACTERISTICS

η_{FE}	DC Current Gain ($I_C=-1.0mA_{dc}$, $V_{CE}=-10V_{dc}$) ($I_C=-10mA_{dc}$, $V_{CE}=-10V_{dc}$) ($I_C=-30mA_{dc}$, $V_{CE}=-10V_{dc}$)	25 40 25		
$V_{CE(sat)}$	Collector-Emmitter Saturation Voltage ($I_C=-20mA_{dc}$, $I_B=-2.0mA_{dc}$)		-0.5	Vdc
$V_{BE(sat)}$	Base-Emmitter Saturation Voltage ($I_C=-20mA_{dc}$, $I_B=-2.0mA_{dc}$)		-0.9	Vdc

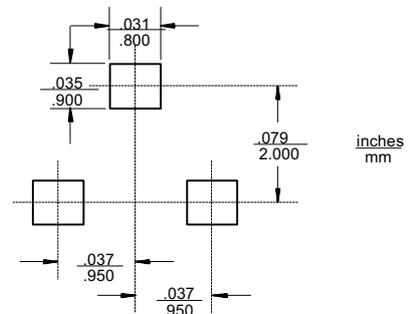
SMALL-SIGNAL CHARACTERISTICS

f_T	Current Gain-Bandwidth Product ($I_C=-10mA_{dc}$, $V_{CE}=-20V_{dc}$, $f=100MHz$)	50		MHz
C_{cb}	Collector-Base Capacitance ($V_{CB}=-20V_{dc}$, $I_E=0$, $f=1.0MHz$)		8.0	pF

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Collector Power Dissipation	P_c	300	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	417	°C/mW
Junction and Storage Temperature	T_J, T_{stg}	-55 to +150	°C

Suggested Solder Pad Layout



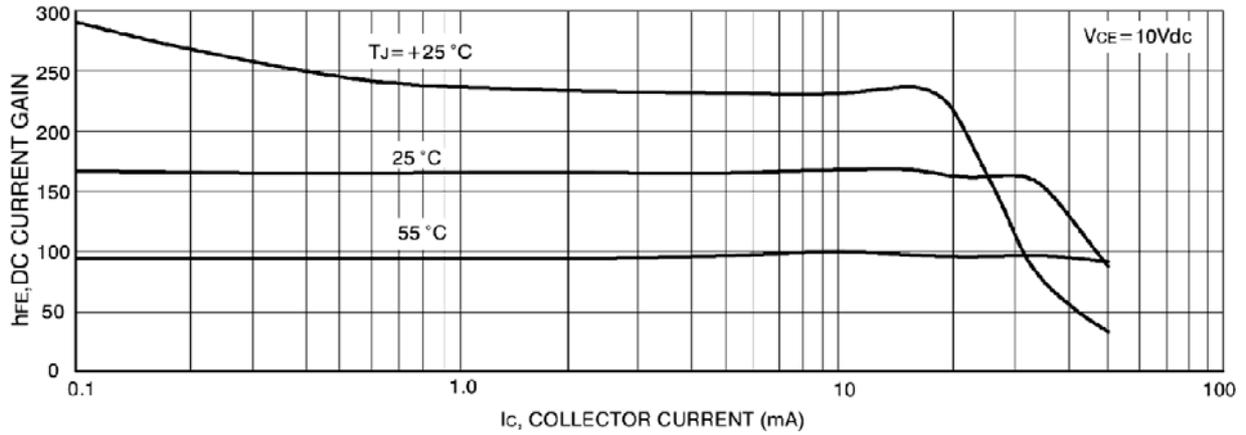


Figure 1. DC Current Gain

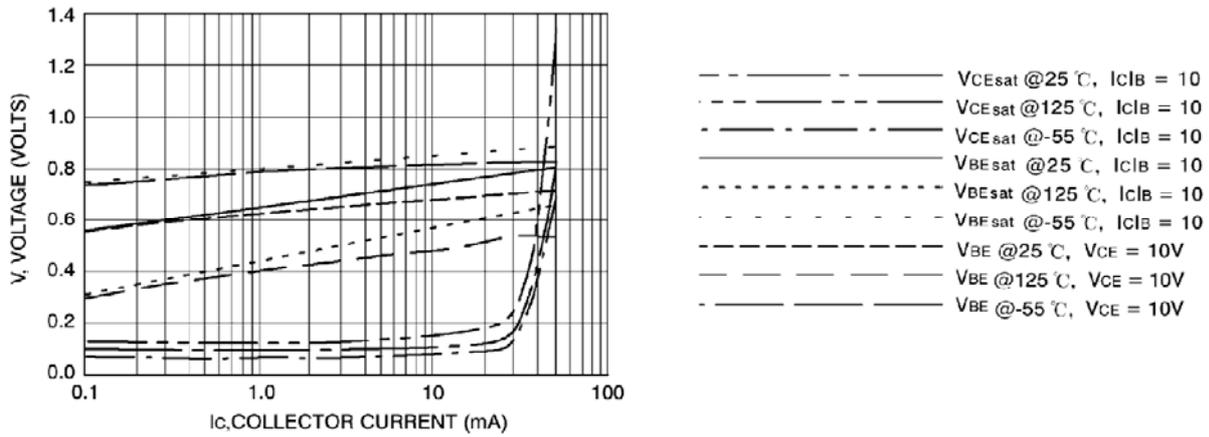


Figure 2. "ON" Voltages

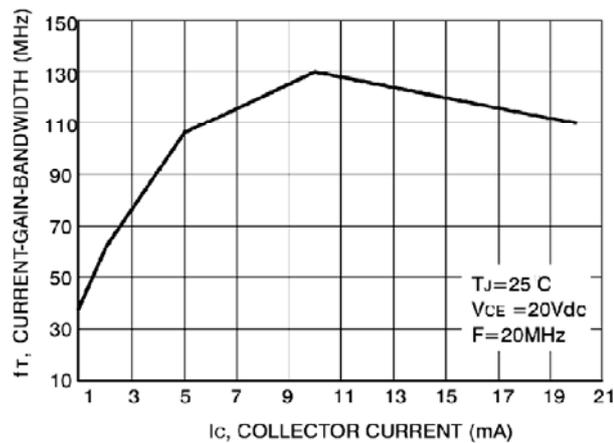


Figure 3. Current-Gain - Bandwidth



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Ordering Information :

Device	Packing
Part Number-TP	Tape & Reel; 3 Kpcs/Reel

Note : Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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