

Product Brief

Generic features

- Compatible with low or high IF outputting CAN tuners and silicon tuners
- High-performance integrated ADC for direct IF architecture in all QAM modes
- Built-in PLL, supporting either external crystal, oscillator or clock impulse
- Built-in SDRAM for de-interleaving
- Ultra low power consumption and multi level power saving modes
- Full I²C bus support, with I²C pass-through feature to isolate tuner from the I²C bus to avoid interference
- Flexible and diversified forms for serial or parallel TS output interface

Characteristics

- Multi supplies: 1.2V (core) and 3.3V (I/O)
- Chip package: 48pins QFN 7×7×0.85 mm³
- Typical power consumption: 240 mW

Applications

- iDTV, Set-top-box and car receiver
- DTV NIM module and extension module
- DTV testing and detection equipment
- Portable or handled TV receiver
- PCTV card and USB TV dongle

DTMB demodulation

- Full compliance with GB20600-2006
- Full compliance with requirements of the GB/T 26683-2011 and GB/T 26686-2011
- Channel impairments management:
 - *Built-in reception quality indicators*
 - *Impulsive noise rejection capable*
 - *Outstanding adjacent and co-channel rejection capability with integrated and flexible digital channel filtering*
 - *High-performance digital carrier, timing and symbol recovery loops*
- Performance advantages
 - *Rapid PLL lock and channel changing*
 - *Outstanding multipath and pre-echo performance in SFN environment*
 - *High speed mobile receiving*

DVB-C demodulation

- Full compliance with GY/T 170-2001
- 16, 32, 64, 128 and 256QAM
- Variable symbol rates in wide range
- Front de-rotator for better low symbol rate performance and relaxed tuner constraints
- Integrated matched filtering
- Fast signal acquisition

