



THCV217

V-by-One® HS High-speed video data transmitter

General Description

THCV217 is designed to support video data transmission between the host and display.

One high-speed lane can carry up to 32bit data and 3 bits of synchronizing signals at a pixel clock frequency from 20MHz to 85MHz.

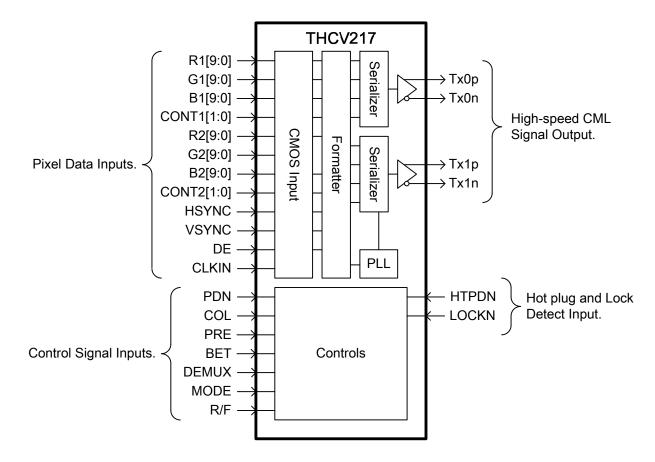
This chip, which has two high-speed data lanes, can transmit video data up to 1080p/10b/60Hz. The maximum serial data rate is 3.4Gbps/lane.

Width	Link	CMOS IO Frequency
24bit and 32bit	Si/So Di/Do	20MHz to 85MHz
	Si/Do	40MHz to 170MHz

Features

- Color depth selectable: 24(8*3)/32(10*3)bit
- Single-in/Single-out, Single-in/Dual-out, and Dual-in/Dual-out selectable.
- AC coupling for high-speed lines
- CORE 1.8v, CMOS IO 3.3v
- Package: TFBGA105
- Wide frequency range
- CDR requires no external frequency reference
- 3.3V logic compatible input interface.

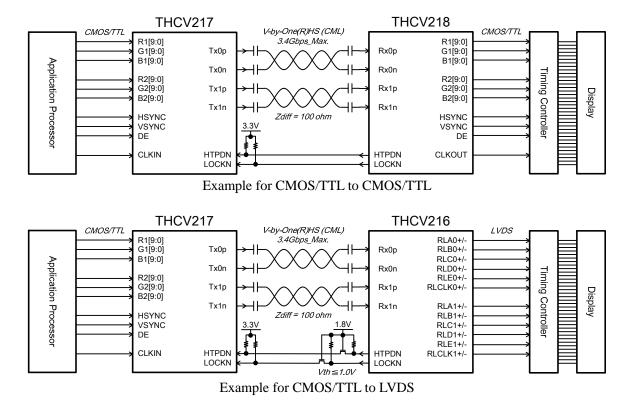
Si/So: Single-in/Single-out, Di/So: Dual-in/Single-out, Si/Do: Single-in/Dual-out



Block Diagram



Example System Diagram



Notices and Requests

- 1. The product specifications described in this material are subject to change without prior notice.
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- 6. Despite our utmost efforts to improve the quality and reliability of the product, faults will occur with a certain small probability, which is inevitable to a semi-conductor product. Therefore, you are encouraged to have sufficiently redundant or error preventive design applied to the use of the product so as not to have our product cause any social or public damage.
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