# RICOH

# **R1121N SERIES**

#### **LOW NOISE 150mA LDO REGULATOR**

NO.EA-058-111026

#### **OUTLINE**

The R1121N Series are CMOS-based voltage regulator ICs with high output voltage accuracy, extremely low supply current, low ON-resistance, and high Ripple Rejection. Each of these voltage regulator ICs consists of a voltage reference unit, an error amplifier, resistors, a current limit circuit, and a chip enable circuit.

These ICs perform with low dropout voltage and a chip enable function. The line transient response and load transient response of the R1121N Series are excellent, thus these ICs are very suitable for the power supply for hand-held communication equipment.

The output voltage of these ICs is fixed with high accuracy.

Since the package for these ICs is SOT-23-5 (Mini-mold) package, high density mounting of the ICs on boards is possible.

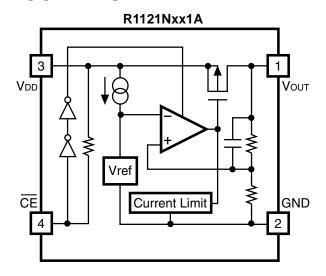
#### **FEATURES**

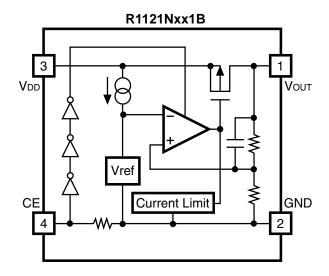
Supply Current	Τyp. 35μA
Standby Mode	Τyp. 0.1μA
Dropout Voltage	Тур. 0.2V (Іоυт=100mA)
Ripple Rejection	Typ. 70dB(f=1kHz)
Temperature-Drift Coefficient of Output Voltage	Typ. ±100ppm/℃
Line Regulation	Typ. 0.05%/V
Output Voltage Accuracy	±2.0%
Output Voltage Range	1.5V to 5.0V (0.1V steps)
	(For other voltages, please refer to MARK INFORMATIONS.)
Package	SOT-23-5 (Mini-mold)
• Built-in chip enable circuit (2 types; A: active "L", B:	active "H")
• Pin-out	Similar to the TK112,TK111

#### **APPLICATIONS**

- Power source for cellular phones such as GSM, CDMA and various kinds of PCSs.
- Power source for domestic appliances such as cameras, VCRs and camcorders.
- Power source for battery-powered equipment.

# **BLOCK DIAGRAM**





## **SELECTION GUIDE**

The output voltage, the active type for the ICs can be selected at the user's request.

Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
R1121Nxx1*-TR-FE	SOT-23-5	3,000 pcs	Yes	Yes

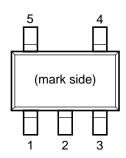
xx: The output voltage can be designated in the range from 1.5V(15) to 5.0V(50) in 0.1V steps. (For other voltages, please refer to MARK INFORMATIONS.)

- \* : Designation of Active Type. (A) "L" active

  - (B) "H" active

## **PIN CONFIGURATION**

#### **SOT-23-5**



## **PIN DESCRIPTION**

Pin No	Symbol	Description
1	Vоит	Output pin
2	GND	Ground Pin
3	$V_{DD}$	Input Pin
4	CE or CE	Chip Enable Pin
5	NC	No Connection

#### **ABSOLUTE MAXIMUM RATINGS**

Symbol	Item	Rating	Unit
Vin	Input Voltage	9.0	V
Vce	Input Voltage( CE or CE Pin)	-0.3 ~ V <sub>IN</sub> +0.3	V
Vouт	Output Voltage	-0.3 ~ VIN+0.3	V
<b>І</b> оит	Output Current	200	mA
P□	Power Dissipation (SOT-23-5)*	420	mW
Topt	Operating Temperature Range	-40 ~ 85	°C
Tstg	Storage Temperature Range	-55 ~ 125	°C

<sup>\*)</sup> For Power Dissipation, please refer to PACKAGE INFORMATION.

#### **ABSOLUTE MAXIMUM RATINGS**

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field.

The functional operation at or over these absolute maximum ratings is not assured.

# **ELECTRICAL CHARACTERISTICS**

• R1121Nxx1A Topt=25°C

Symbol	Item	Conditions	Min.	Тур.	Max.	Unit
Vouт	Output Voltage	VIN = Set VOUT + 1V $1mA \le IOUT \le 30mA$	Vо∪т ×0.98		Vо∪т ×1.02	V
Іоит	Output Current	Refer to the ELECTRICAL CHARAC	CTERISTICS b	y OUTPU	T VOLTAGE	
ΔVουτ/ΔΙουτ	Load Regulation	VIN = Set VOUT + 1V 1mA ≤ IOUT ≤ 80mA		12	40	mV
VDIF	Dropout Voltage	Refer to the ELECTRICAL CHARACTERISTICS by OUTPUT VOLTAGE				
Iss	Supply Current	VIN = Set Vout + 1V		35	70	μА
Istandby	Supply Current (Standby)	VIN = VCE = Set Vout + 1V		0.1	1.0	μА
ΔVουτ/ΔVιν	Line Regulation	Set Vout+0.5V $\leq$ Vin $\leq$ 8.0V Iout = 30mA		0.05	0.20	%/V
RR	Ripple Rejection	f = 1kHz, Ripple 0.5Vp-p VIN = Set Vout + 1V		70		dB
Vin	Input Voltage		2.0		8.0	V
ΔVουτ/ ΔTopt	Output Voltage Temperature Coefficient	$ IOUT = 30mA  -40°C \le Topt \le 85°C $		±100		ppm/°C
Isc	Short Current Limit	Vout = 0V		50		mA
Rpu	CE Pull-up Resistance		2.5	5.0	10.0	МΩ
Vсен	CE Input Voltage "H"		1.5		Vin	V
VCEL	CE Input Voltage "L"		0.00		0.25	V
en	Output Noise	BW=10Hz to 100kHz		30		μVrms

#### RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

● R1121Nxx1B

Symbol	Item	Conditions	Min.	Тур.	Max.	Unit
Vouт	Output Voltage	VIN = Set VOUT + 1V 1mA ≦ IOUT ≦ 30mA	Vо∪т ×0.98		Vо∪т ×1.02	V
Іоит	Output Current	Refer to the ELECTRICAL CHARAC	TERISTICS b	y OUTPU	T VOLTAGE	
ΔVουτ/ΔΙουτ	Load Regulation	VIN = Set VOUT + 1V 1mA ≦ IOUT ≦ 80mA		12	40	mV
VDIF	Dropout Voltage	Refer to the ELECTRICAL CHARAC	TERISTICS b	y OUTPU	T VOLTAGE	
Iss	Supply Current	VIN = Set Vout + 1V		35	70	μА
Istandby	Supply Current (Standby)	VIN = Set Vout + 1V VCE =GND		0.1	1.0	μА
ΔVουτ/ΔVιν	Line Regulation			0.05	0.20	%/V
RR	Ripple Rejection	f = 1kHz, Ripple 0.5Vp-p VIN = Set Vout + 1V		70		dB
Vin	Input Voltage		2.0		8.0	V
ΔVουτ/ ΔTopt	Output Voltage Temperature Coefficient	$ lout = 30mA  -40°C \le Topt \le 85°C $		±100		ppm/°C
Isc	Short Current Limit	Vout = 0V		50		mA
Rpd	CE Pull-down Resistance		2.5	5.0	10.0	МΩ
VCEH	CE Input Voltage "H"		1.5		Vin	V
VCEL	CE Input Voltage "L"		0.00		0.25	V
en	Output Noise	BW=10Hz to 100kHz		30		μVrms

#### RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

# **ELECTRICAL CHARACTERISTICS by OUTPUT VOLTAGE**

Topt = 25℃

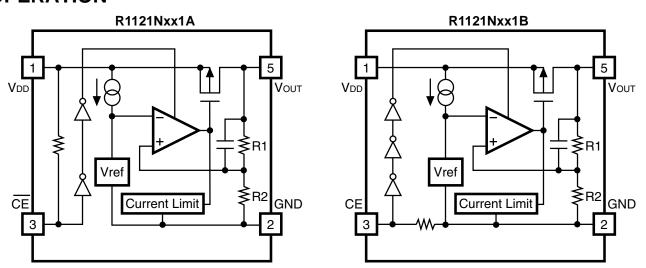
Output Voltage	Output Current	·
Vout (V)	Iουτ (mA)	
	Condition	Min.
1.5 ≦ Vout ≦ 1.7	VIN - VOUT = 1.0V	100
1.8 ≤ Vout ≤ 5.0		150

Topt = 25℃

Output Valtage	Dropout Voltage				
Output Voltage Vουτ (V)	V <sub>DIF</sub> (V)				
1001 (1)	Condition	Min.	Тур.	Max.	
1.5		0.50			
1.6		0.40			
1.7	Iоит = 100mA	0.30			
1.8 ≦ Vout ≦ 1.9			0.60	1.40	
$2.0 \leq V_{\text{OUT}} \leq 2.4$			0.35	0.70	
$2.5 \leq V$ out $\leq 2.7$			0.24	0.35	
$2.8 \leq V_{\text{OUT}} \leq 3.3$			0.20	0.30	
$3.4 \leq V_{\text{OUT}} \leq 5.0$			0.17	0.26	

Note : When set Output Voltage is equal or less than 2.0V,  $V_{\text{IN}}$  should be equal or more than 2.0V.

#### **OPERATION**



In these ICs, fluctuation of output voltage, VouT is detected by feed-back registers R1, R2, and the result is compared with a reference voltage by the error amplifier, so that a constant voltage is output.

A current limit circuit for protection in short mode and a chip enable circuit, are included.

# **TEST CIRCUITS**

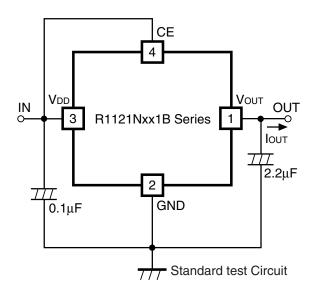
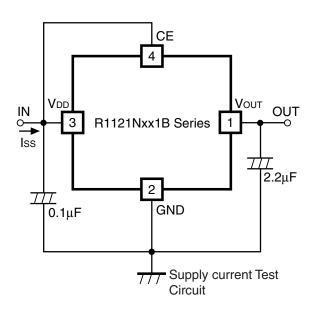


Fig.1 Standard test Circuit



**Fig.2 Supply Current Test Circuit** 

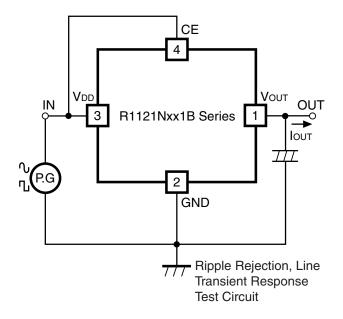


Fig.3 Ripple Rejection, Line Transient Response Test Circuit

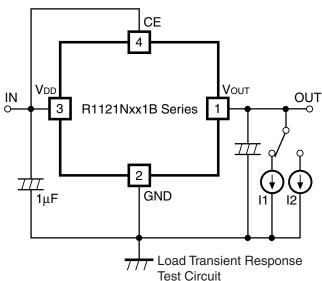
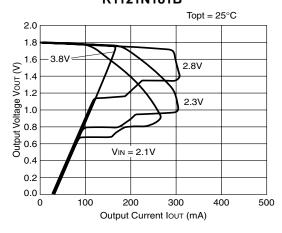
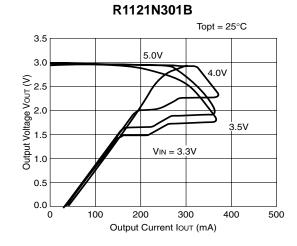


Fig.4 Load Transient Response Test Circuit

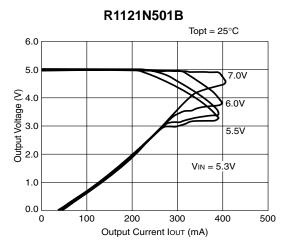
# **TYPICAL CHARACTERISTICS**

# 1) Output Voltage vs. Output Current R1121N181B

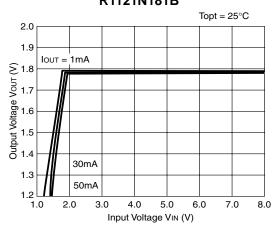


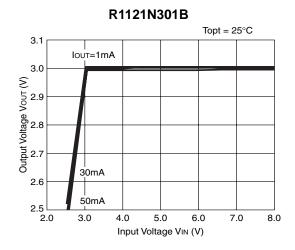


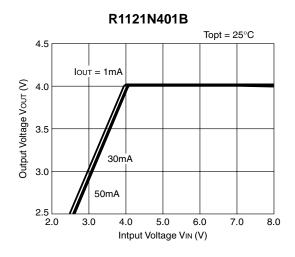
# R1121N401B Topt = 25°C 4.5 4.0 6.0V 3.5 3.0 98 2.5 1.0 0.5 0.0 Output Current lout (mA)

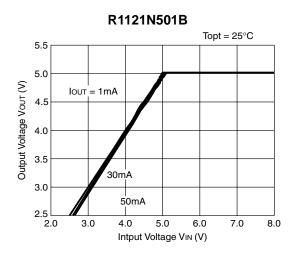


# 2) Output Voltage vs. Input Voltage R1121N181B

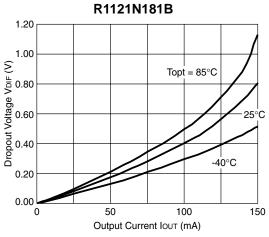


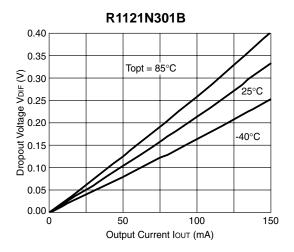


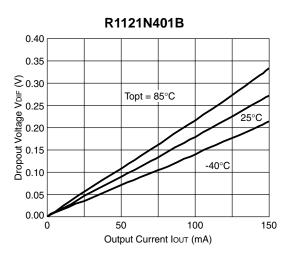


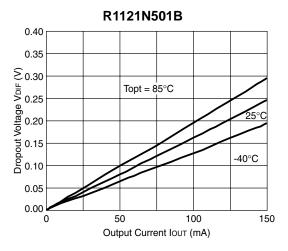


# 3) Dropout Voltage vs. Output Current



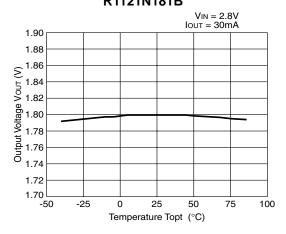




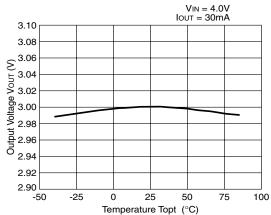


#### R1121N

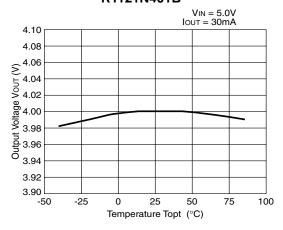
# 4) Output Voltage vs. Temperature R1121N181B



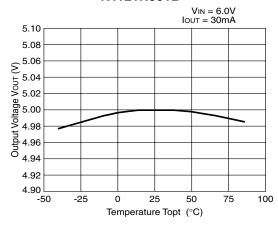
## R1121N301B



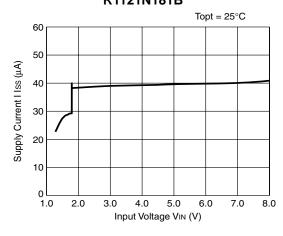
#### R1121N401B



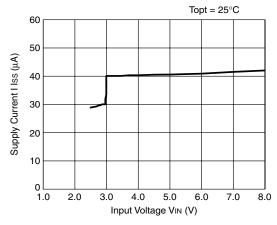
#### R1121N501B

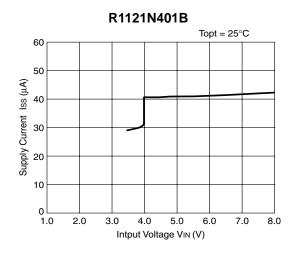


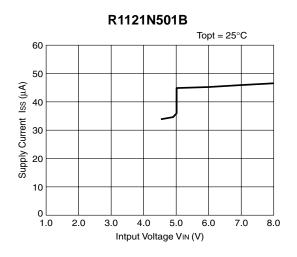
# 5) Supply Current vs. Input Voltage R1121N181B



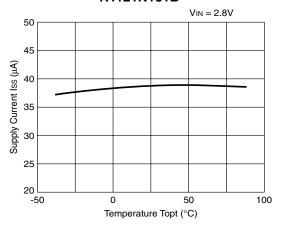
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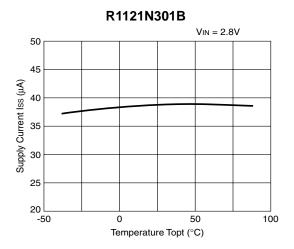


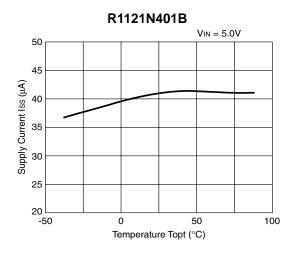


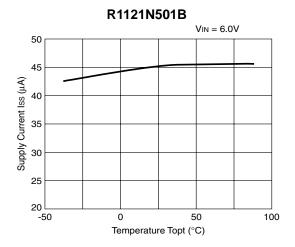


# 6) Supply Current vs. Temperature R1121N181B

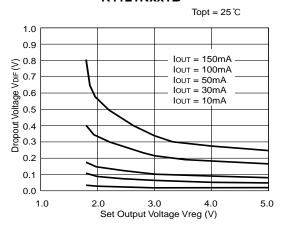








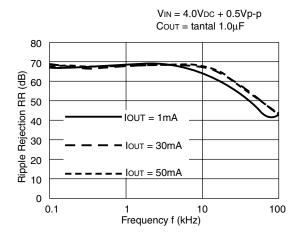
# 7) Dropout Voltage vs. Set Output Voltage R1121Nxx1B



#### 8) Ripple Rejection vs. Frequency R1121N181B

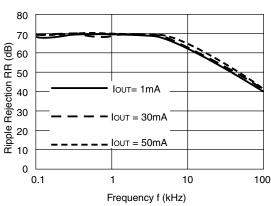
 $\begin{array}{c} \text{Vin} = 2.8 \text{Vdc} + 0.5 \text{Vp-p} \\ \text{Cout} = \text{tantal } 1.0 \mu\text{F} \\ \\ 80 \\ \hline 70 \\ \hline 80 \\ \hline 70 \\ \hline 80 \\ \hline 60 \\ \hline \hline 80 \\ \hline 50 \\ \hline \hline 90 \\ \hline 60 \\ \hline \hline 90 \\ \hline 100 \\$ 

#### R1121N301B



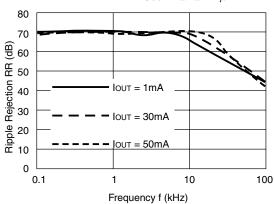
#### R1121N181B

VIN = 2.8 VDC + 0.5 Vp-p  $C\text{OUT} = tantal \ 2.2 \mu F$ 



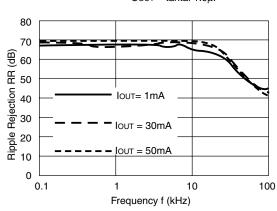
#### R1121N301B

VIN = 4.0VDC + 0.5Vp-p COUT = tantal 2.2μF



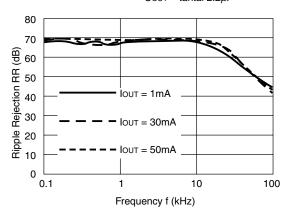
#### R1121N401B

VIN = 5.0VDC + 0.5Vp-p $COUT = tantal 1.0\mu F$ 



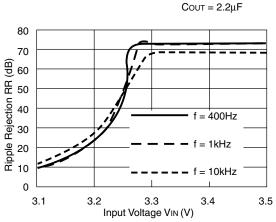
#### R1121N401B

 $V_{\text{IN}} = 5.0 V_{\text{DC}} + 0.5 V_{\text{p-p}}$   $C_{\text{OUT}} = tantal~2.2 \mu F$ 



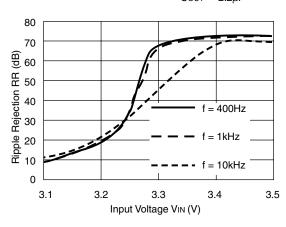
# 9) Ripple Rejection vs. Input Voltage (DC bias) R1121N301B

IOUT = 1mA



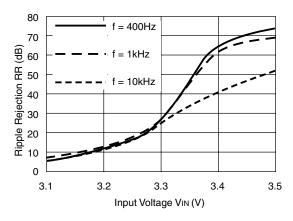
#### R1121N301B

IOUT = 10mA $COUT = 2.2\mu F$ 



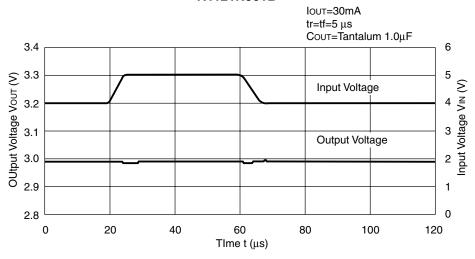
#### R1121N301B

 $\begin{array}{l} \text{IOUT} = 50 \text{mA} \\ \text{COUT} = 2.2 \mu \text{F} \end{array}$ 

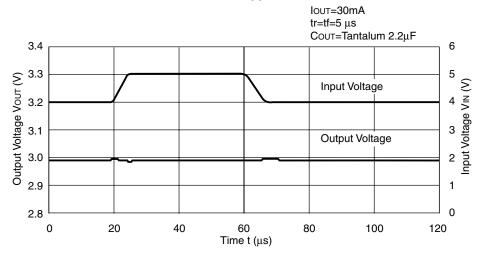


## 10) LineTransient Response

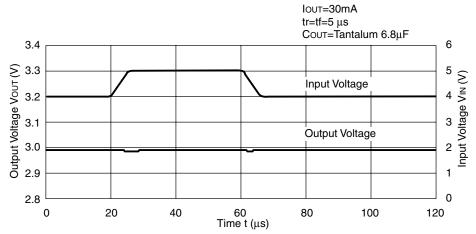
### R1121N301B



#### R1121N301B

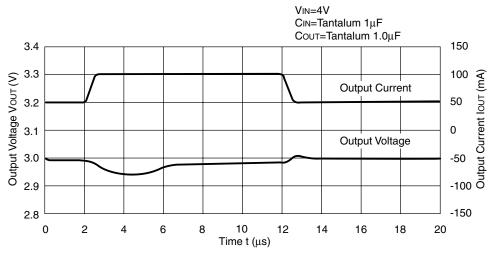


#### R1121N301B

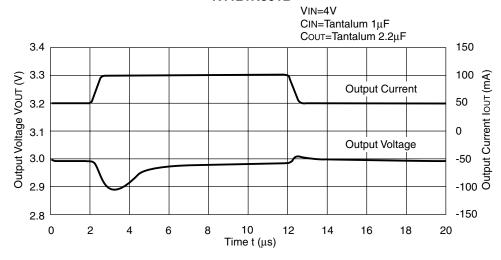


#### 11) Load Transient Response

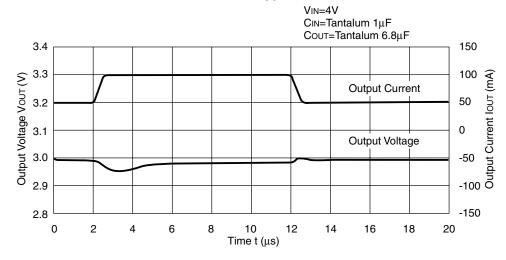
#### R1121N301B



#### R1121N301B



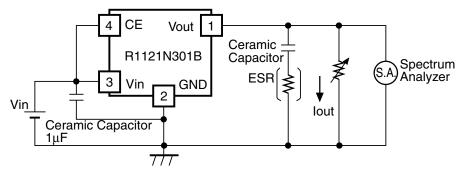
#### R1121N301B



#### **TECHNICAL NOTES**

When using these ICs, be sure to consider the following points:

In these ICs, phase compensation is made for securing stable operation even if the load current is varied. For this purpose, be sure to use a capacitor Cout with good frequency characteristics and ESR (Equivalent Series Resistance) in the range described as follows:



Measuring Circuit for white noise; R1121N301B

The relationship between IouT (output current) and ESR of output capacitor is shown in the graphs below. The conditions when the white noise level is under 40mV (Avg.) are indicated by the hatched area in the graph.

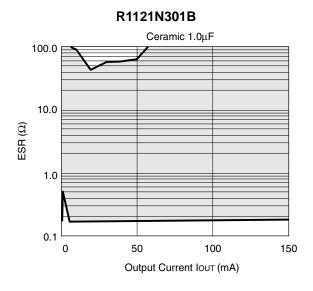
(note: When the additional ceramic capacitors are connected to the output pin with output capacitor for phase compensation, the operation might be unstable. Because of this, test these ICs with as the same external components as the ones to be used on the PCB.)

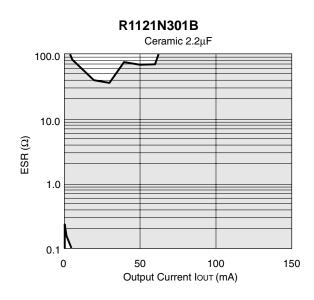
#### <Measurement conditions>

(1) V<sub>IN</sub>=4V

(2) Frequency Band: 10Hz to 1MHz

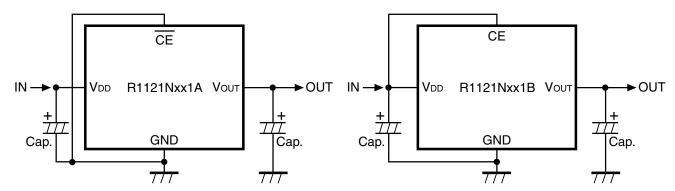
(3) Temperature: 25°C





- · Make V<sub>DD</sub> and GND lines sufficient. If their impedance is high, noise pick up or incorrect operation may result.
- · Connect the capacitor with a capacitance of  $1\mu F$  or more between  $V_{DD}$  and GND as close as possible.
- · Set external components, especially the output capacitor, as close as possible to the ICs and make wiring as short as possible.

# **TYPICAL APPLICATION**





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# RICOH COMPANY., LTD. Electronic Devices Company



■Ricoh presented with the Japan Management Quality Award for 1999. Ricoh continually strives to promote customer satisfaction, and shares the achievements of its management quality improvement program with people and society



■Ricoh awarded ISO 14001 certification.

The Ricoh Group was awarded ISO 14001 certification, which is an international standard for environmental management systems, at both its domestic and overseas production facilities. Our current aim is to obtain ISO 14001 certification for all of our business offices.

## http://www.ricoh.com/LSI/

RICOH COMPANY, LTD. **Electronic Devices Company** 

 Higashi-Shinagawa Office (International Sales) 3-32-3, Higashi-Shinagawa, Shinagawa-ku, Tokyo 14 Phone: +81-3-5479-2857 Fax: +81-3-5479-0502

RICOH EUROPE (NETHERLANDS) B.V.

 Semiconductor Support Centre
 Prof. W.H.Keesomlaan 1, 1183 DL Amstelveen, The Netherlands Prof. W.H.Keesomlaan 1, 1183 DL Amstelveen, The P.O.Box 114, 1180 AC Amstelveen Phone: +31-20-5474-309 Fax: +31-20-5474-791

RICOH ELECTRONIC DEVICES KOREA Co., Ltd. 11 floor, Haesung 1 building, 942, Daechidong, Gangnamgu, Seoul, Kore Phone: +82-2-2135-5700 Fax: +82-2-2135-5705

RICOH ELECTRONIC DEVICES SHANGHAI Co., Ltd. Room403, No.2 Building, 690#Bi Bo Road, Pu Dong New district, Shanghai 201203, People's Republic of China Phone: +86-21-5027-3200 Fax: +86-21-5027-3299

RICOH COMPANY, LTD. Electronic Devices Company Taipei office

Room109, 10F-1, No.51, Hengyang Rd., Taipei City, Taiwan (R.O.C.) Phone: +886-2-2313-1621/1622 Fax: +886-2-2313-1623



Ricoh completed the organization of the Lead-free production for all of our products. After Apr. 1, 2006, we will ship out the lead free products only. Thus, all products that will be shipped from now on comply with RoHS Directive.