

Provisional Data

Insulated Gate Bi-Polar Transistor Type T0900EB45A

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{CES}	Collector – emitter voltage	4500	V
$V_{DC\ link}$	Permanent DC voltage for 100 FIT failure rate.	2800	V
V_{GES}	Peak gate – emitter voltage	±20	V

	RATINGS	MAXIMUM LIMITS	UNITS
$I_{C(DC)}$	Continuous DC collector current, IGBT	900	A
I_{CRM}	Repetitive peak collector current, $t_p=1ms$, IGBT	1800	A
$I_{F(DC)}$	Continuous DC forward current, Diode	900	A
I_{FRM}	Repetitive peak forward current, $t_p=1ms$, Diode	1800	A
I_{FSM}	Peak non-repetitive surge $t_p=10ms$, $V_{RM}=60\%V_{RRM}$, Diode (Note 4)	14.2	kA
I_{FSM2}	Peak non-repetitive surge $t_p=10ms$, $V_{RM}\leq 10V$, Diode (Note 4)	15.6	kA
P_{MAX}	Maximum power dissipation, IGBT (Note 2)	7.1	kW
$(di/dt)_{cr}$	Critical diode di/dt (note 3)	2500	A/ μs
T_j	Operating temperature range	-40 to +125	°C
T_{stg}	Storage temperature range.	-40 to +125	°C

Notes: -

- 1) Unless otherwise indicated $T_j = 125^\circ C$
- 2) $T_{sink} = 25^\circ C$, double side cooled.
- 3) Maximum commutation loop inductance 250nH.
- 4) Half-sinewave, $125^\circ C$ T_j initial.

Characteristics

IGBT Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
V _{CE(sat)}	Collector – emitter saturation voltage	-	3.05	3.40	I _C = 900A, V _{GE} = 15V, T _j = 25°C	V
		-	3.80	4.20	I _C = 900A, V _{GE} = 15V	V
V _{T0}	Threshold voltage	-	-	1.73	Current range: 400 – 1200A	V
r _T	Slope resistance	-	-	2.68		mΩ
V _{GE(TH)}	Gate threshold voltage	-	5.3	-	V _{CE} = V _{GE} , I _C = 90mA	V
I _{CES}	Collector – emitter cut-off current	-	10	30	V _{CE} = V _{CES} , V _{GE} = 0V	mA
I _{GES}	Gate leakage current	-	-	±10	V _{GE} = ±20V	μA
C _{ies}	Input capacitance	-	150	-	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz	nF
t _{d(on)}	Turn-on delay time	-	2.2	-	I _C = 900A, V _{CE} = 2800V, V _{GE} = ±15V, L _S = 250nH R _{g(ON)} = 6.6Ω, R _{g(OFF)} = 5.0Ω, C _{GE} = 100nF Integral diode used as freewheel diode (Note 3)	μs
t _{r(l)}	Rise time	-	3.4	-		μs
Q _{g(on)}	Turn-on gate charge	-	5	-		μC
E _{on}	Turn-on energy	-	4.3	-		J
t _{d(off)}	Turn-off delay time	-	1.9	-		μs
t _f	Fall time	-	2.4	-		μs
Q _{g(off)}	Turn-off gate charge	-	10	-		μC
E _{off}	Turn-off energy	-	3.6	-		J
t _{d(on)}	Turn-on delay time	-	2.4	-	I _C = 900A, V _{CE} = 2800V, V _{GE} = ±15V, L _S = 250nH R _{g(ON)} = 6.6Ω, R _{g(OFF)} = 5.0Ω, C _{GE} = 100nF Free wheel diode type E900NC450 (Note 3)	μs
t _{r(l)}	Rise time	-	3.2	-		μs
Q _{g(on)}	Turn-on gate charge	-	5	-		μC
E _{on}	Turn-on energy	-	3.8	-		J
t _{d(off)}	Turn-off delay time	-	1.9	-		μs
t _f	Fall time	-	2.4	-		μs
Q _{g(off)}	Turn-off gate charge	-	10	-		μC
E _{off}	Turn-off energy	-	3.6	-		J
I _{SC}	Short circuit current	-	3400	-	V _{GE} = +15V, V _{CC} = 2800V, V _{CEmax} ≤ V _{CES} , t _p ≤ 10μs	A

Diode Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
V _F	Forward voltage	-	3.4	3.7	I _F = 900A, T _j = 25°C	V
		-	3.9	4.2	I _F = 900A	V
V _{To}	Threshold voltage	-	-	2.43	Current range 400-1200A	V
r _T	Slope resistance	-	-	1.86		mΩ
I _{rm}	Peak reverse recovery current	-	610	-	I _F = 900A, V _{GE} = ±15V, di/dt = 2000A/μs	A
Q _{rr}	Recovered charge	-	920	-		μC
t _{rr}	Reverse recovery time, 50% chord	-	2.3	-		μs
E _r	Reverse recovery energy	-	0.9	-		J

Thermal Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
R _{thJK}	Thermal resistance junction to sink, IGBT	-	-	14	Double side cooled	K/kW
		-	-	23	Collector side cooled	K/kW
		-	-	35	Emitter side cooled	K/kW
R _{thJK}	Thermal resistance junction to sink, Diode	-	-	26	Double side cooled	K/kW
		-	-	41	Cathode side cooled	K/kW
		-	-	78	Anode side cooled	K/kW
F	Mounting force	25	-	35	Note 2	kN
W _t	Weight	-	1.2	-		kg

Notes:-

- 1) Unless otherwise indicated T_j=125°C.
- 2) Consult application note 2008AN01 for detailed mounting requirements
- 3) C_{GE} is additional gate – emitter capacitance added to output of gate drive

Curves

Figure 1 – Typical collector-emitter saturation voltage characteristics

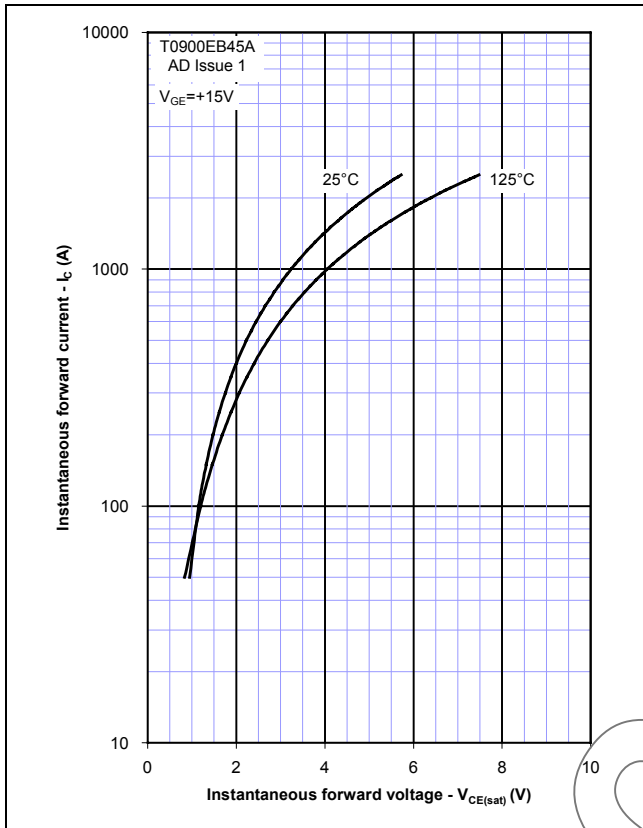


Figure 2 – Typical output characteristic

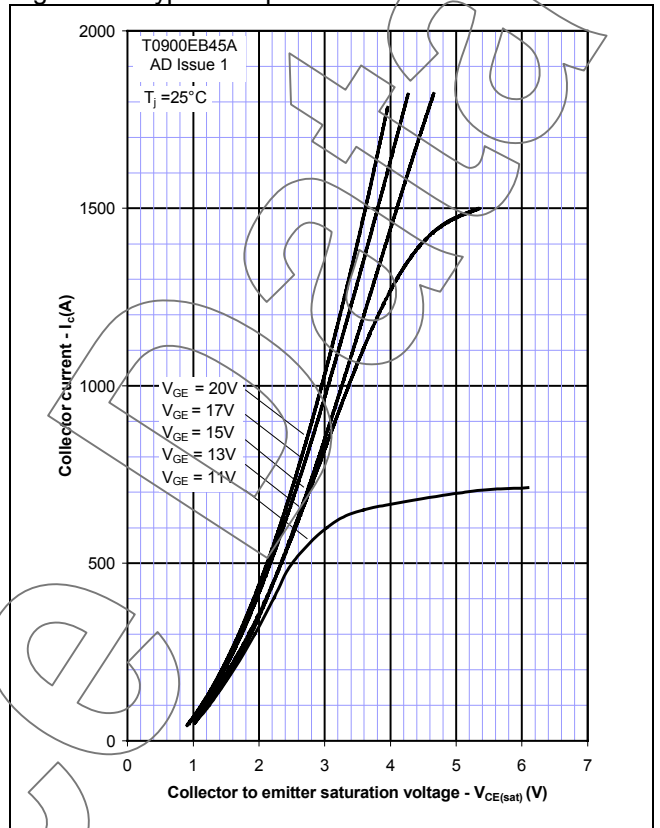


Figure 3 – Typical output characteristic

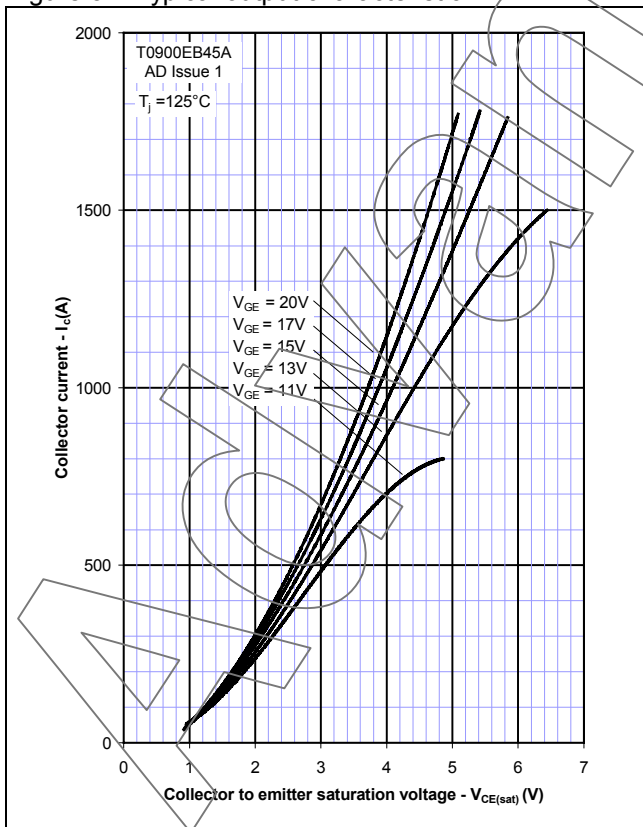


Figure 4 – Typical turn-on delay time vs gate resistance

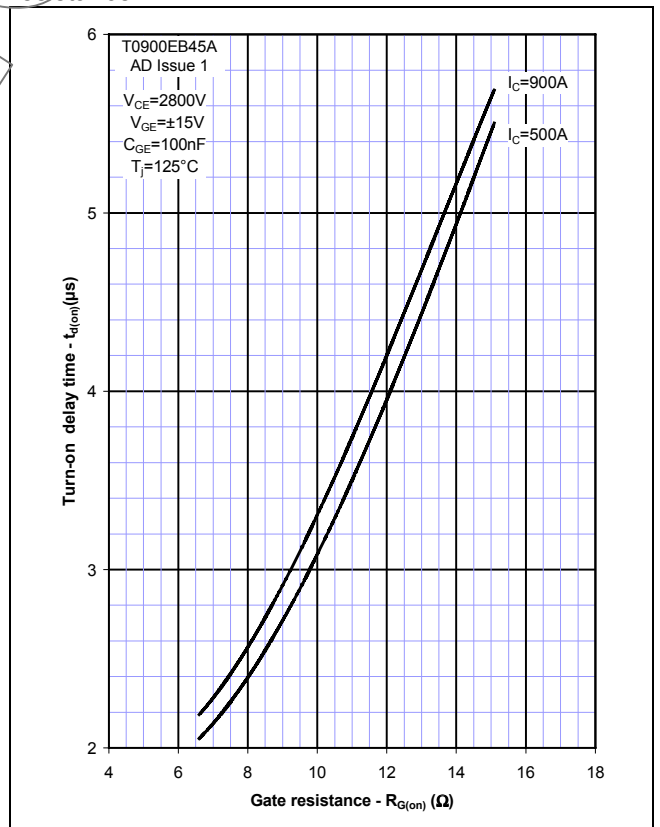


Figure 5 – Typical turn-off delay time vs. gate resistance

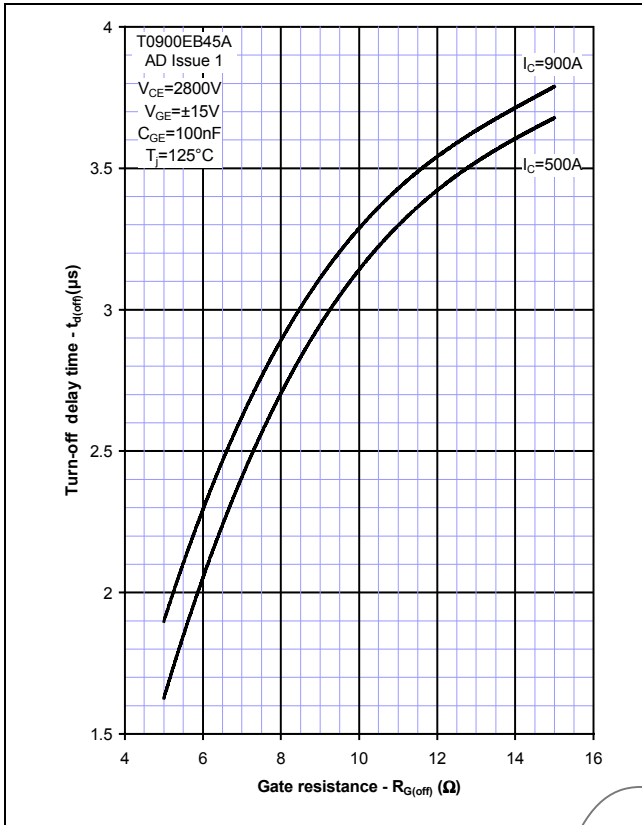


Figure 6 – Typical turn-on energy vs. collector current

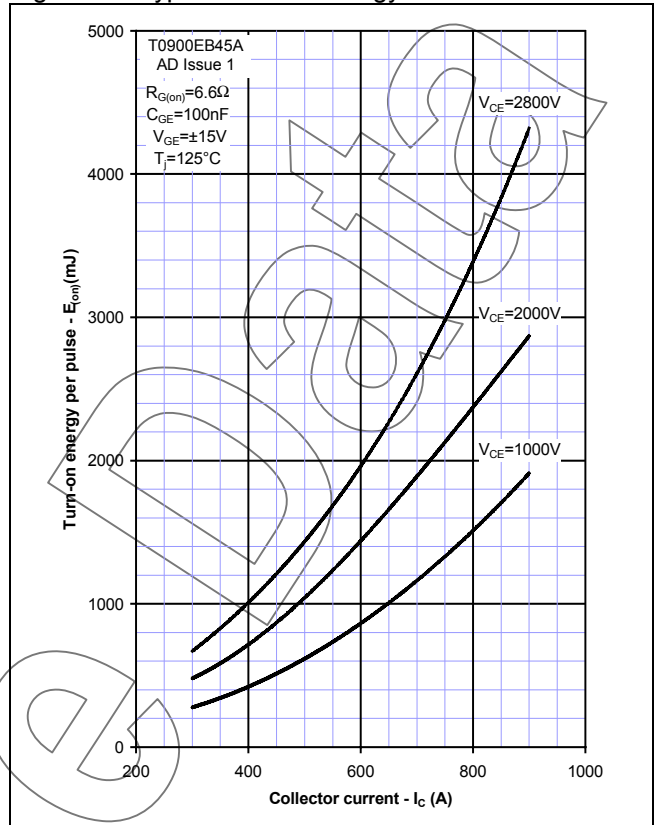


Figure 7 – Typical turn-on energy vs. di/dt

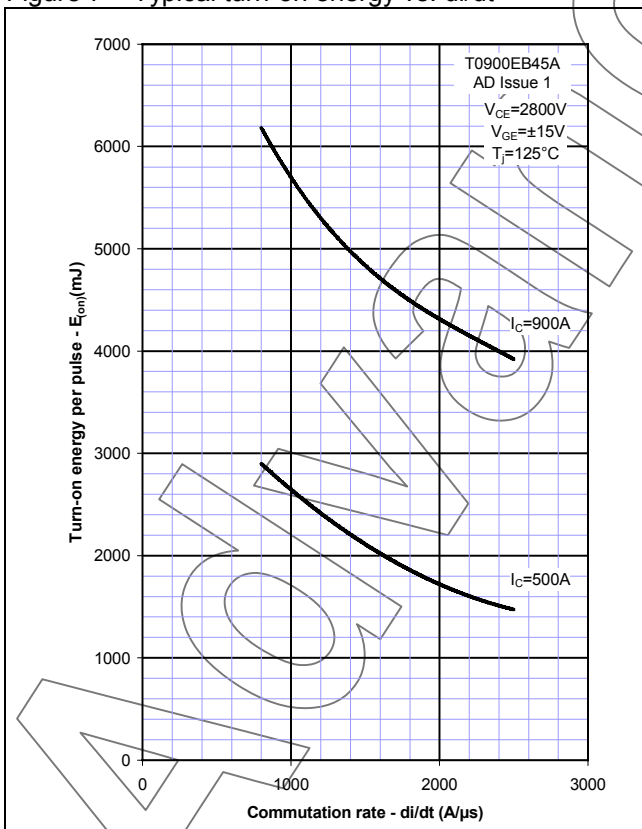


Figure 8 – Typical turn-off energy vs. collector current

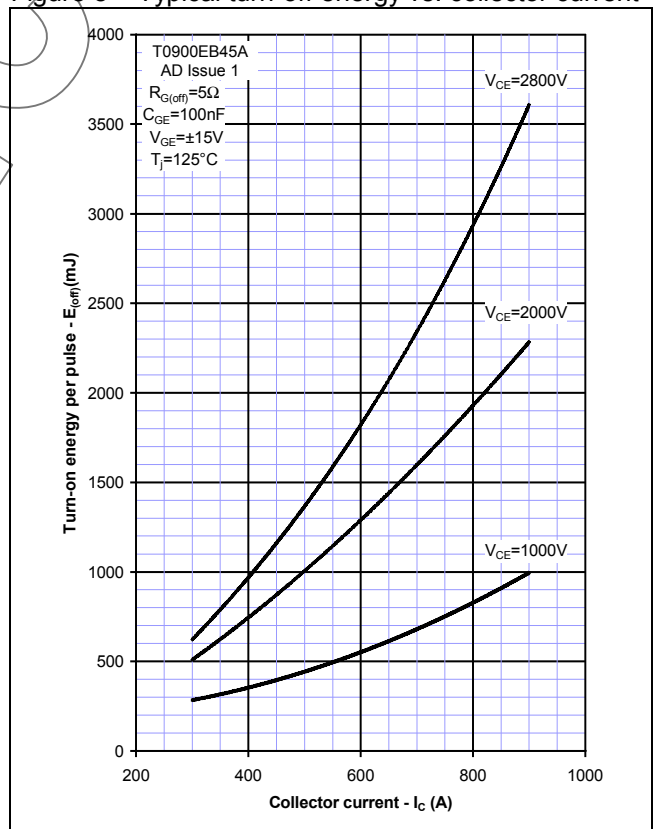


Figure 9 – Turn-off energy vs voltage

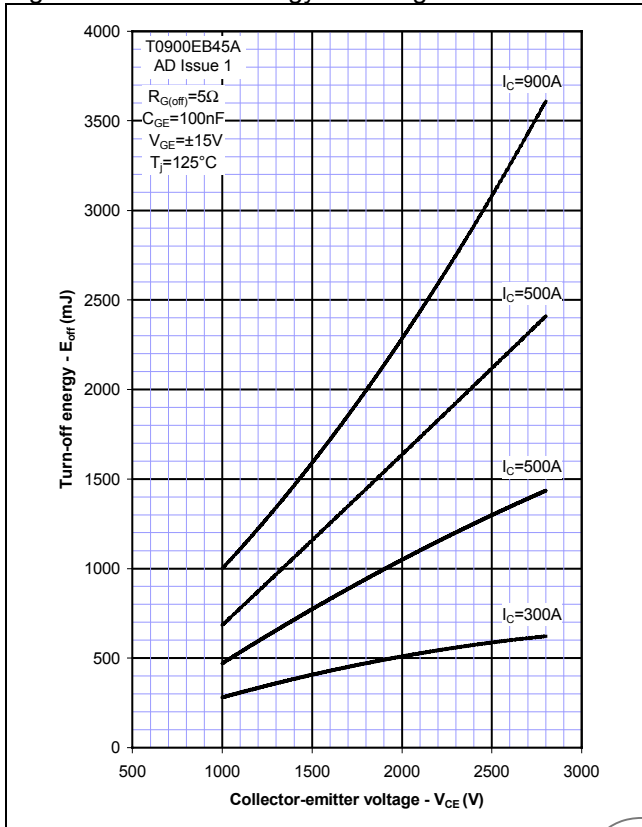


Figure 10 – Safe operating area

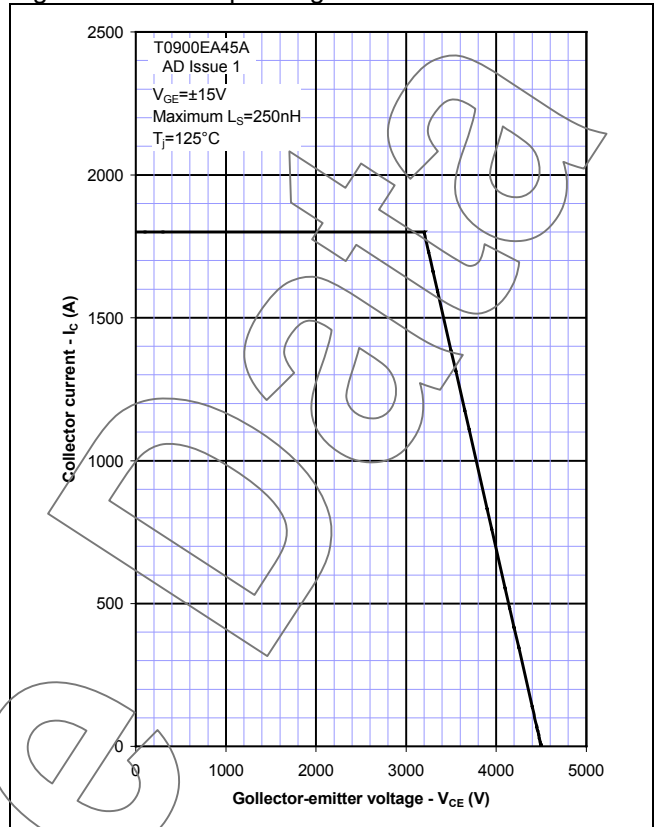


Figure 11 – Typical diode forward characteristic

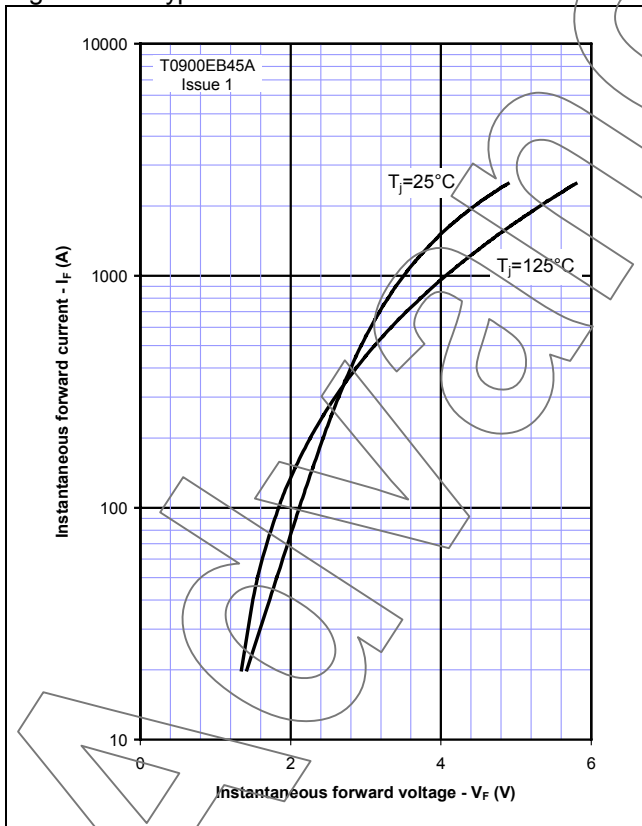


Figure 12 – Typical recovered charge

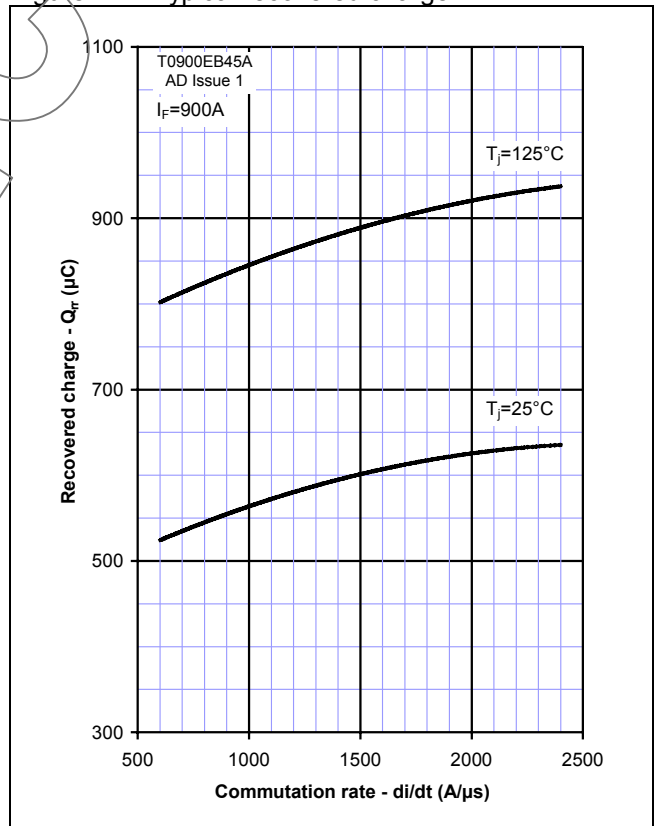


Figure 13 – Typical reverse recovery current

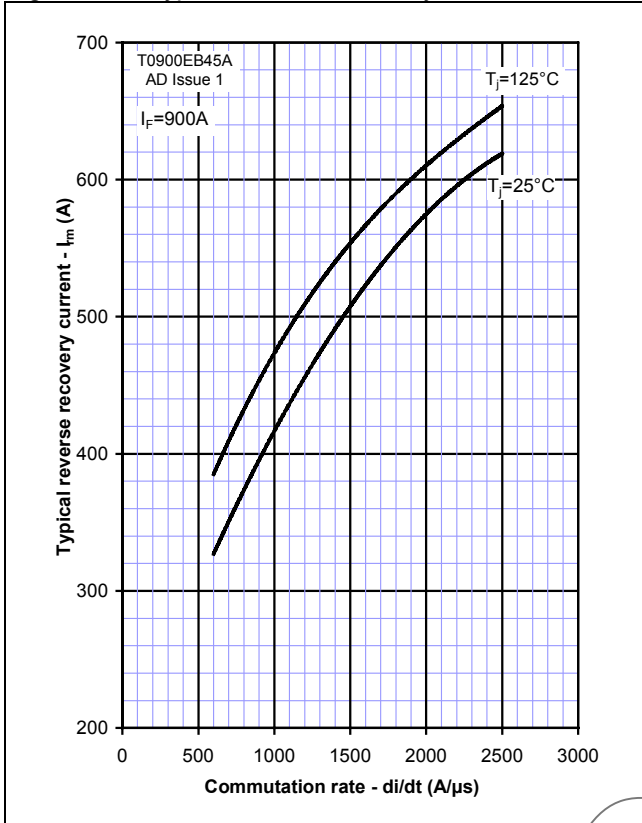


Figure 14 – Typical reverse recovery time

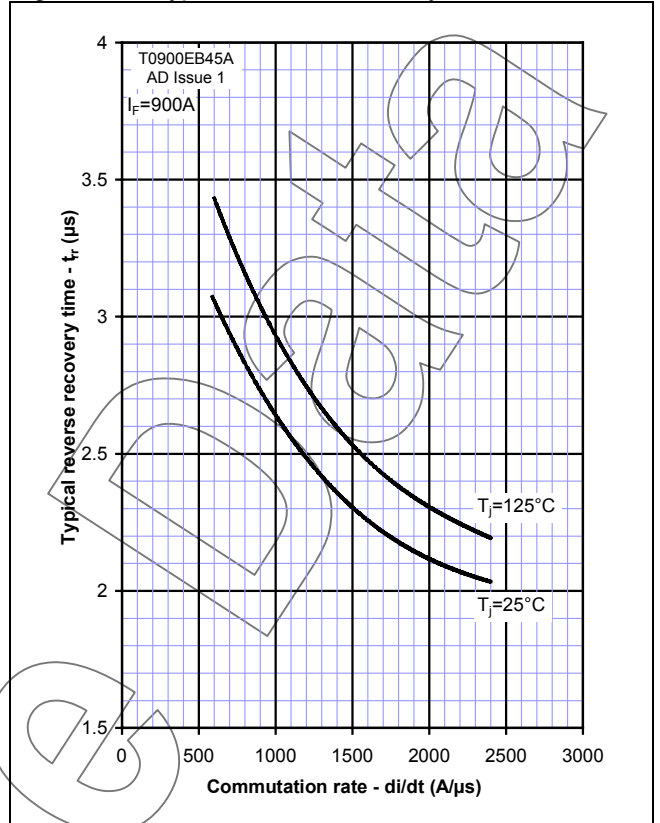


Figure 15 – Transient thermal impedance (IGBT)

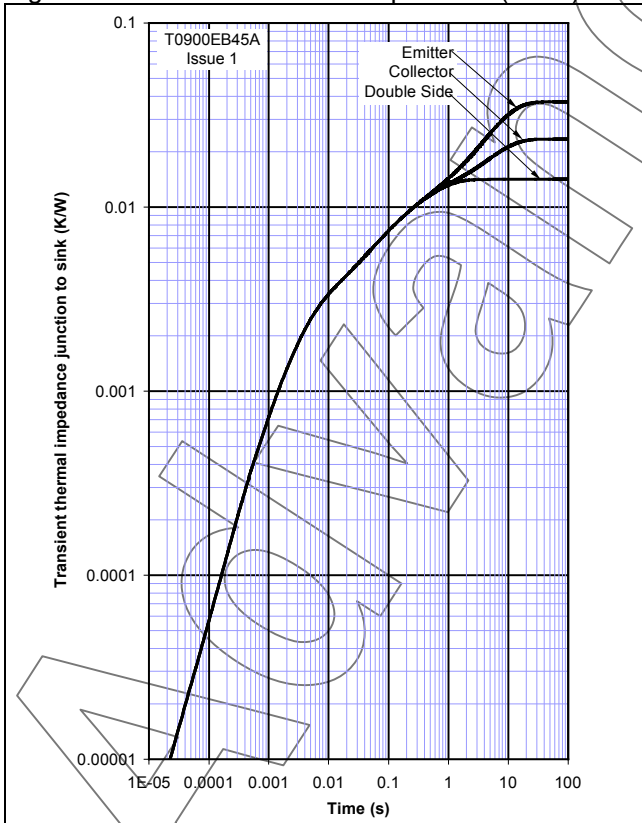
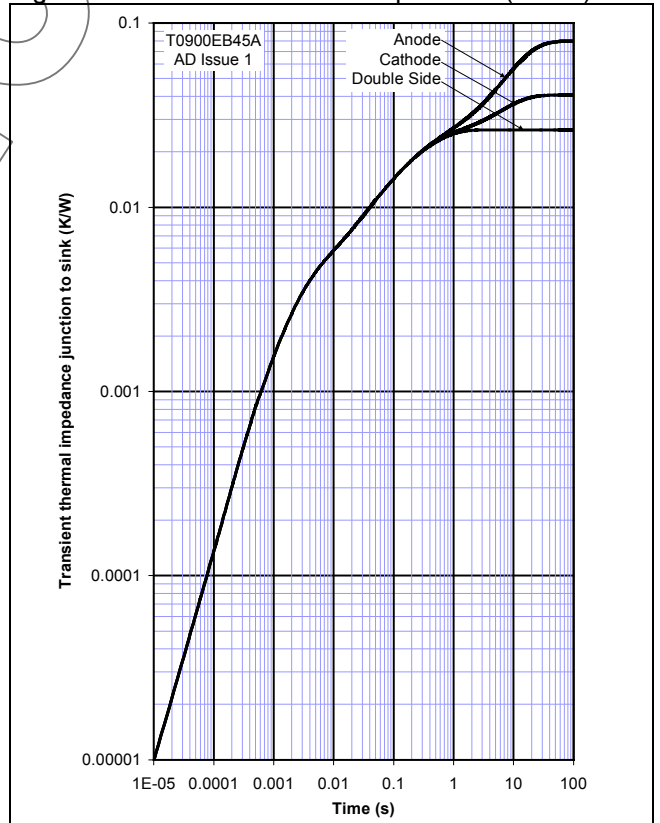
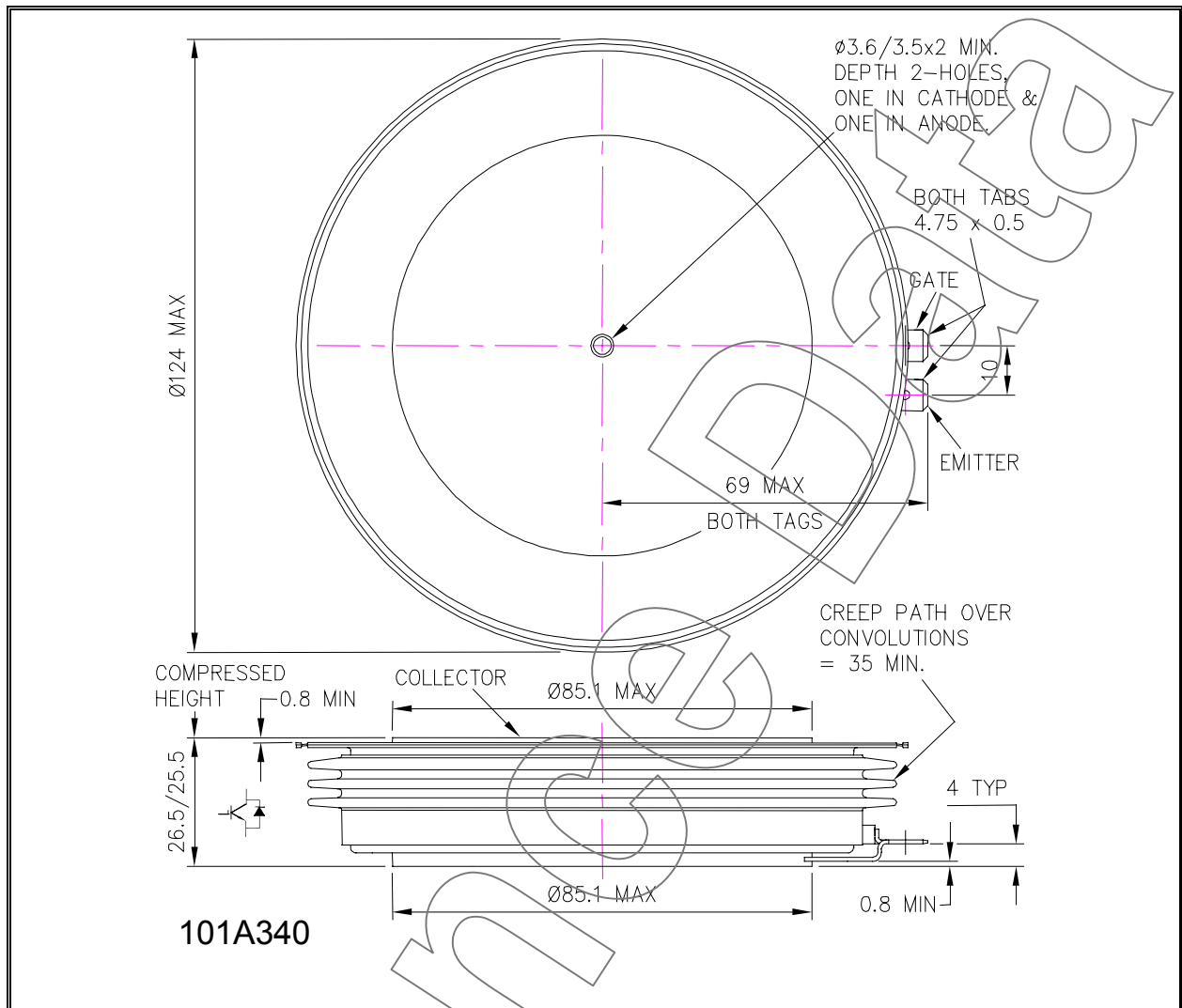


Figure 16 – Transient thermal impedance (Diode)



Outline Drawing & Ordering Information



ORDERING INFORMATION

(Please quote 10 digit code as below)

T0900 Fixed type Code	EB Fixed Outline Code	45 Voltage Grade 4500	A Fixed format code
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Typical order code: T0900EB45A ($V_{CES} = 4500V$)

IXYS Semiconductor GmbH
Edisonstraße 15
D-68623 Lampertheim
Tel: +49 6206 503-0
Fax: +49 6206 503-627
E-mail: marcom@ixys.de

WESTCODE

An IXYS Company

Westcode Semiconductors Ltd
Langley Park Way, Langley Park,
Chippenham, Wiltshire, SN15 1GE.
Tel: +44 (0)1249 444524
Fax: +44 (0)1249 659448
E-mail: WSL.sales@westcode.com

IXYS Corporation
1590 Buckeye Drive
Milpitas CA 95035-7418
Tel: +1 (408) 457-9000
Fax: +1 (408) 496 0670
E-mail: sales@ixys.net

www.westcode.com

www.ixys.net

IXYS Long Beach
IXYS Long Beach, Inc
2500 Mira Mar Ave, Long Beach
CA 90815
Tel: +1 (562) 296 6584
Fax: +1 (562) 296 6585
E-mail: service@ixyslongbeach.com

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