

# Phase Control Thyristor Stud Types N0180S#120 to N0180S#160

The data sheet on the subsequent pages of this document is a scanned copy of existing data for this product.  
(Rating Report 86TR12 Issue 1)

This data reflects the old part number for this product which is: N105PH12-16.  
This part number must **NOT** be used for ordering purposes – please use the ordering particulars detailed below.

The limitations of this data are as follows:  
No reverse recovery data available

The following links will direct you to the appropriate outline drawings  
[Outline W16](#) – ½” ceramic stud + lug  
[Outline W17](#) – ½” ceramic stud

Where any information on the product matrix page differs from that in the following data, the product matrix must be considered correct

An electronic data sheet for this product is presently in preparation.

For further information on this product, please contact your local ASM or distributor.

Alternatively, please contact Westcode as detailed below.

<b>Ordering Particulars</b>			
N0180	S#	◆◆	0
Fixed Type Code	SH – ½” ceramic stud SJ – ½” ceramic stud + lug	Voltage code V <sub>RRM</sub> /100 12-16	Fixed Code
Typical Order Code: N0180SJ120, ½” ceramic stud + lug, 1200V V <sub>RRM</sub> /V <sub>DRM</sub>			

<p><b>IXYS Semiconductor GmbH</b> Edisonstraße 15 D-68623 Lampertheim Tel: +49 6206 503-0 Fax: +49 6206 503-627 E-mail: <a href="mailto:marcom@ixys.de">marcom@ixys.de</a></p>	<p>An  IXYS Company</p> <p style="color: blue; text-decoration: underline;"><a href="http://www.westcode.com">www.westcode.com</a></p> <p style="color: blue; text-decoration: underline;"><a href="http://www.ixys.com">www.ixys.com</a></p>	<p><b>Westcode Semiconductors Ltd</b> Langley Park Way, Langley Park, Chippenham, Wiltshire, SN15 1GE. Tel: +44 (0)1249 444524 Fax: +44 (0)1249 659448 E-mail: <a href="mailto:WSL.sales@westcode.com">WSL.sales@westcode.com</a></p>	
<p><b>IXYS Corporation</b> 3540 Bassett Street Santa Clara CA 95054 USA Tel: +1 (408) 982 0700 Fax: +1 (408) 496 0670 E-mail: <a href="mailto:sales@ixys.net">sales@ixys.net</a></p>		<p><b>Westcode Semiconductors Inc</b> 3270 Cherry Avenue Long Beach CA 90807 USA Tel: +1 (562) 595 6971 Fax: +1 (562) 595 8182 E-mail: <a href="mailto:WSI.sales@westcode.com">WSI.sales@westcode.com</a></p>	
<p>The information contained herein is confidential and is protected by Copyright. The information may not be used or disclosed except with the written permission of and in the manner permitted by the proprietors Westcode Semiconductors Ltd.</p> <p>In the interest of product improvement, Westcode reserves the right to change specifications at any time without prior notice.</p> <p>Devices with a suffix code (2-letter, 3-letter or letter/digit/letter combination) added to their generic code are not necessarily subject to the conditions and limits contained in this report.</p>			<p>© Westcode Semiconductors Ltd.</p>

QUALITY EVALUATION LABORATORY

Rating Report: 86TR12  
Origin:

Date : 26th August, 1986  
Pages : 13

Thyristor Types N105PH02-H15 & N105RH02-H15

Written: *M.N. Dunlop*

Checked: *M(NW)*

Approved: *[Signature]*

These thyristors have diffused silicon slices of 19 mm diameter mounted under spring pressure in stud base, top hat housings with or without a flexible lead. This Report supersedes Rating Report No. 79TR25 (Issue 2).

Ratings

Voltage Grades	:	H02-H15
$V_{DSM}$	:	200-1500V
$V_{RSM}$	:	300-1600V
$V_{DRM}, V_{RRM}$	:	200-1500V
$I_T$ (AV) : Single phase : 50 Hz, 180° sinewave		
$T_{CASE} = 90^\circ C *$	:	110A
$I_T$ (rms) max.	:	175A
$I_T$ d.c. max.	:	175A
$I_{TSM}$ : t = 10ms half sinewave; $T_J$ (initial) = 125°C: $V_{RM} = 0.6V_{RRM}$ (MAX)	:	2450A
$I_{TSM}$ : t = 10ms half sinewave; $T_J$ (initial) = 125°C: $V_{RM} \leq 10V$	:	2695A
$I^2t$ : t = 10 ms; $T_J$ (initial) = 125°C : $V_{RM} = 0.6V_{RRM}$ (MAX)	:	$30.0 \times 10^3 A^2S$
$I^2t$ : t = 10 ms; $T_J$ (initial) = 125°C : $V_{RM} \leq 10V$	:	$36.3 \times 10^3 A^2S$
$I^2t$ : t = 3 ms; $T_J$ (initial) = 125°C : $V_{RM} \leq 10V$	:	$27.0 \times 10^3 A^2S$
di/dt : (Repetitive) $T_J = 125^\circ C$ Gate: 20 $\mu$ s Rise time 1 $\mu$ s	:	500A/ $\mu$ s
$I_{FGM}$ : Anode positive with respect to cathode	:	20A
$V_{FGM}$ : " " " " " "	:	18V
$V_{RGM}$ :	:	5V
$P_G$ (AV) :	:	2W
$P_{GM}$ :	:	100W
$V_{GD}$ :	:	0.25V
$T_C$ operating range	:	-40 to 125°C
$T_{stg}$ Non-operating	:	-40 to 150°C

\*Case temperature corresponding to current cut-off

Characteristics

(maximum values unless stated otherwise)

$I_{GT}$ : $T_J = 25^{\circ}C$ )		: 150mA
$I_H$ : $T_J = 25^{\circ}C$ )	$V_A = 6V ; I_A = 1A$	: 600mA
$V_{GT}$ : $T_J = 25^{\circ}C$ )		: 3V
$V_O$ : $T_J = 125^{\circ}C$		: 0.9V
$r_T$ : $T_J = 125^{\circ}C$		: 1.79mohms
$V_{TM}$ : $I_{TM} = 377A$ $T_{VJ} = 125^{\circ}C$		: 1.57V
$R_{th}$ (J/C)		: 0.23°C/W
dv/dt : Linear ramp to $0.8V_{DRM}(max)$ , $T_J = 125^{\circ}C$ ; Gate O/C; repetitive : 200V/uS*		
$I_{DRM}$ : $T_J = 125^{\circ}C$ $V_{DM} = V_{DRM}(max)$		: 20mA
$I_{RRM}$ : $T_J = 125^{\circ}C$ $V_{RM} = V_{RRM}(max)$		: 20mA
$Q_{RR}$ : $I_{TM} = 300A$ dI/dt    10 A/uS, 50% chord value		
	$V_{RM} : 50V$ $T_{VJ} = 125^{\circ}C$	: 280uC Typical
tq : $I_{TM}$ dI/dt    A/uS; $T_J = 125^{\circ}C$ $V_{RM} = 50V$		:
	dV/dt = 200V/uS to $0.8V_{DRM}$	:
	When specified, 20V/uS to $0.8V_{DRM}$ Typical	:
Outline drawings		: 101A231, 101A235
$R_{th}$ (C-H.S.)		: 0.08°C/W
Mounting torque		: 1.45Kg.f-m
Outline (JEDEC NO.)		:

\*Repetitive dv/dt

Higher dv/dt selections are available up to 1000V/uS on request.

CONTENTS

	<u>Page</u>
Ratings	1
Characteristics	2
Contents	3
Voltage Ratings	4
Dissipation and Heatsink Temperature vs Mean Current	5, 6
Limit On-state Characteristics	7
Transient Thermal Impedance Characteristic	8
Gate Characteristics and Gate Trigger Limits	9, 10
Surge Current and $I^2t$ vs Duration of Surge	11
Outline Drawings	12, 13

Changes to 79TR25(Issue 2)

p1: N105RHO2-H15 added

$V_{DWM}$ ,  $V_{RWM}$  deleted

$I_{FGM}$  increased to 20A

$T_{HS}$  - operating range MIN decreased to  $-40^{\circ}\text{C}$

p2: JEDEC No. deleted

Note 1 deleted; replaced by note on  $dv/dt$

p7:  $I_T - V_T, Z_{th-t}$  drawn on separate pages

Old p8:  $V_G - I_G$  re-drawn with  $I_{FGM} = 20\text{A}$

Old p9:  $I_{gt} - V_{gt}$  : axes interchanged

New p13 N105R outline drawing added

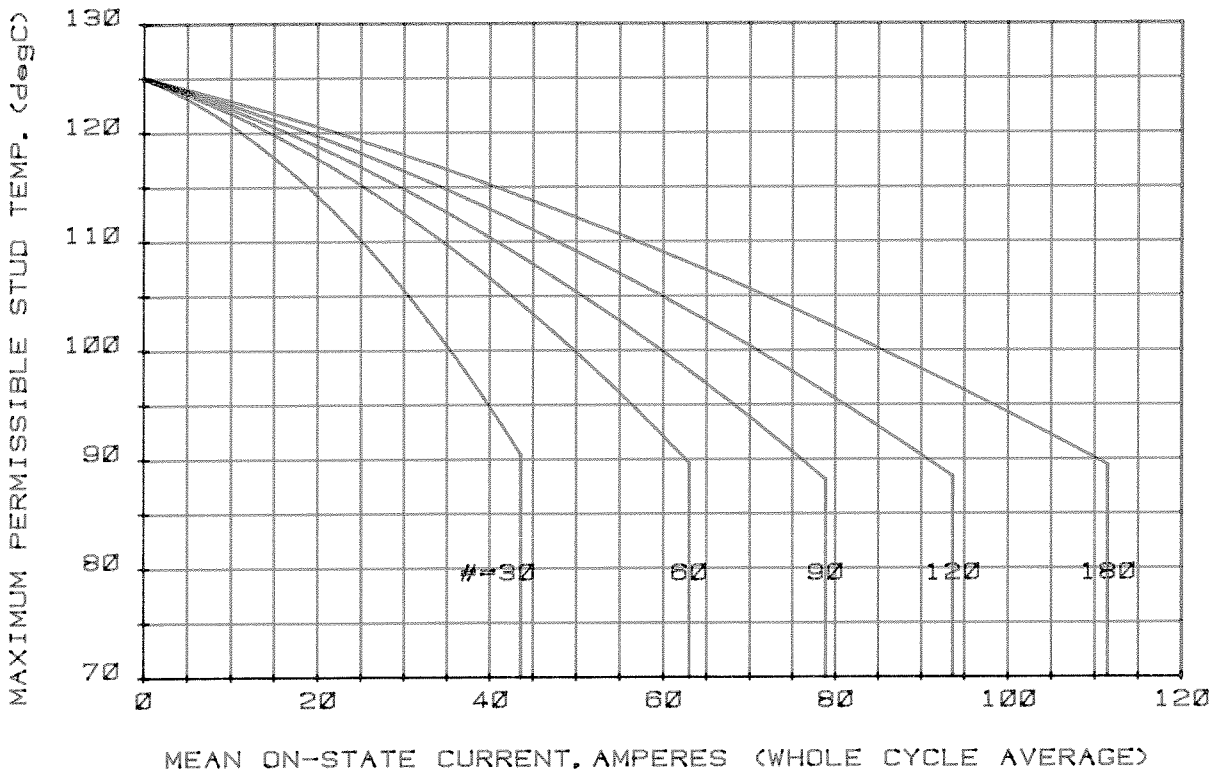
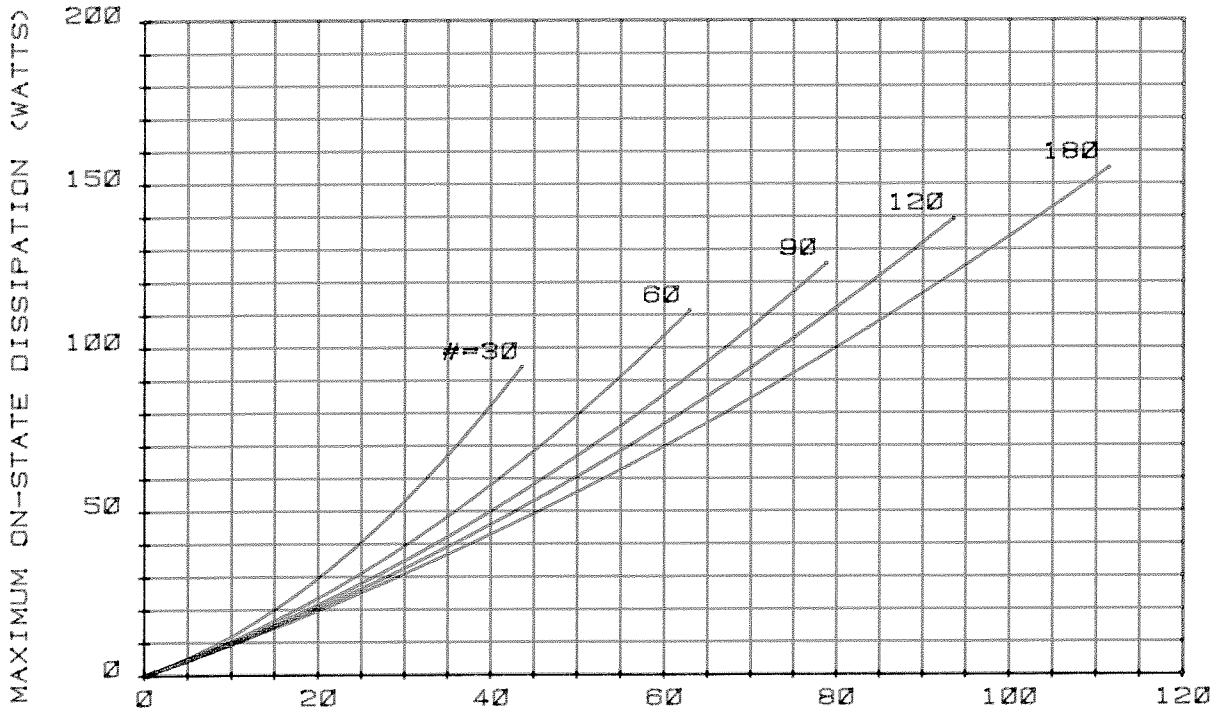
Voltage Ratings

Voltage Grade	$V_{DSM}$ $V_{DRM}$ $V_{RRM}$	$V_{RSM}$	$V_D$ $V_R$
'H'	V	V	DC
02	200	300	140
03	300	400	210
04	400	500	260
06	600	700	420
08	800	900	560
10	1000	1100	700
12	1200	1300	810
14	1400	1500	930
15	1500	1600	980

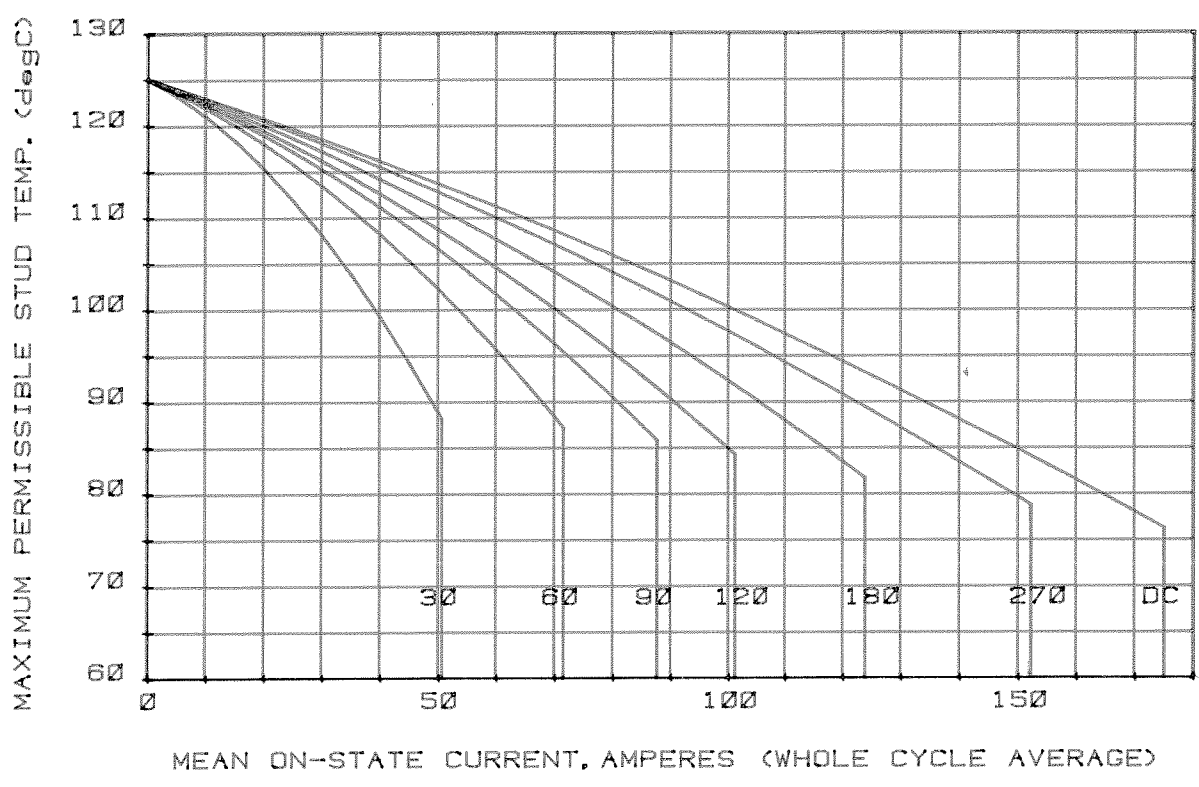
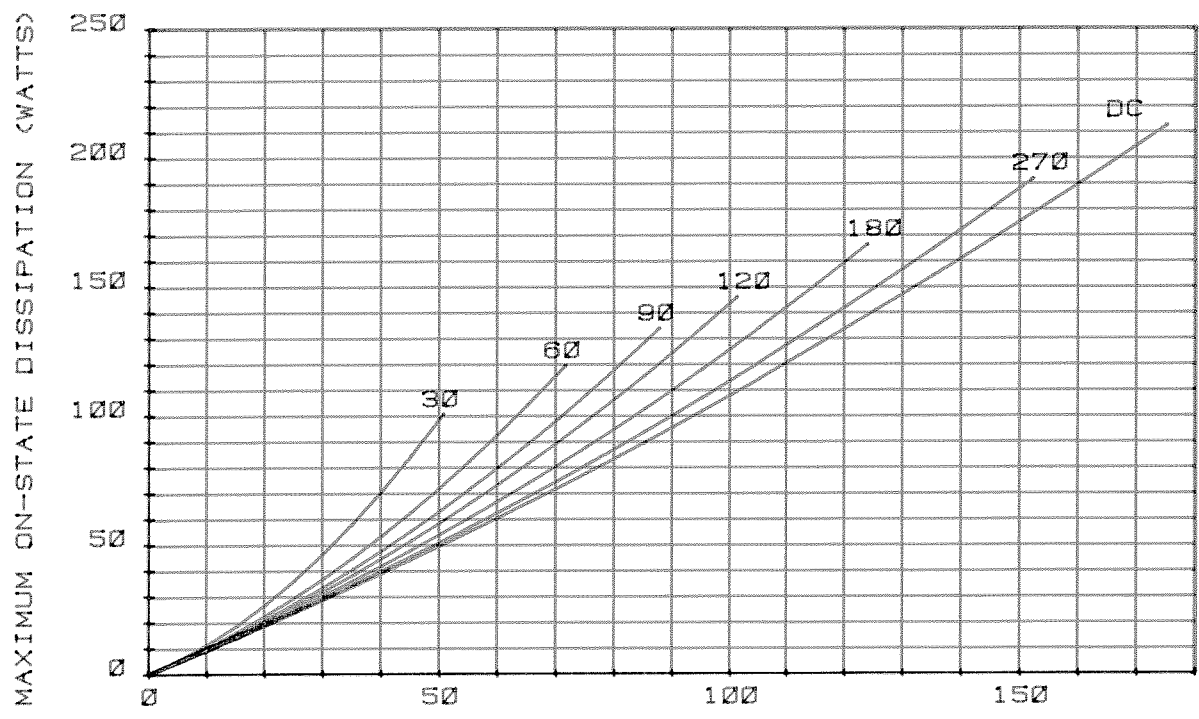
Extension of Voltage Grades

This report is applicable to other and higher voltage grades when supply has been agreed by Sales/Production.

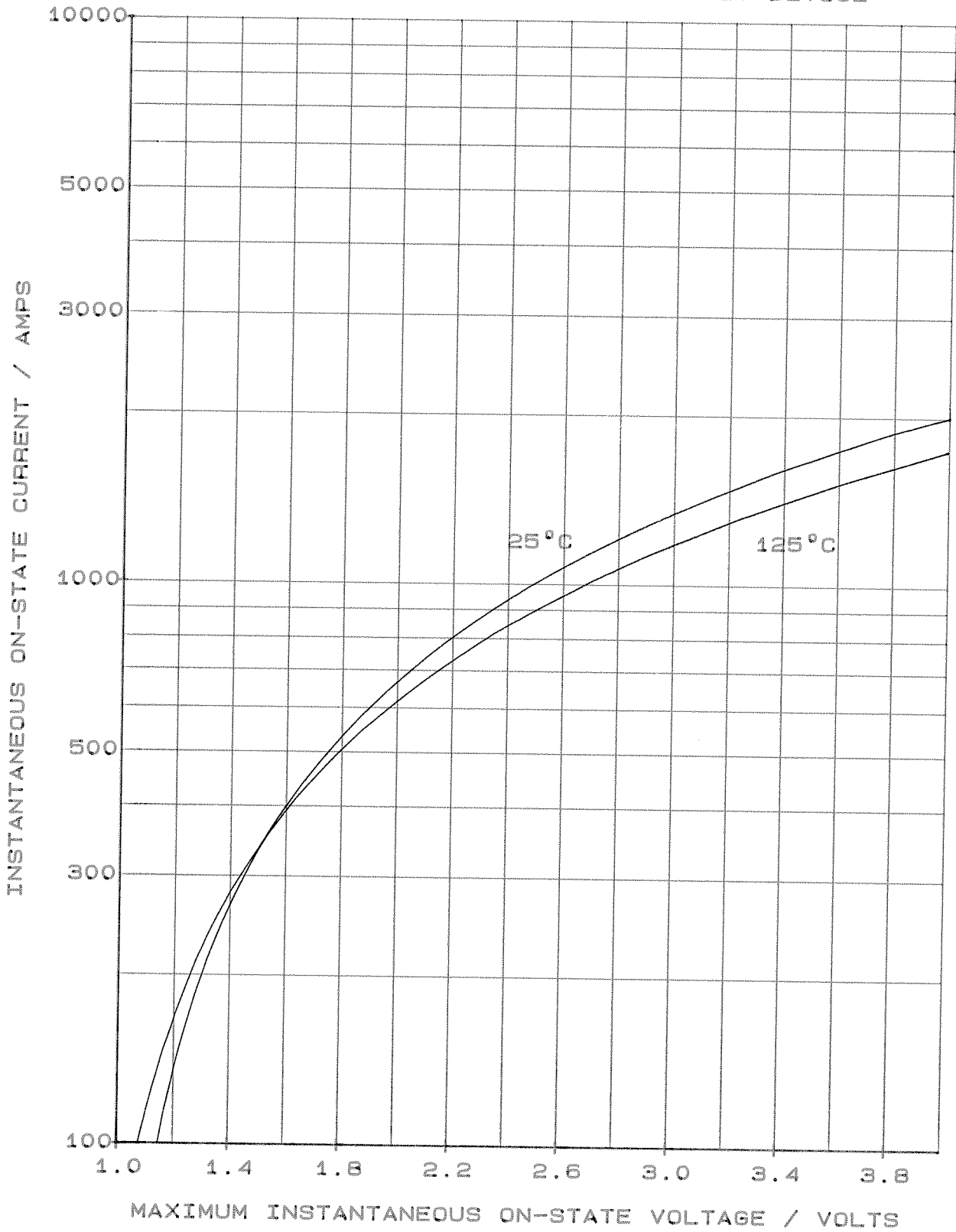
SINE WAVE  
# = conduction angle (degrees)

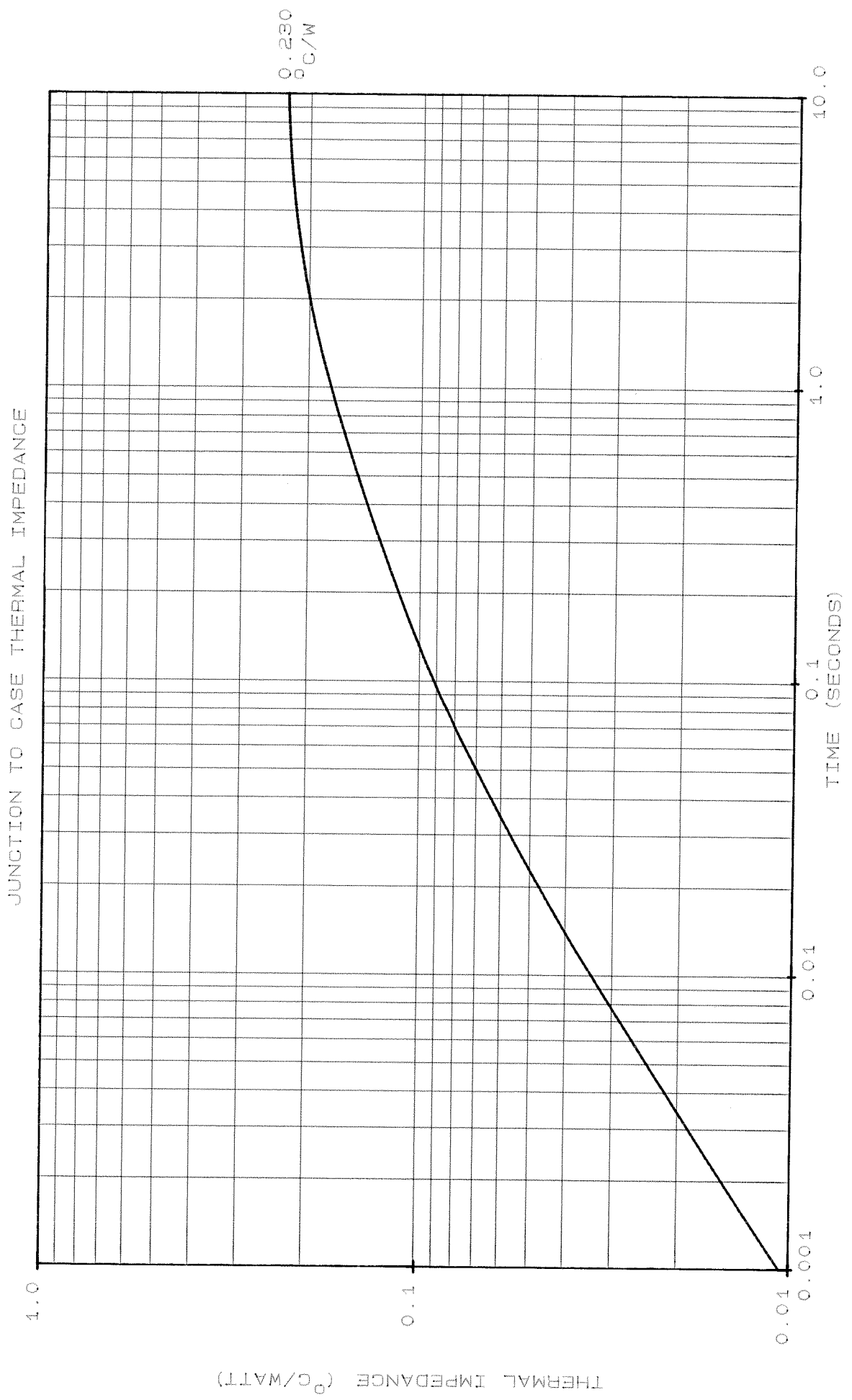


### SQUARE WAVE # = conduction angle (degrees)

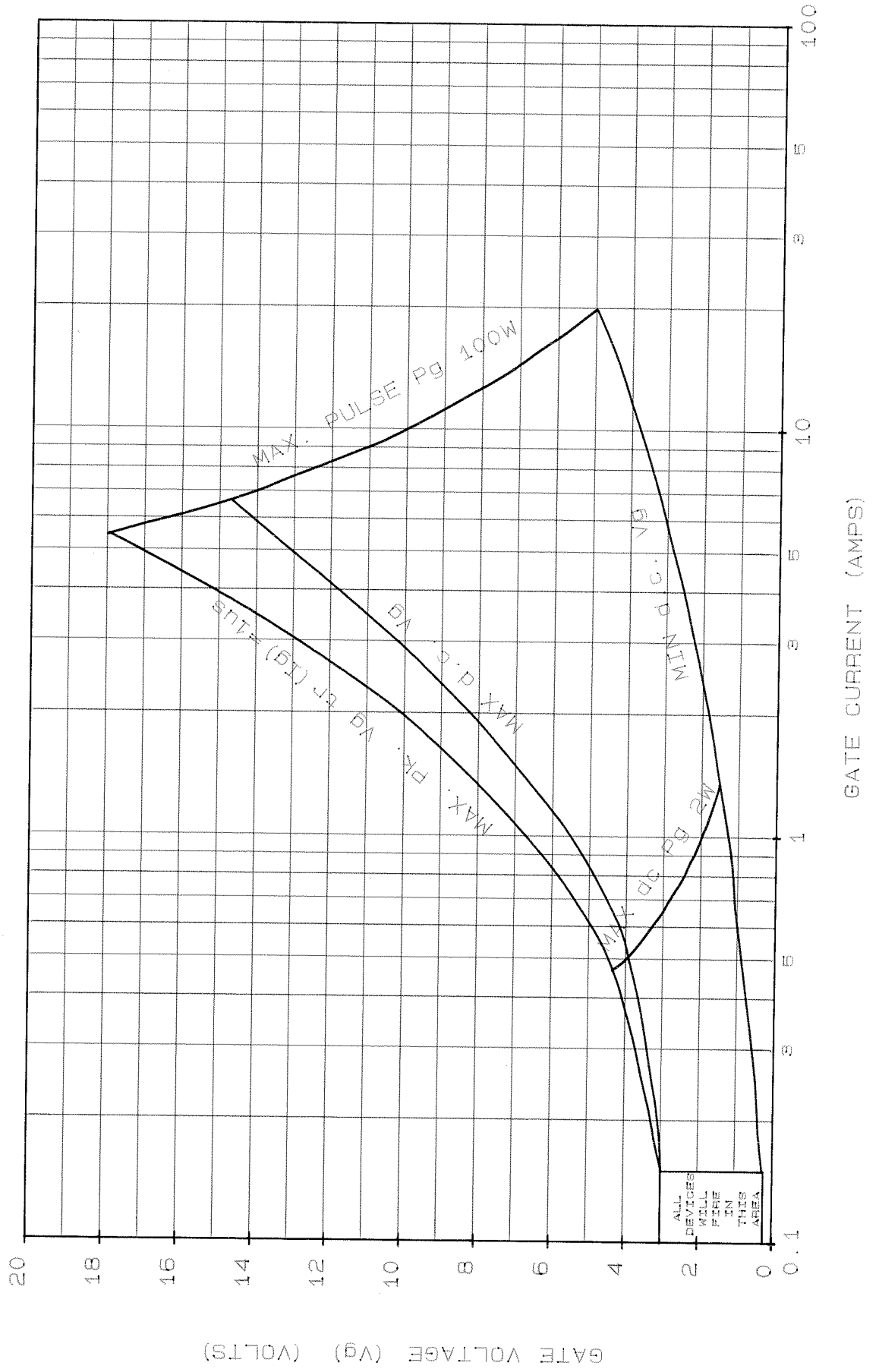


ON-STATE CHARACTERISTIC OF LIMIT DEVICE

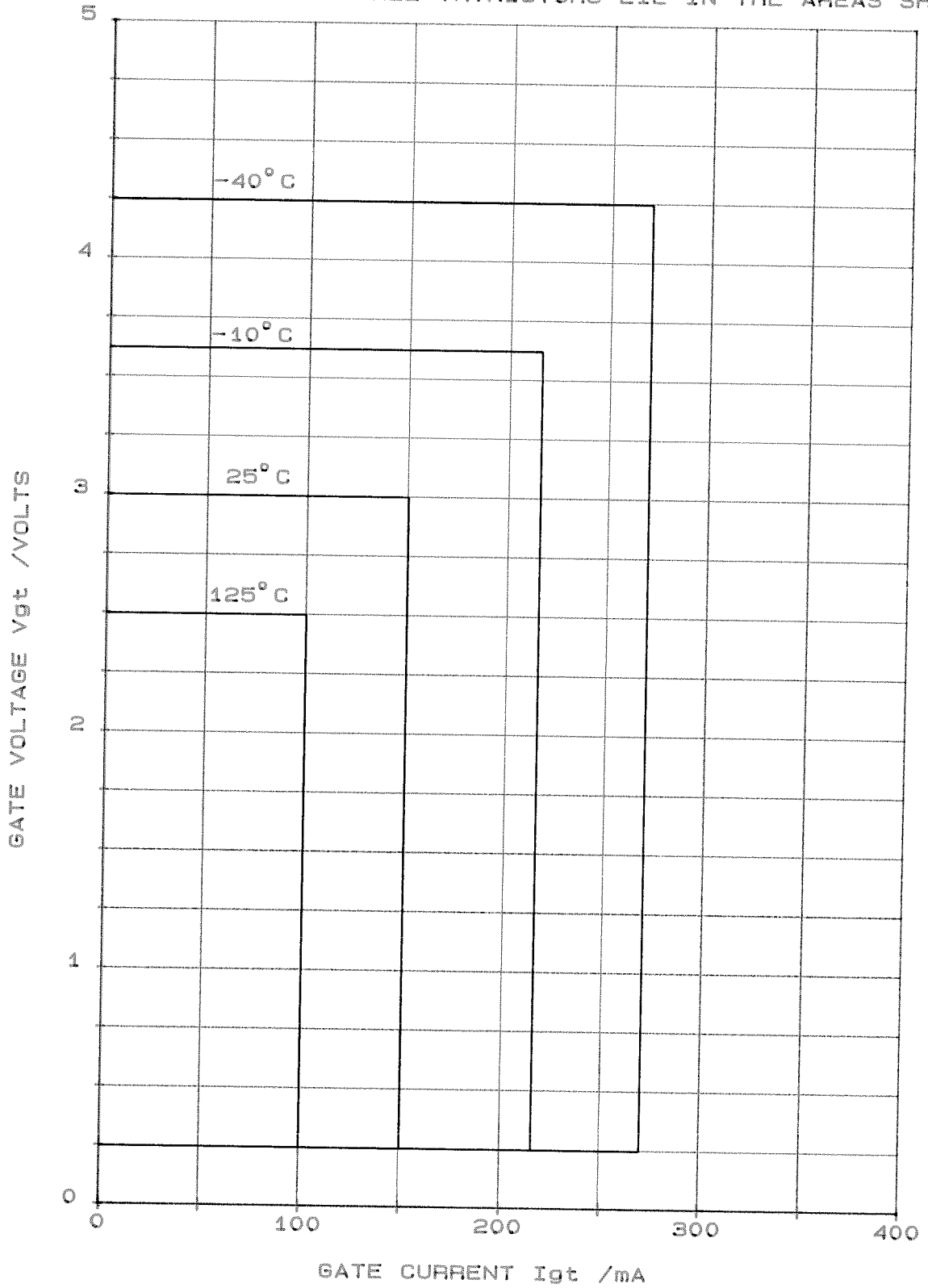




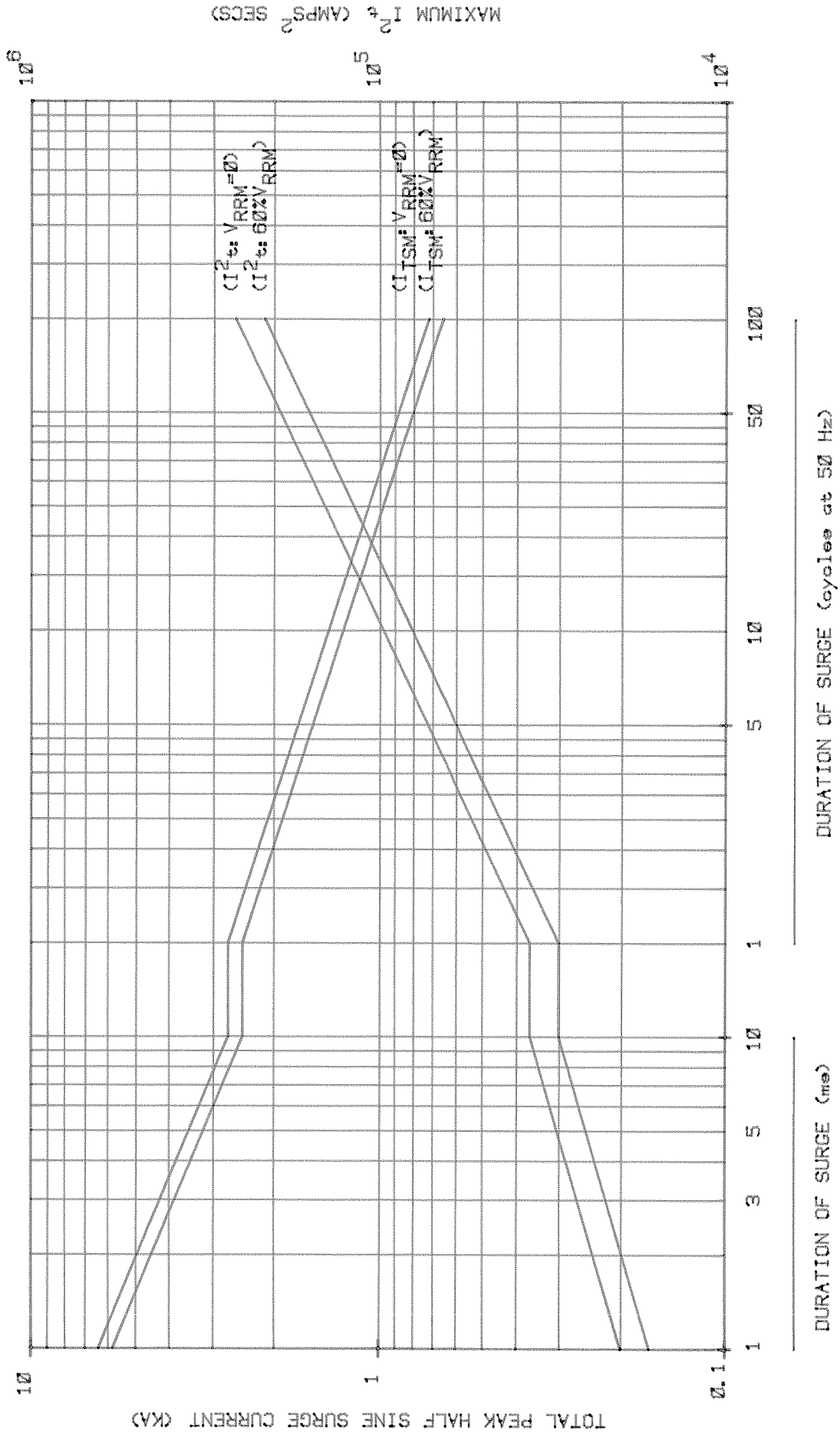
GATE CHARACTERISTICS AT 25°C JUNCTION TEMPERATURE



GATE TRIGGERING CHARACTERISTICS  
(TRIGGER POINTS OF ALL THYRISTORS LIE IN THE AREAS SHOWN)



MAXIMUM NON REPETITIVE SURGE CURRENT AT INITIAL JUNCTION TEMPERATURE 125°C  
 (GATE MAY TEMPORARILY LOSE CONTROL OF CONDUCTION ANGLE)



DURATION OF SURGE (ms)      DURATION OF SURGE (cycles at 50 Hz)



