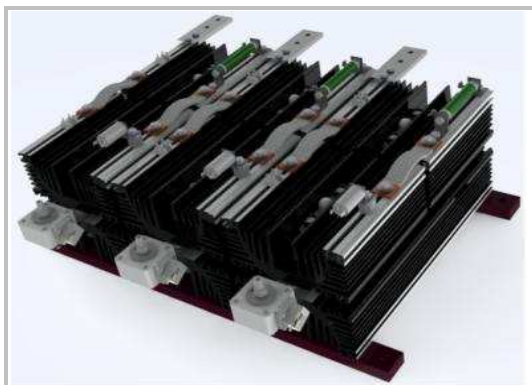


SKS 650N W3C 560 V16 SU



Thyristor Presspack stack

SEMISTACK® CLASSICS - W3C

Three phase reverse parallel thyristor converter

Preliminary Data

Ordering No. 08785211
Description SKS 650N W3C 560 V16 SU

Features

- Non-isolated power stacks
- SKT 760/16
- Heatsink U3/515
- Natural cooling
- RC circuit included
- Fuses with microswitches

Typical Applications

- Soft starters

Remarks

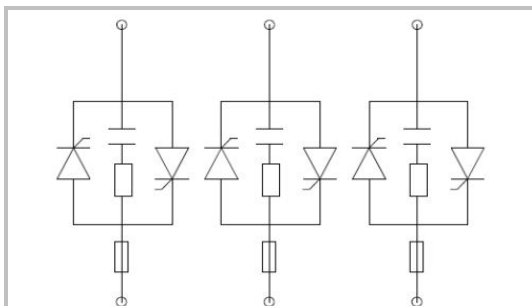
This technical information specifies semiconductor devices but promises no characteristics. No warranty or guarantee, expressed or implied, is made regarding delivery, performance or suitability.

Electrical Characteristics							
Symbol	Conditions			min	typ	max	Unit
Electrical Data							
I _{OUT}	Maximum output current	T _{AMBIENT} = 35°C ; No overload			650		A _{RMS}
V _{AC}	Maximum AC voltage (+/-10%)				500		V _{AC}
P _{TOTAL}	Maximum stack power				560		kW
P _{LOSS}	Stack power loss (T _{AMBIENT} = 35°C)						W

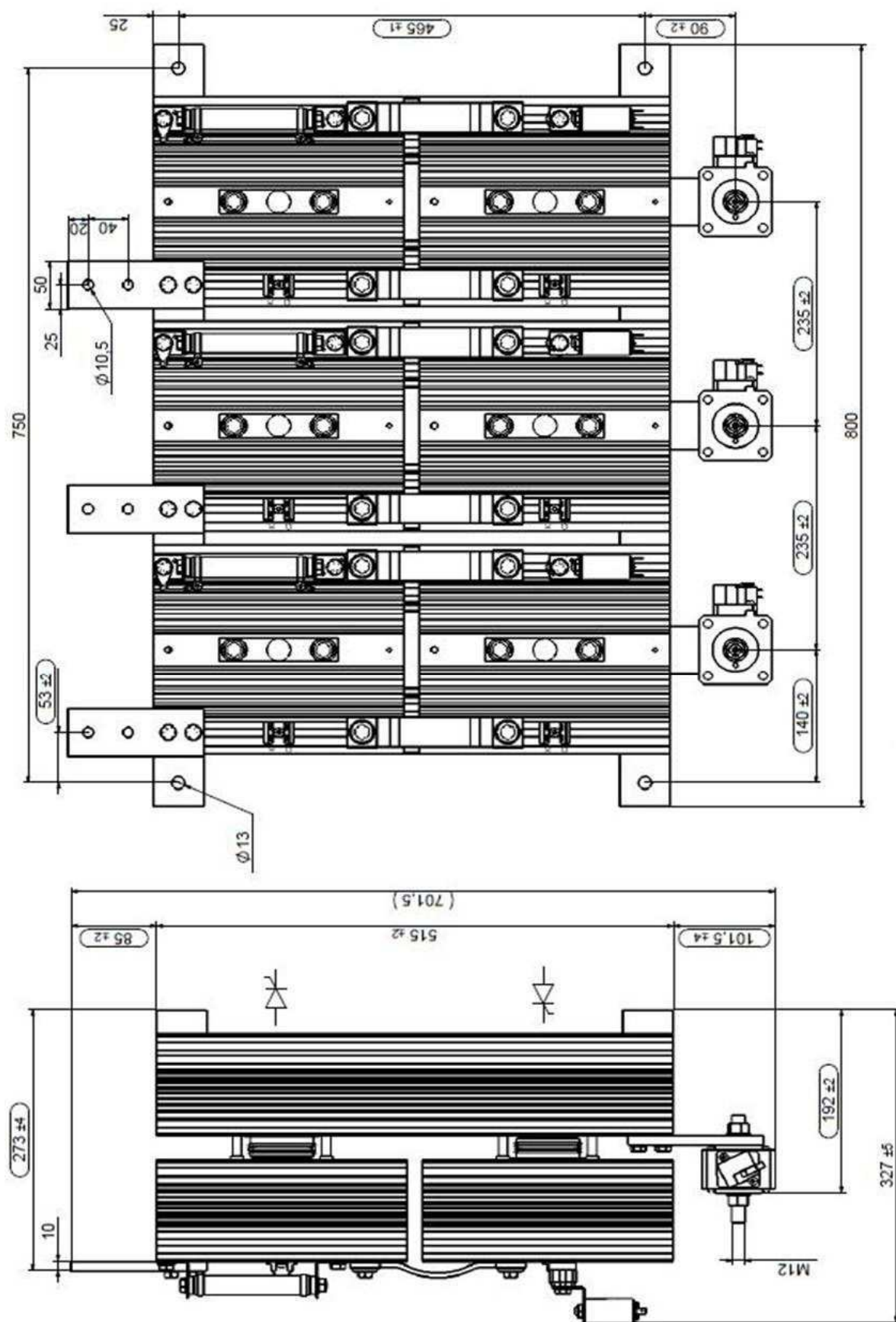
Environmental Data						
Symbol	Conditions		min	typ	max	Unit
Mechanical Data						
Drawing	SEMIKRON document number.revision.version		12051112.00		-	
Weight	Approximate total weight		104.1		kg	
Altitude	Installation altitude without derating				1 000	m
Protection	IEC 60529		IP00		-	
Pollution Degree	EN 50178		2		-	

Stack Protection						
Symbol	Conditions		min	typ	max	Unit
RC Circuit						
Type	RC in parallel with each switch		-		-	
R	Resistance (80W)		33		Ohm	
C			0.47		μF	

Fuses			
Size	1 fuse per phase, with microswitches	33	-
$I_{N RMS}$	Caliber	1 250	A
U_N	Nominal voltage (IEC)	690	V
I^2t	Total at U_N at room temperature (approx. 20...25°C)	1 942.5	kA^2s



W3C



Dimensions

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