

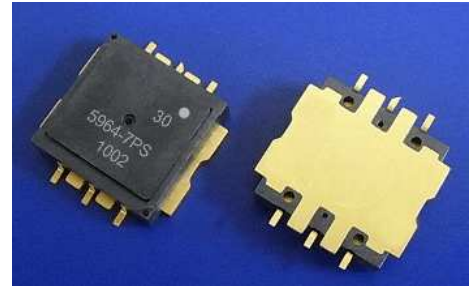


# ELM5964-7PS

## C-Band Internally Matched FET

### FEATURES

High Output Power: P1dB=39.0dBm (Typ.)  
 High Gain: G1dB=11.0dB (Typ.)  
 High PAE:  $\eta_{add}$ =36% (Typ.)  
 Broad Band: 5.9 to 6.4GHz  
 Internally matched  
 Plastic Package for SMT applications



### DESCRIPTION

The ELM5964-7PS is a power GaAs FET that is internally matched for standard communication bands to provide optimum power and gain.

#### ABSOLUTE MAXIMUM RATING (Case Temperature Tc=25 deg.C)

Item	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	15	V
Gate-Source Voltage	$V_{GS}$	-5	V
Total Power Dissipation	$P_T$	42.8	W
Storage Temperature	$T_{STG}$	-40 to +125	deg.C
Channel Temperature	$T_{CH}$	175	deg.C

#### RECOMMENDED OPERATING CONDITION (Case Temperature Tc=25 deg.C)

Item	Symbol	Condition	Limit	Unit
DC Input Voltage	$V_{DS}$		<10	V
Forward Gate Current	$I_{GF}$	$R_G=100$ ohm	<+16	mA
Reverse Gate Current	$I_{GR}$	$R_G=100$ ohm	>-2.2	mA
Channel Temperature	$T_{CH}$		155	deg.C

#### ELECTRICAL CHARACTERISTICS (Case Temperature Tc=25 deg.C)

Item	Symbol	Condition	Limit			Unit	
			Min.	Typ.	Max.		
Drain Current	$I_{DSS}$	$V_{DS}=5V, V_{GS}=0V$	-	3400	5200	mA	
Trans conductance	$gm$	$V_{DS}=5V, I_{DS}=2200mA$	-	3400	-	mS	
Pinch-off Voltage	$V_p$	$V_{DS}=5V, I_{DS}=170mA$	-0.5	-1.5	-3.0	V	
Gate-Source Breakdown Voltage	$V_{GSO}$	$I_{GS}=170\mu A$	-5.0	-	-	V	
Output Power at 1dB G.C.P.	$P_{1dB}$	$V_{DS}=10V$ $I_{DS(DC)}=2200mA$ (typ.) $f=5.9$ to $6.4$ GHz	38.0	39.0	-	dBm	
Power Gain at 1dB G.C.P.	$G_{1dB}$		9.5	11.0	-	dB	
Drain Current	$I_{dsr}$		-	2200	2600	mA	
Power Added Efficiency	$\eta_{add}$		-	36	-	%	
Gain Flatness	$\Delta G$		-	-	1.2	dB	
3rd Order Inter Modulation Distortion	$IM_3$		$f=6.4GHz$ $\Delta f=10MHz$ , 2-tone Test $P_{out}=28.0dBm$ (S.C.L)	-40	-45	-	dBc
$R_{th}$	$R_{th}$		Channel to Case	-	2.5	3.0	deg.C/W
$\Delta T_{ch}$	$\Delta T_{ch}$	$10V \times I_{dsr} \times R_{th}$	-	-	80	deg.C	



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# **ELM5964-7PS**

**C-Band Internally Matched FET**

### **CASE STYLE: I2C**

<b>ESD</b>	<b>Class 3 A</b>	<b>4000 to 8000V</b>
<b>MSL</b>	<b>2A</b>	<b>4 weeks after open the package</b>

**Note : Based on JEDEC JESD22-A114 (C=100pF, R=1500ohm)**

### **Ordering Information**

<b>Model Type</b>	<b>MOQ</b>	<b>MOU</b>	<b>Packing Style</b>
ELM5964-7PS	15pcs	15pcs	15pcs Tray
ELM5964-7PST	500pcs	500pcs	24mm width Tape (500pcs/Reel)

\*MOQ stands for Minimum Order Quantity.

\*MOU stands for Minimum Order Unit size.

### **Note**

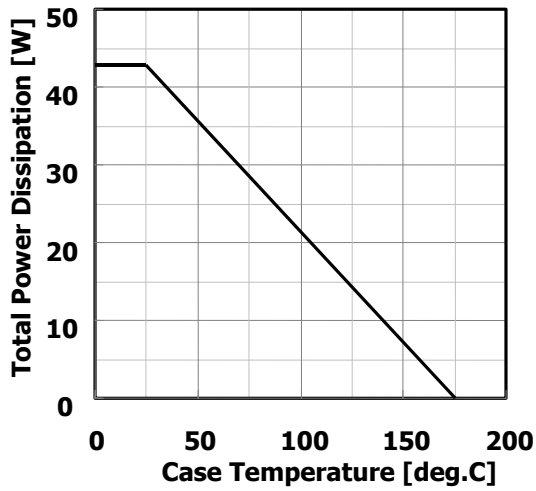
- This device will not be delivered with test data but tested pass/fail 100% against DC and RF specifications.
- NO liquid cleaning process is suitable for this device. (including de-ionized water or solvent)



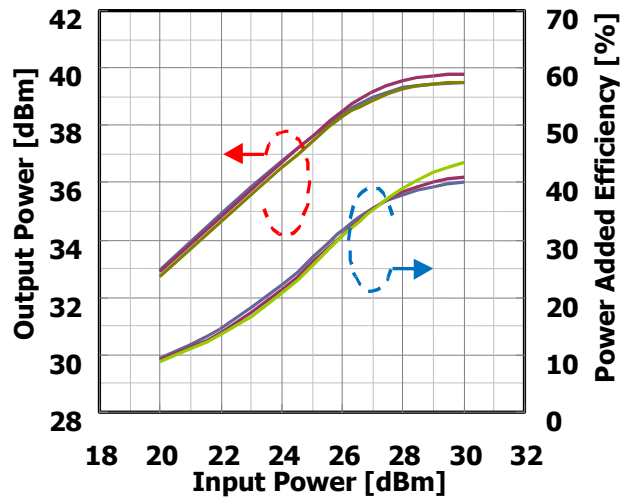
# ELM5964-7PS

C-Band Internally Matched FET

Power Derating Curve

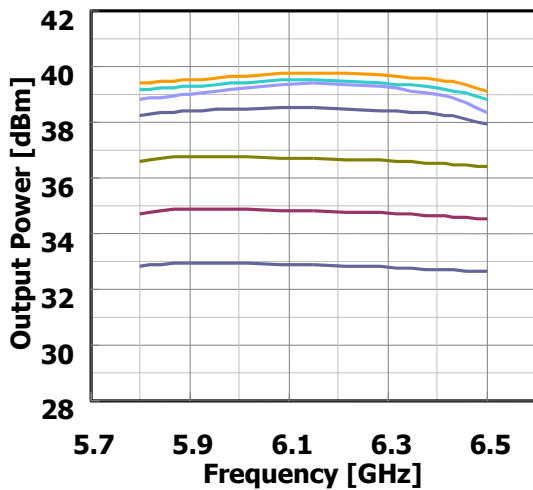


Input Power vs. Output Power, Power Added Efficiency  
 $V_{DS}=10V, I_{DS(DC)}=2200mA$



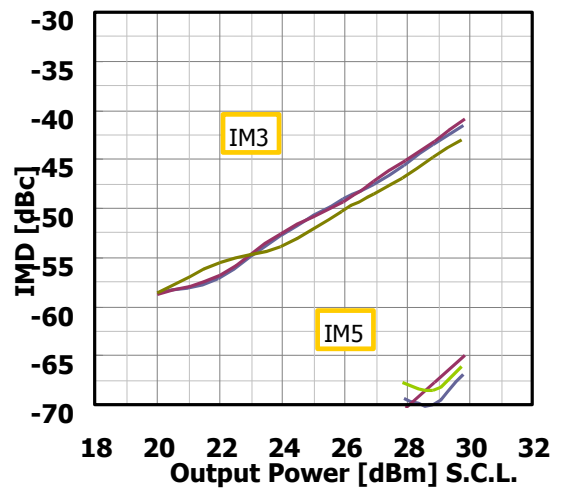
— 5.9 GHz — 6.15 GHz — 6.4 GHz

Output Power vs. Frequency  
 $V_{DS}=10V, I_{DS(DC)}=2200mA$



— 20 dBm — 22 dBm — 24 dBm — 26 dBm  
 — 28 dBm — 30 dBm — P1dB

IMD vs. Output Power  
 $V_{DS}=10V, I_{DS(DC)}=2200mA$



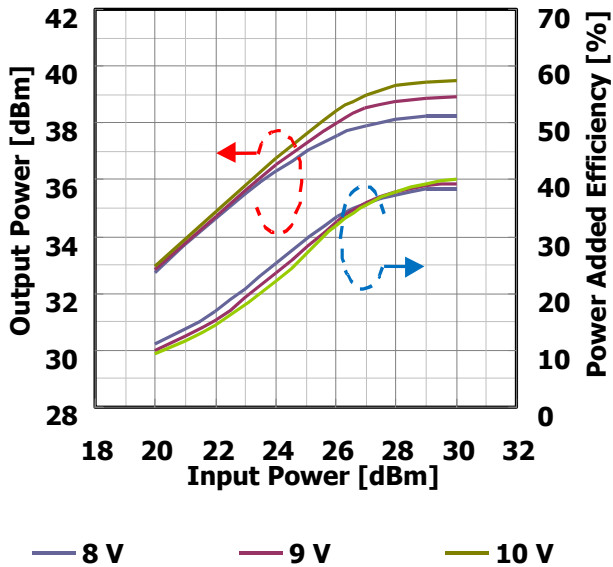
— 5.9 GHz — 6.15 GHz — 6.4 GHz



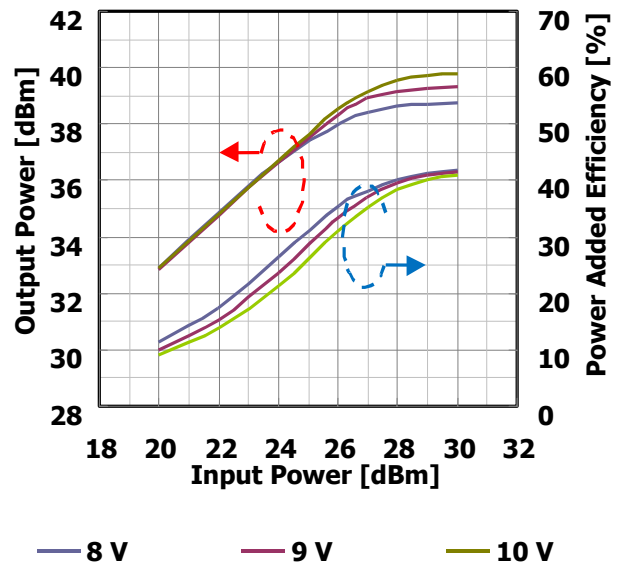
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C-Band Internally Matched FET

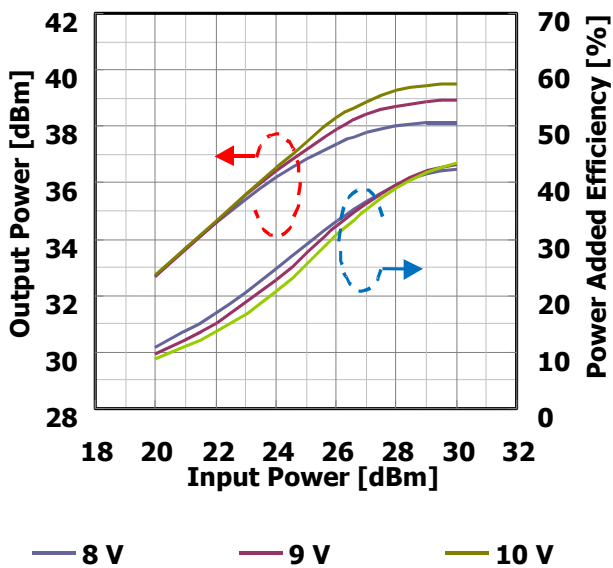
Input Power vs. Output Power,  
Power Added Efficiency by Drain Voltage  
 $I_{DS(DC)}=2200\text{mA}$  @5.9GHz



Input Power vs. Output Power,  
Power Added Efficiency by Drain Voltage  
 $I_{DS(DC)}=2200\text{mA}$  @6.15GHz



Input Power vs. Output Power,  
Power Added Efficiency by Drain Voltage  
 $I_{DS(DC)}=2200\text{mA}$  @6.4GHz

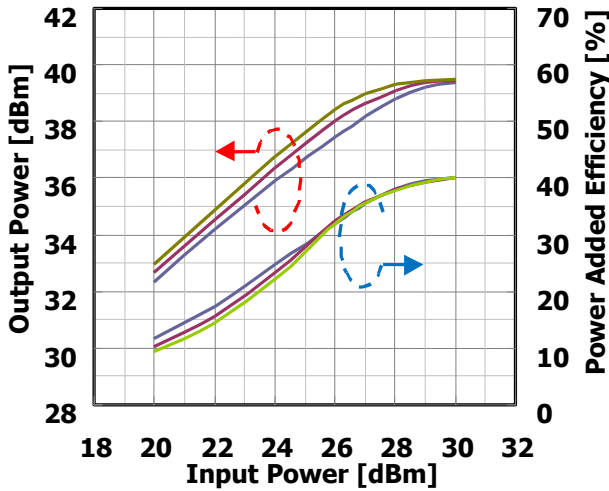




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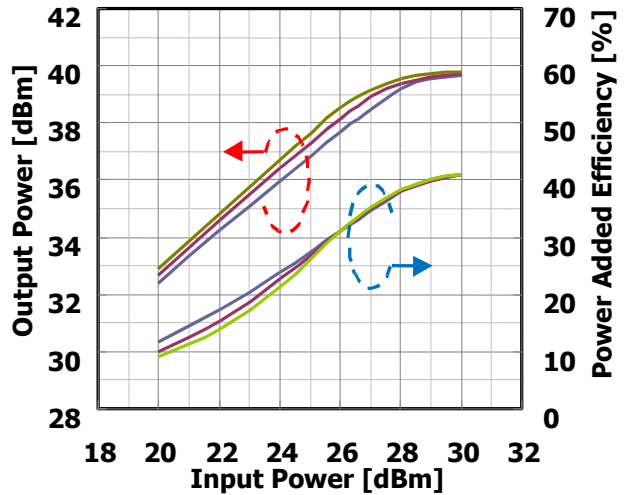
C-Band Internally Matched FET

Input Power vs. Output Power, Power Added Efficiency by Quiescent Drain Current  
VDS=10V @5.9GHz



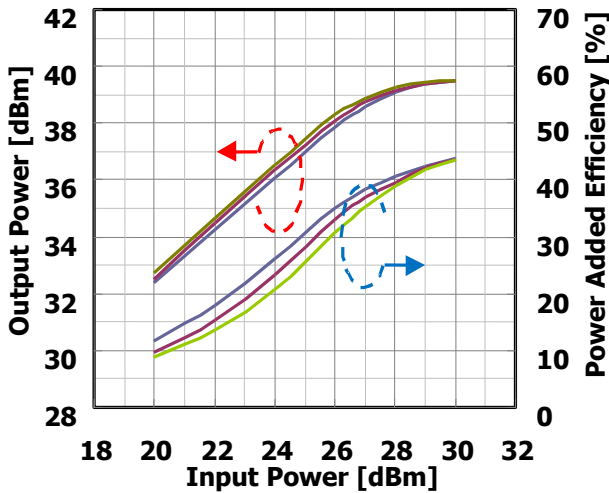
— 1400 mA — 1800 mA — 2200 mA

Input Power vs. Output Power, Power Added Efficiency by Quiescent Drain Current  
VDS=10V @6.15GHz



— 1400 mA — 1800 mA — 2200 mA

Input Power vs. Output Power, Power Added Efficiency by Quiescent Drain Current  
VDS=10V @6.4GHz



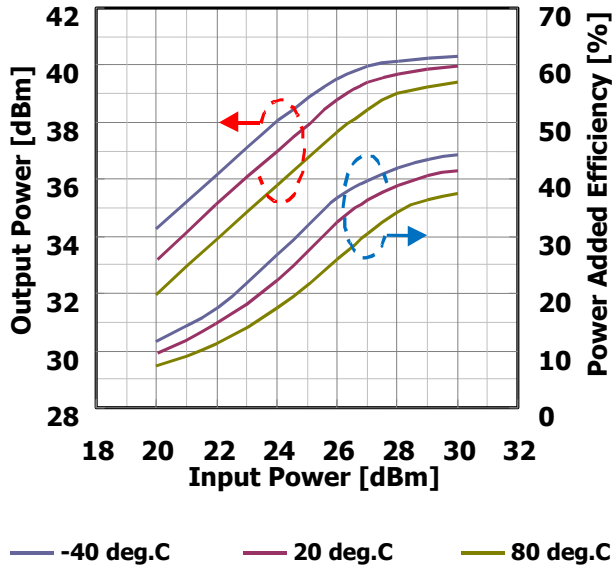
— 1400 mA — 1800 mA — 2200 mA



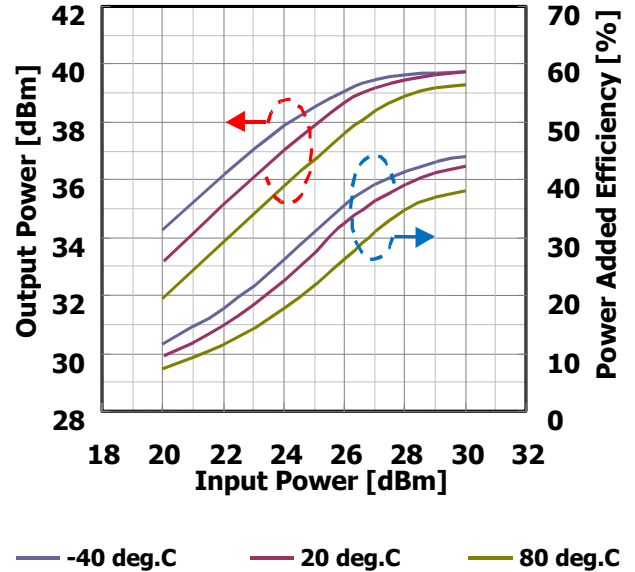
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C-Band Internally Matched FET

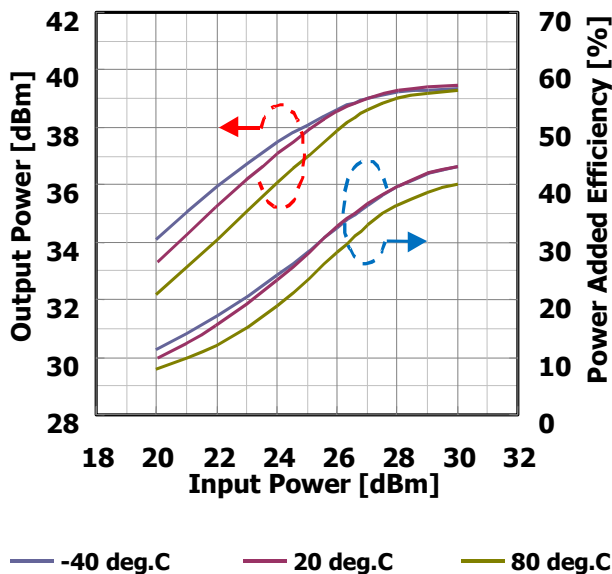
Input Power vs. Output Power, Power Added Efficiency by Case Temperature  
 $V_{DS}=10V$   $I_{DS(DC)}=2200mA$  @5.9GHz



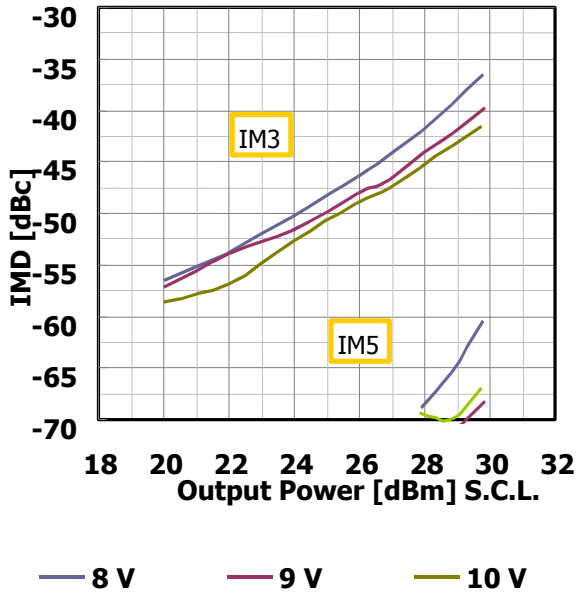
Input Power vs. Output Power, Power Added Efficiency by Case Temperature  
 $V_{DS}=10V$   $I_{DS(DC)}=2200mA$  @6.15GHz



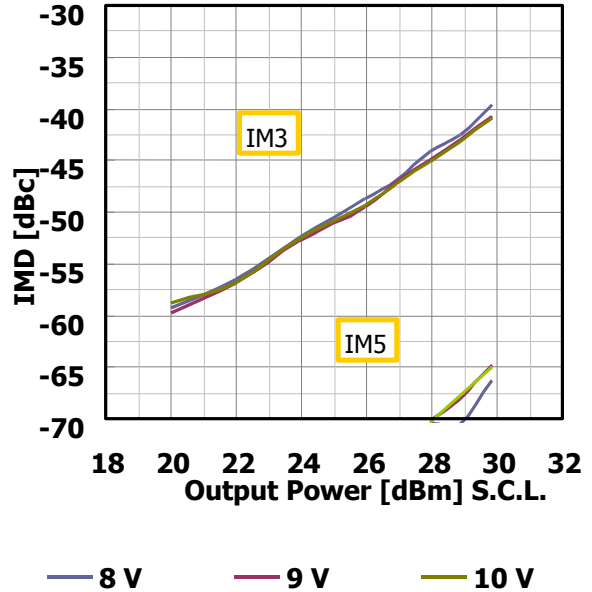
Input Power vs. Output Power, Power Added Efficiency by Case Temperature  
 $V_{DS}=10V$   $I_{DS(DC)}=2200mA$  @6.4GHz



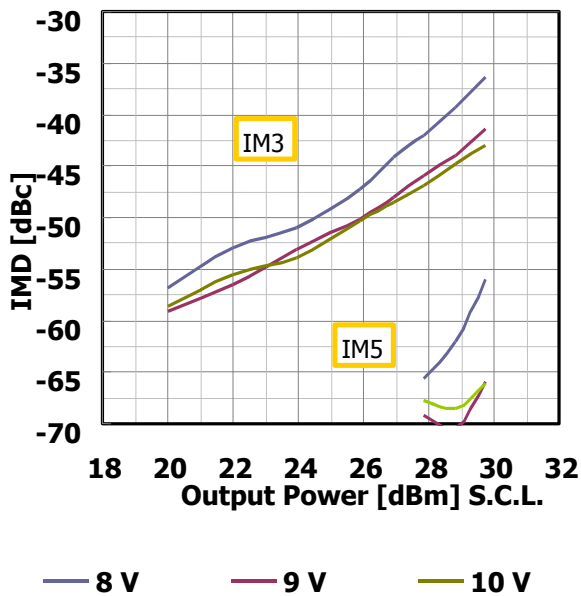
**IMD Performance vs. Output Power by Drain Voltage**  
 $I_{DS(DC)} = 2200\text{mA} @ 5.9\text{GHz}$



**IMD Performance vs. Output Power by Drain Voltage**  
 $I_{DS(DC)} = 2200\text{mA} @ 6.15\text{GHz}$



**IMD Performance vs. Output Power by Drain Voltage**  
 $I_{DS(DC)} = 2200\text{mA} @ 6.4\text{GHz}$

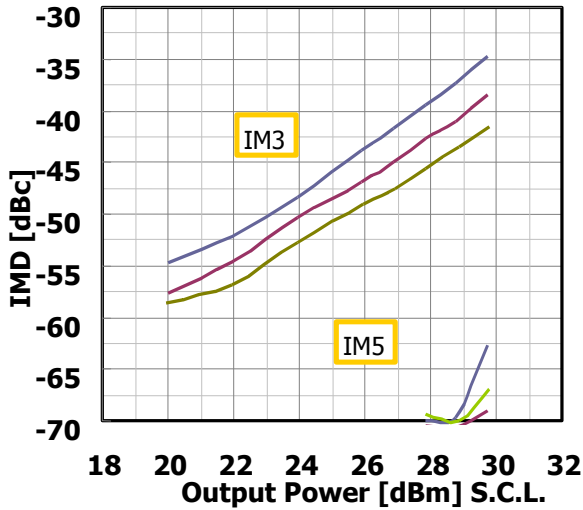




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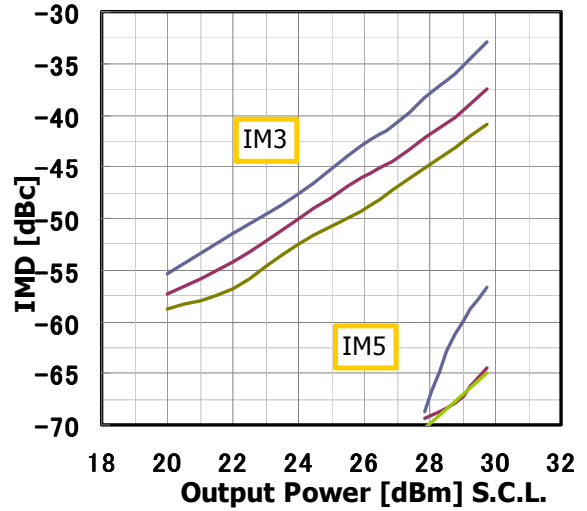
C-Band Internally Matched FET

IMD Performance vs. Output Power  
by Quiescent Drain Current  
 $V_{DS}=10V$  @5.9GHz



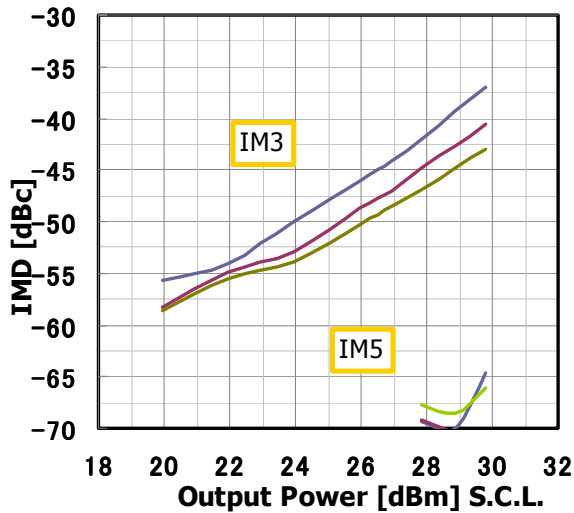
— 1400 mA — 1800 mA — 2200 mA

IMD Performance vs. Output Power  
by Quiescent Drain Current  
 $V_{DS}=10V$  @6.15GHz



— 1400 mA — 1800 mA — 2200 mA

IMD Performance vs. Output Power  
by Quiescent Drain Current  
 $V_{DS}=10V$  @6.4GHz



— 1400 mA — 1800 mA — 2200 mA

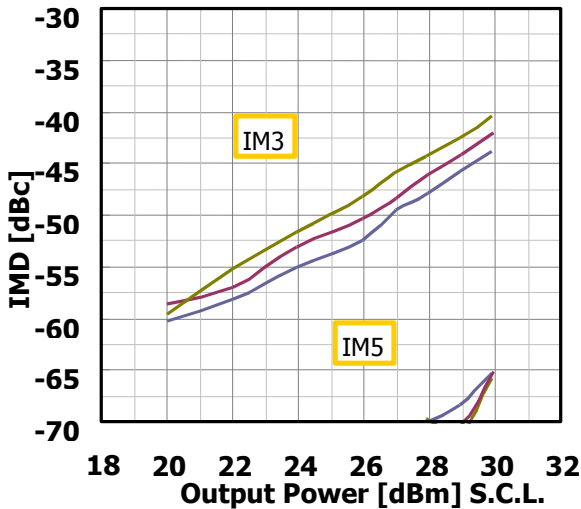


# ELM5964-7PS

C-Band Internally Matched FET

IMD Performance vs. Output Power  
by Case Temperature

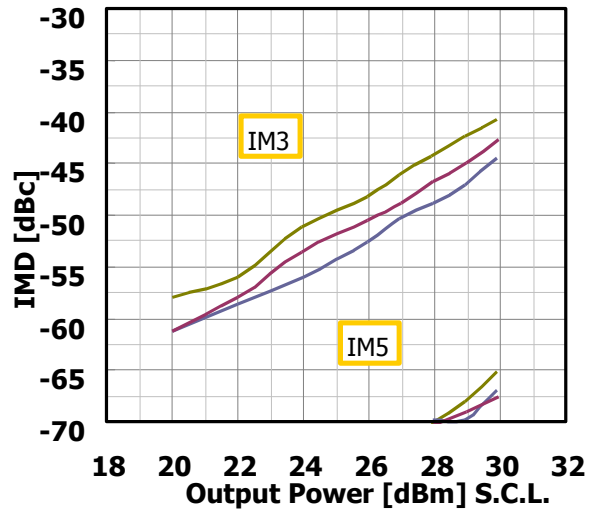
$V_{DS}=10V$   $I_{DS(DC)}=2200mA$  @5.9GHz



— -40 deg.C — 20 deg.C — 80 deg.C

IMD Performance vs. Output Power  
by Case Temperature

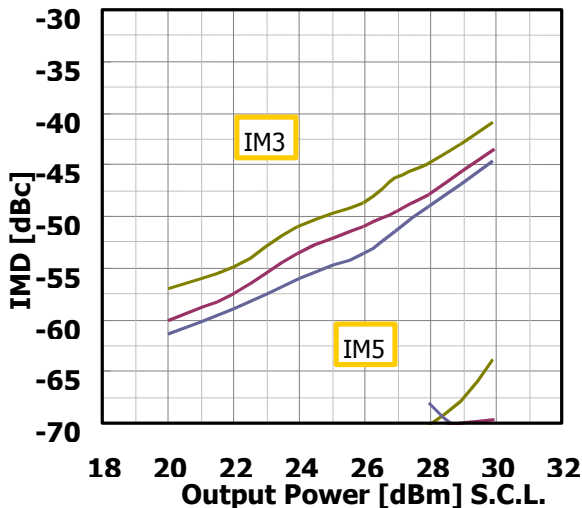
$V_{DS}=10V$   $I_{DS(DC)}=2200mA$  @6.15GHz



— -40 deg.C — 20 deg.C — 80 deg.C

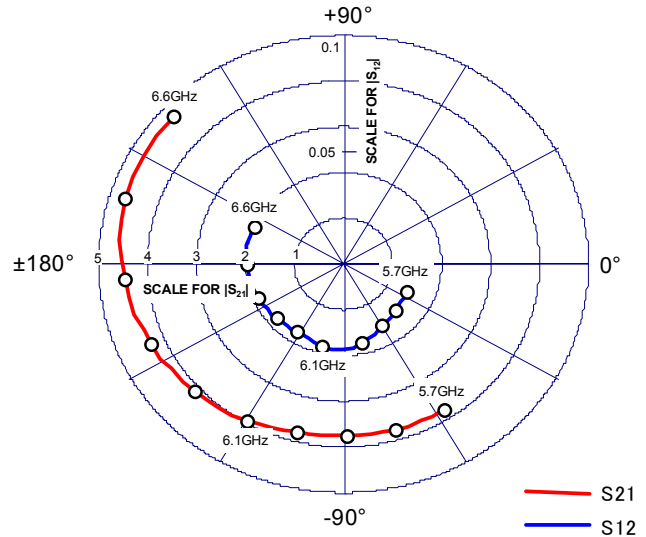
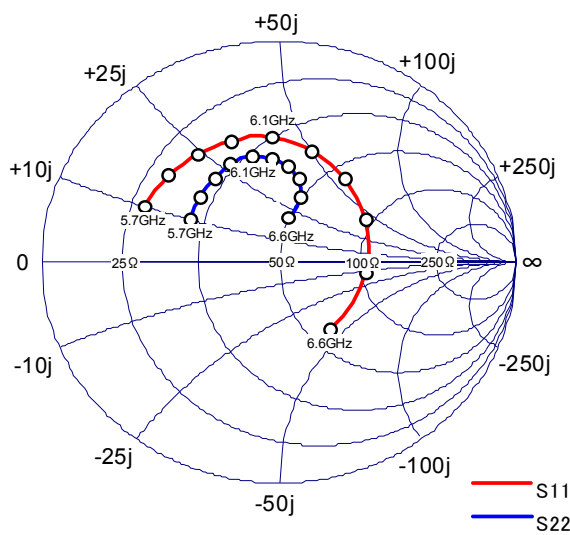
IMD Performance vs. Output Power  
by Case Temperature

$V_{DS}=10V$   $I_{DS(DC)}=2200mA$  @6.4GHz

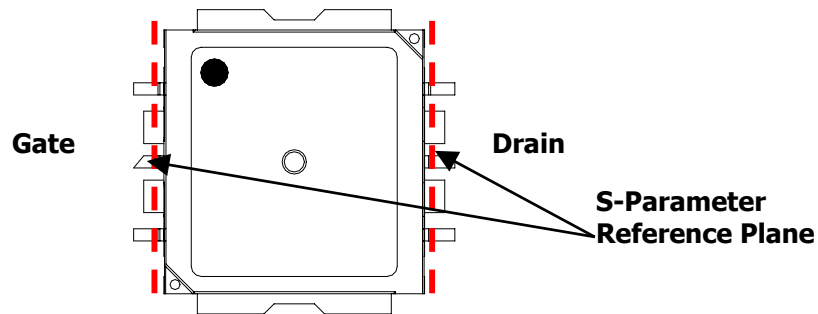


— -40 deg.C — 20 deg.C — 80 deg.C

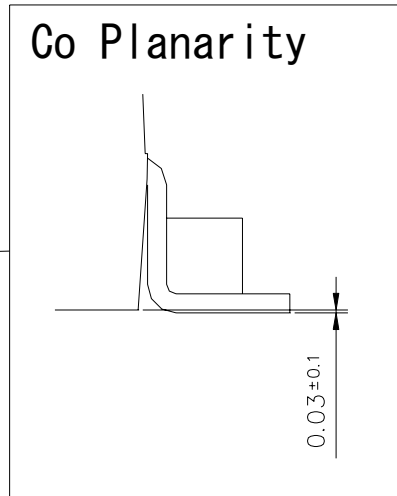
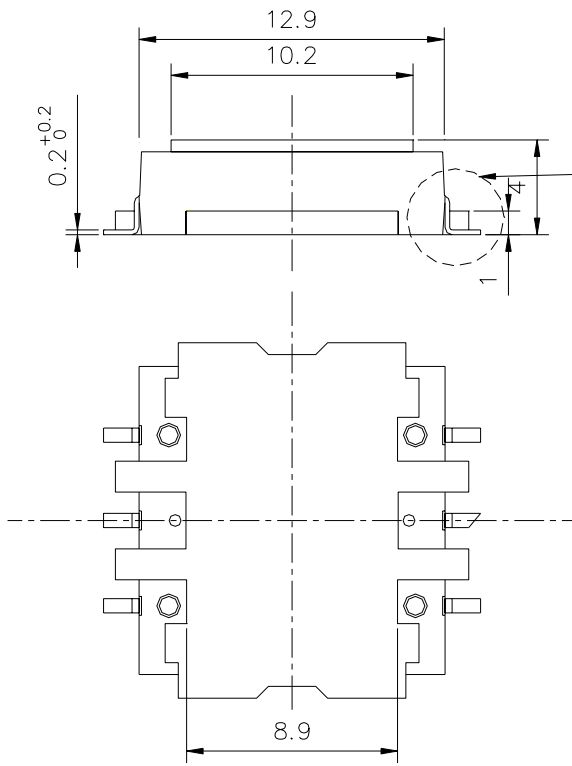
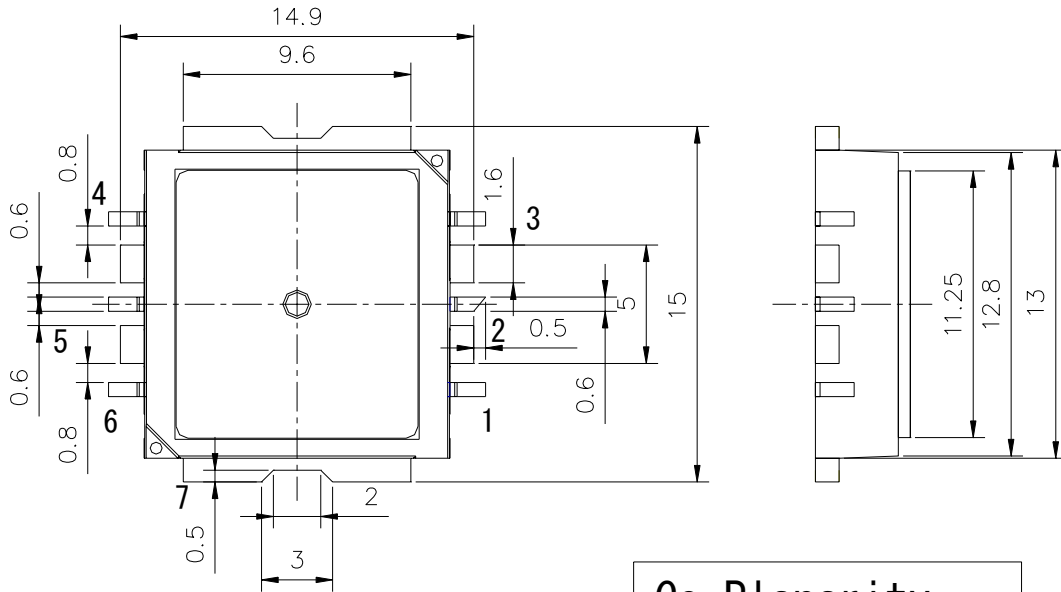
•S-Parameter



Frequency (MHz)	S11		S21		S12		S22	
	MAG	ANG	MAG	ANG	MAG	ANG	MAG	ANG
5700	0.618	155.6	3.778	-57.4	0.029	-25.9	0.42	152.6
5800	0.607	139.7	3.782	-73.6	0.029	-43.4	0.442	138.1
5900	0.589	124.9	3.773	-89.3	0.031	-59.7	0.463	126
6000	0.582	110	3.789	-104.3	0.035	-78.1	0.486	114.9
6100	0.567	92.6	3.953	-120.1	0.037	-103.2	0.494	103.1
6200	0.519	74.3	4.12	-137.4	0.035	-122.3	0.467	93.6
6300	0.467	54	4.297	-155.9	0.036	-138.8	0.434	85.6
6400	0.413	28	4.485	-175.7	0.038	-157.7	0.384	78.2
6500	0.367	-8.1	4.678	162	0.04	-179.5	0.307	73.2
6600	0.374	-54.7	4.72	137.1	0.04	156	0.209	79.3



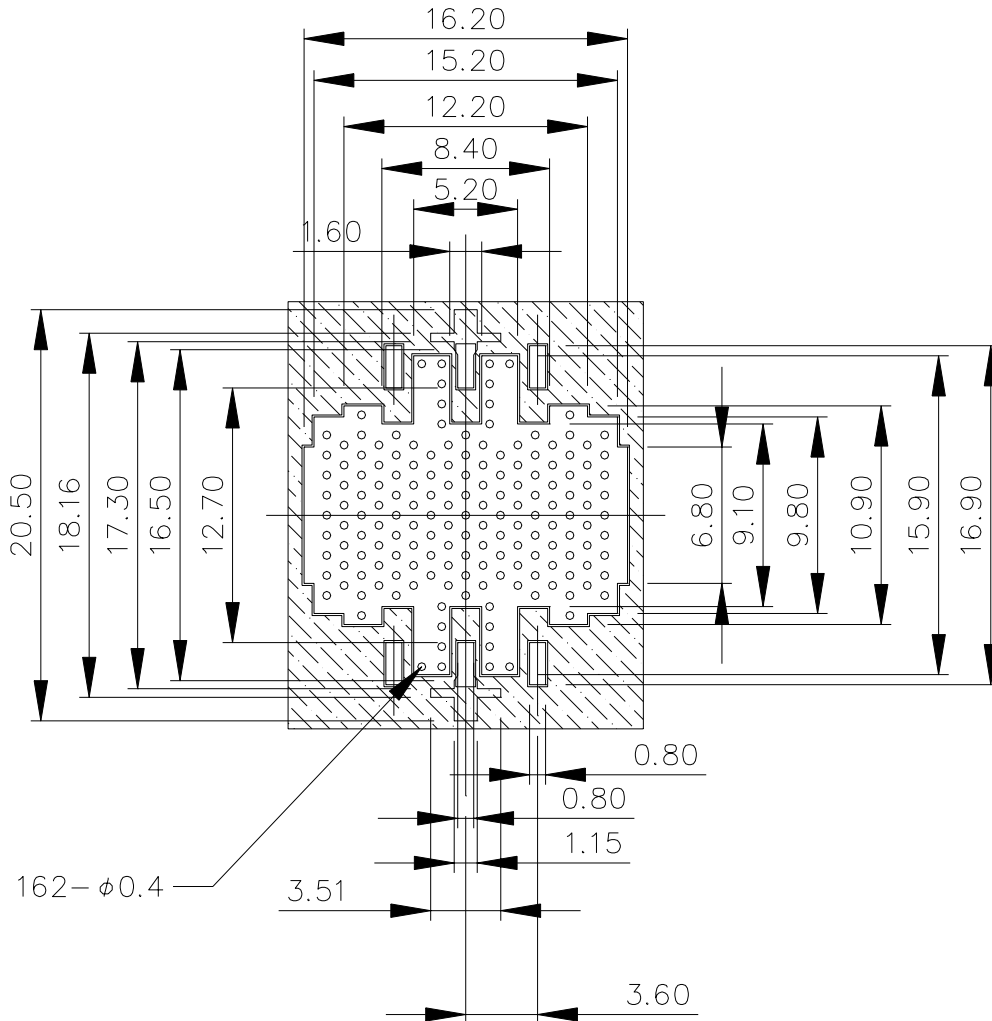
**Package Out Line**



**Pin Assignments**

1	: NC
2	: Gate
3	: NC
4	: NC
5	: Drain
6	: NC
7	: Source

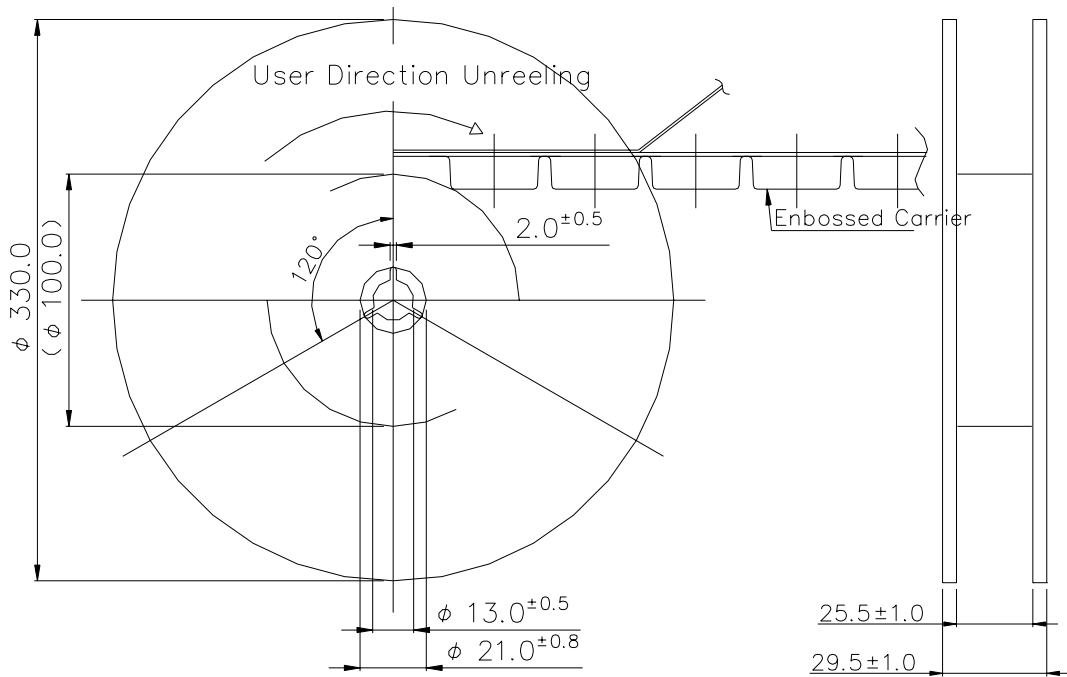
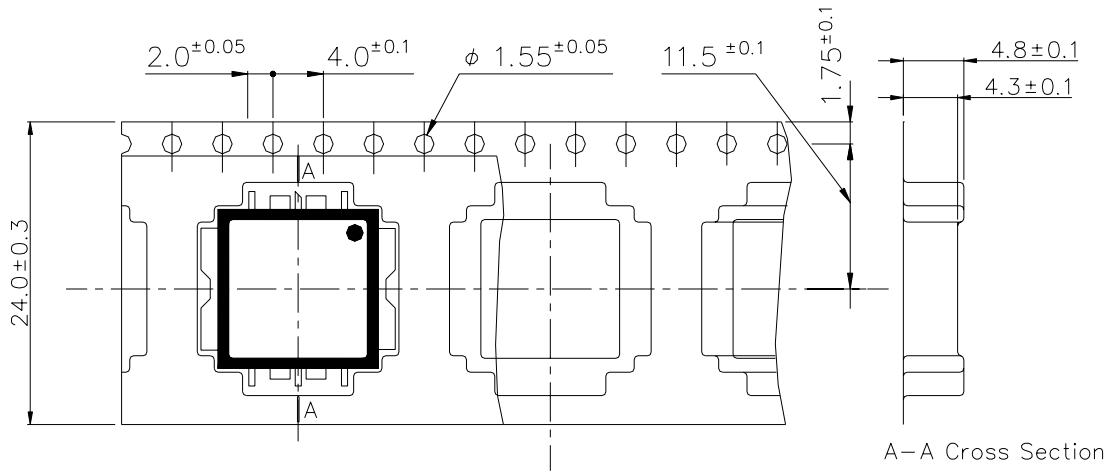
**PCB Pads and Solder-Resist Pattern**



Notes :

1. Laminate : Rogers Corporation R04003, Thickness  $t=0.508\text{mm}$ , Cu Foil  $18\mu\text{m}$ .  
 Finish to copper foil : Ni  $0.1\mu\text{m}$  min. / Au  $0.1\mu\text{m}$  (Both side).
2. : Resist

**Marking and Tape/Reel Configuration**



Quantity: 500pcs/tape  
 Tape Material: Conductive PS

(unit in mm)

● **Mounting Instructions for Package for Lead-free solder**

● **Mounting Condition**

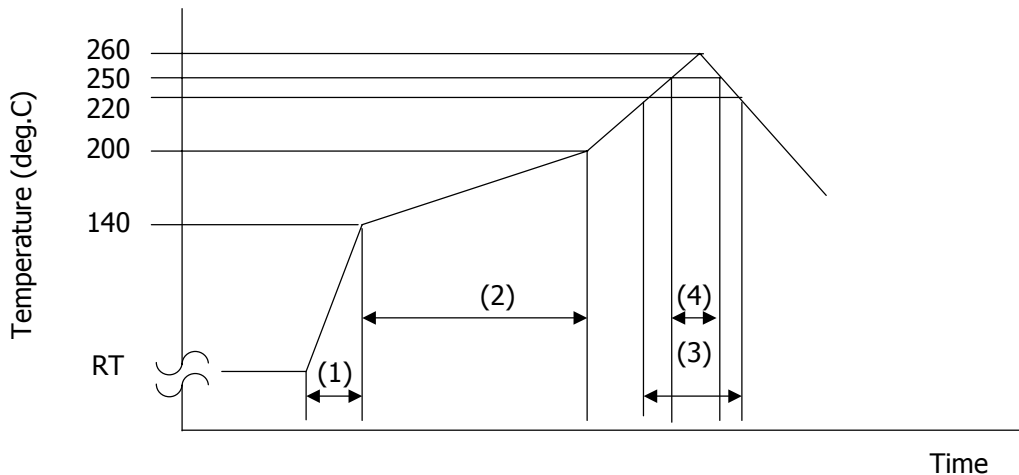
For soldering, Lead-free solder (Sn-3.0Ag-0.5Cu)\*1 which is no liquid cleaning type shall be used.

1. The example solder is a tin-rich alloy with 3.0% silver and 0.5% copper, often called Sn 96 for its approximate Tin content.
2. A rosin type flux with chlorine content of 0.2% or less shall be used. The rosin flux with low halogen content is recommended. When soldering, use the following time/ temperature profile with any of the methods listed for acceptable solder joints.
3. Make sure the devices have been properly prepared with flux prior soldering.

● **Reflow soldering method (Infrared reflow / Heat circulation reflow / Hot plate reflow);**

Limit solder to 3 reflow cycles because resin is used in the modules manufacturing process. Excessive reflow will effect the resin resulting in a potential failure or latent defect. The recommended reflow temperature profile is shown below. The temperature of the reflow profile must be measured at the device lead.

● **Reflow temperature profile and condition:**



- (1). Temperature rise: 3 deg.C/seconds.
  - (2). Preheating: 150 - 200 deg.C, 60 - 180seconds.
  - (3). Main heating: 220 deg.C, 60 seconds max.
  - (4). Main heating: 260 deg.C max., more than 250 deg.C, 20 - 40 seconds max.
- \* Measurement point: Device Heat-sink (Source Pin).

1. The above-recommended conditions were confirmed using the manufacturer's equipment and materials. However, when soldering these products, the soldering condition should be verified by customer using their own particular equipment and materials.

● **Cleaning**

Avoid washing of the device after soldering by reflow method due to the risk of liquid absorption by the resin used in this part.



## **Humidity Lifetime for ELMxxxx-7PST**

The following graph shows the effect of moisture on lifetime (moisture resistance) for the **ELMxxxx-7PST**. Each graph indicates the MTTF and failure rate prediction (Confidential Level = 90 %) which calculated from the results of highly accelerated temperature and humidity stress test (HAST).

Representative of device type : ELM7179-7PST

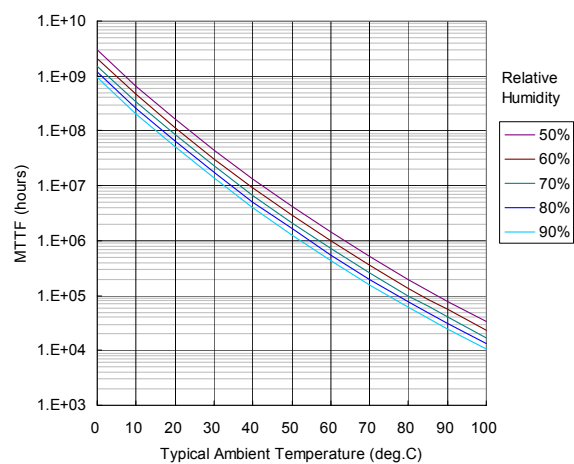
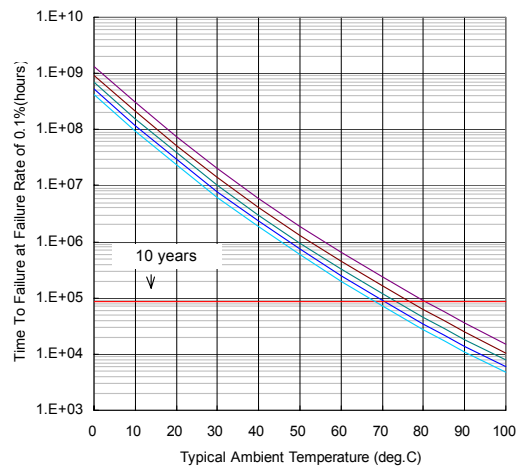
Subject of device type : ELMxxxx-7PST

### **Field environmental conditions for operation**

If the **ELMxxxx-7PST** is installed in a non-hermetic environment, please refer to the following recommendations and notes for design with, and assembly and use of our products.

Note 1. When drain current cuts off, it should be cut off by drain bias, and not cut off by gate bias only. The humidity lifetime becomes shorter in case of the gate-only cut off operation due to electric field strength interacting with humidity.

Note 2. **ELMxxxx-7PST** should be used under the environment conditions of no dew condensation. These plots do not apply in the case of liquid absorbed into the resin, whether applied to the part in assembly or as condensate in the application.





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***ELM5964-7PS***  
***C-Band Internally Matched FET***

**For further information please contact:**

**<http://global-sei.com/Electro-optic/about/office.html>**

**CAUTION**

This product contains **gallium arsenide (GaAs)** which can be hazardous to the human body and the environment. For safety, observe the following procedures:

- Do not put these products into the mouth.
- Do not alter the form of this product into a gas, powder, or liquid through burning, crushing, or chemical processing as these by-products are dangerous to the human body if inhaled, ingested, or swallowed.
- Observe government laws and company regulations when discarding this product. This product must be discarded in accordance with methods specified by applicable hazardous waste procedures.