IO-Link

ELMOS is a member of the IO-Link consortium

driven by ELMOS

DUAL IO-LINK MASTER TRANSCEIVER WITH UARTS E981.12

ADVANCE PRODUCT INFORMATION - MAR 17, 2011

Features

- 2-port IO-Link Master
- Integrated UART-Interface for each port
- Output driver typ. 1Ω
- IO-Link supply switch supporting external PMOS transistors
- Wake-up generation support
- Supply voltage range
  VDDH: 8V – 32V / VDD: 3.15V - 3.45V
- Over-current & short-circuit protection at output stages with configurable thresholds
- Digital inputs configurable for IO-LINK or IEC 61131-2 compatible interface
- SPI interface for configuration, programming and diagnostic functions
- Undervoltage monitor at VDDH and VDD
- Overtemperature protection

Applications

- IO-Link Master application in modular SPS
- Gateway applications

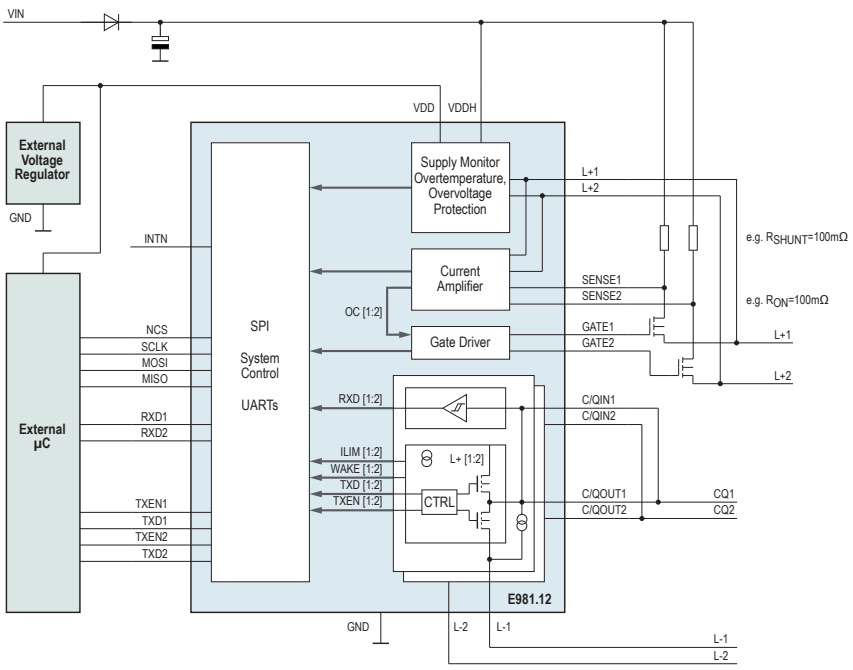
General Description

This device comes with two independently operating IO-Link MASTER PHYs which make it a perfect fit for 2/4/8/16-port Master applications. Especially for multi-port applications the integrated UARTs offer you high flexibility regarding scalability of Master ports and the choice of the µC used for the application. As the E981.12 allows the support of external MOSFETs for sensor supply, it enables cost-effective and power dissipation optimized system concepts.

The dual IO-Link Master is also available as SIP in QFN64L9 package at RENESAS with embedded 78K0R for protocol handling.

Ordering Information

Product ID	Temp. Range	Package
E981.12	-40°C to +105°C	QFN44L7



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