

## General Description

OIT5C consists in a silicon phototransistor's 9 channels monolithic array.

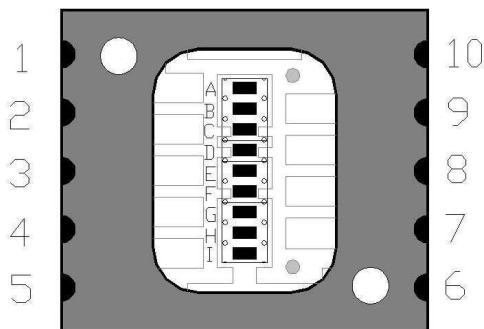
The phototransistors have a common collector and every emitter is available as a pad. The pitch of the silicon arrays is 0.45 mm, while the component electrical pitch is 1.27 mm. The active area of each element is 0.25 x 0.50 mm. The encapsulant is an high quality microelectronic transparent resin, its transmission value is 100% in the range 300-900nm.

The advantages of this product are the high uniformity of the silicon sensors, due to the monolithic construction, the high stability of the signal and the high optical responsivity, due to the antireflective coating deposited on the phototransistor's areas.

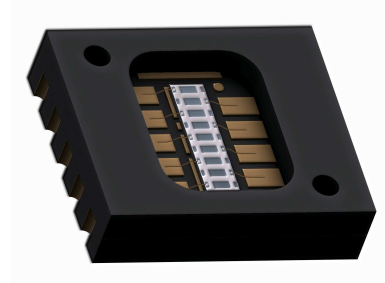
The packaging method is oriented to industrial harsh applications, which means high temperature range, high stability in time and very high uniformity of the silicon cells. The package dimensions are reduced to the minimum, in order to optimize the costs. Two reference marks are available for the precise collimator positioning.

## Applications

Optical encoders  
 Incremental encoders  
 Optical Receivers  
 Controls/drives  
 Light sensors



TOP VIEW



## Features

- High uniformity
- High gain
- Very high stability
- High transparency resin
- Small dimensions
- Designed to meet industrial specifications
- Reference points for precise mounting
- Custom design available
- 0.45 mm optical pitch
- RoHS compliant
- Compatible with OI61A

## Pin Functions

No.	Name	Function
1	AE	Phototransistor A Emitter
2	CE	Phototransistor C Emitter
3	EE	Phototransistor E Emitter
4	GE	Phototransistor G Emitter
5	IE	Phototransistor I Emitter
6	CC	Common collector
7	HE	Phototransistor H Emitter
8	FE	Phototransistor F Emitter
9	DE	Phototransistor D Emitter
10	BE	Phototransistor B Emitter

## Ordering Information

OIT5C 9 elements monolithic SMD phototransistor

## OIT5C

### ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Min	Max	Unit
$T_A$	Operating Temperature Range	-40	85	°C
$T_S$	Storage Temperature	-40	100	°C
$T_{Sol}$	Lead Temperature (solder) 3s		230	°C
$V_{R(BR)}$	Breakdown Voltage Collector-Emitter @ $T_A=25^\circ\text{C}$ $I_B=100\text{nA}$ $I_C=1\text{mA}$	50		V
$P_D$	Power Dissipation @ $T_A=25^\circ\text{C}$		150	mW

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rated conditions for extended periods may affect device reliability.

### ELECTRICAL CHARACTERISTICS

$T_A = 25^\circ\text{C}$  unless otherwise noted.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$I_D$	Dark Current	$V_R=10\text{V}$		5	100	nA
$R_\lambda$	Responsivity	$V_{CE}=5\text{V}$ $\lambda=880\text{nm}$	0.5			A/W
$\lambda_p$	Peak Responsivity	$V_{CE}=5\text{V}$		750		nm
$\Delta\lambda$	Spectral Bandwidth @ 50%	$V_{CE}=5\text{V}$	500		950	nm
$I_{ec0}$	Emitter-Collector Current	$V_{CE}=7.7\text{V}$		0.025	100	$\mu\text{A}$
$I_{ce0}$	Collector-Emitter Current	$V_{CE}=52\text{V}$		0.025	100	$\mu\text{A}$
$H_{FE}$	Gain	$V_{CC}=5\text{V}$ $I_C=2\text{mA}$	500	750	1000	
$V_{CE(sat)}$	Saturation Voltage	$I_E=2\text{mA}$ $I_B=20\mu\text{A}$		130	200	mV
$I_{C(on)}$	On-state Collector Current	$V_{CE}=5\text{V}$ $E_E=1.0\text{mW}/\text{cm}^2$		1		mA

### AC SWITCHING CHARACTERISTICS

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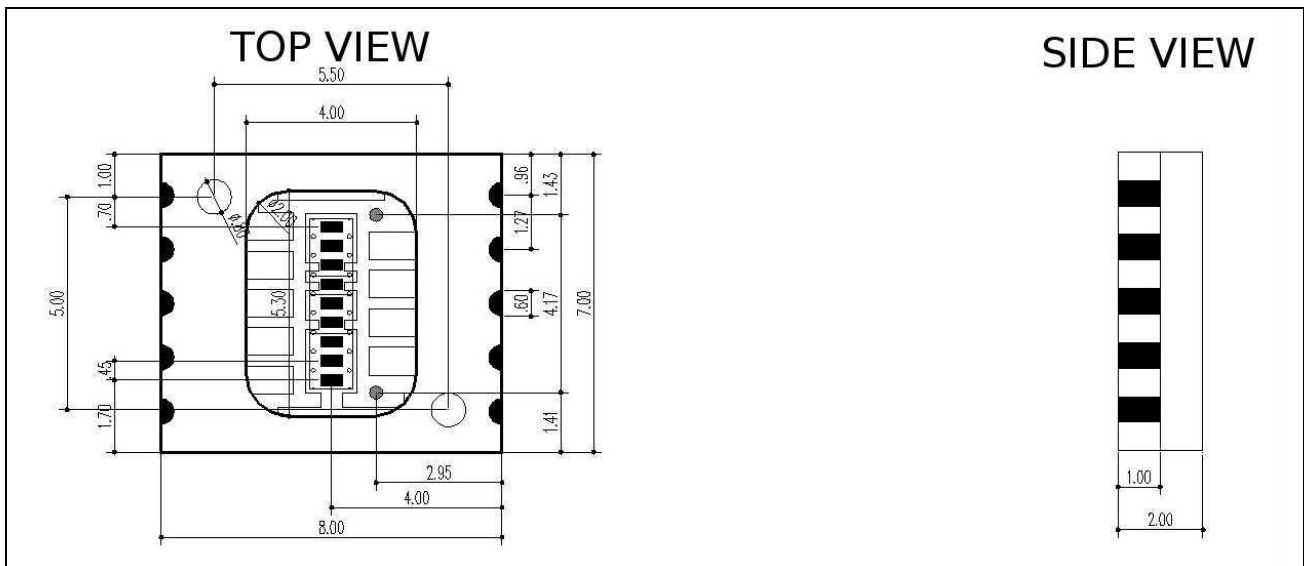
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$t_R$	Rise Time	$V_{CC}=5\text{V}$ $I_C=1\text{mA}$ $R_1=1\text{k}\Omega$		10		$\mu\text{s}$
$t_F$	Fall Time	$V_{CC}=5\text{V}$ $I_C=1\text{mA}$ $R_1=1\text{k}\Omega$		11		$\mu\text{s}$

### MECHANICAL CHARACTERISTICS

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
A	Phototransistor Active Area			0.125		$\text{mm}^2$
L	Length of the Active Area			0.25		mm
W	Width of the Active Area			0.50		mm

### MECHANICAL DIMENSIONS

Units=mm Mechanical tolerance= $\pm 0.2\text{mm}$  Die positioning tolerance= $\pm 0.030\text{mm}$



TYPICAL PERFORMANCE CURVES

Figure 1 – Normalized output voltage Vs Temperature

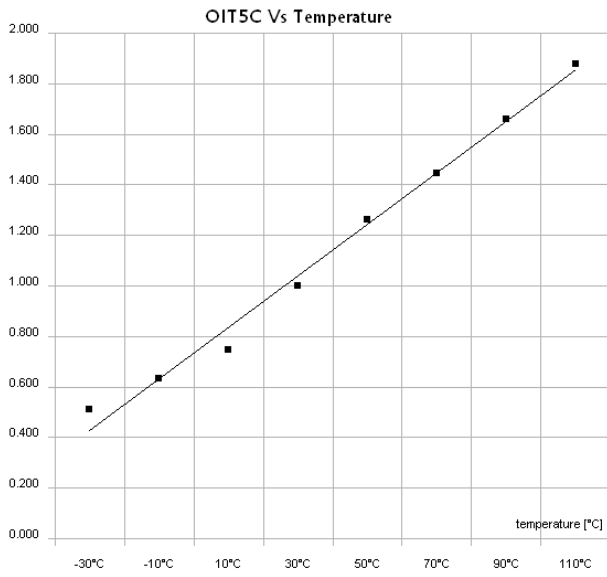


Figure 2 – Normalized spectral responsivity

