

HIGH-SPEED OPERATIONAL AMPLIFIER

■ GENERAL DESCRIPTION

The NJM318 is a precision high-speed operational amplifier, which designed for applications requiring wide bandwidth and high slew rate. They feature a factor of ten increases in speed over general purpose devices without sacrificing DC performance.

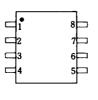
The NJM318 has internal unity gain frequency compensation. This considerably simplifies its application since no external components are necessary for operation. However, unlike most internally compensated amplifiers, external frequency compensation may be added for optimum performance. For inverting applications, feedforward compensation will boost the slew rate to over $150V/\mu s$ and almost double the bandwidth. Overcompensation can be used with the amplifier for greater stability when maximum bandwidth is not needed. Further, a single capacitor can be added to reduce the 0.1% setting time to under $1\mu s$.

The high speed and fast setting time of these op amps make them useful in A/D converters, oscillators, active filters, sample and hold circuits, or general purpose amplifiers. These devices are easy to apply and offer an order of magnitude better AC performance than industry standards such as the NJM741.

■ FEATURES

- Operating Voltage (±5V~±20V)
 Wide Unity Gain Bandwidth (15MHz typ.)
 High Slew Rate (70V/µs typ.)
- Package Outline
 DIP8,DMP8,SOP8 JEDEC 150mil
- Bipolar Technology

■ PIN CONFIGURATION

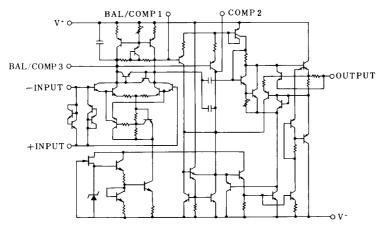


NJM318D NJM318M

PIN FUNCTION

- 1. BAL/COMP1
- 2. –INPUT
- 3. +INPUT
- 5. BAL/COMP3
- 6. OUTPUT
- 7. \
- 8. COMP2

■ EQUIVALENT CIRCUIT



■ PACKAGE OUTLINE





NJM318D

NJM318M



NJM318E

■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ⁺ √	± 20	V
Differential Input Current (note1)	I _{ID}	± 10	mA
Input Voltage (note2)	V _{IC}	± 15	V
Power Dissipation	P _D	(DIP8) 500 (DMP8) 300 (SOP8) 300	mW
Operating Temperature Range	T _{opr}	-40~+85	°C
Storage Temperature Range	T _{stg}	-40~+125	°C

■ ELECTRICAL CHARACTERISTICS

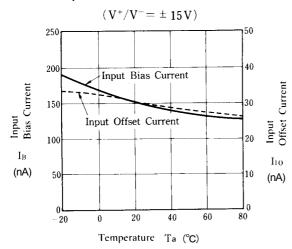
 $(Ta=+25^{\circ}C,V^{\dagger}/V=\pm15V)$

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V _{IO}		-	4	10	mV
Input Offset Current	I _{IO}		-	30	200	nA
Input Bias Current	I _{IB}		-	150	500	nΑ
Input Resistance	R _{IN}		0.5	-	-	ΜΩ
Operating Current	I _{CC}		-	5	10	mA
Large Signal Voltage Gain	A_{V}	R _L ≥2kΩ,V _O =±10V	88	106	-	dB
Slew Rate	SR	$A_V=1,R_S=10k\Omega$	50	70	-	V/µs
Unity Gain Bandwidth	f⊤		-	15	-	MHz
Input Common Mode Voltage Range	V _{ICM}		± 11.5	-	-	V
Common Mode Rejection Ratio	CMR		70	100	-	dB
Supply Voltage Rejection Ratio	SVR		65	80	-	dB
Output Voltage Swing	V_{OM}	R _L =2kΩ	± 12	± 13	-	V

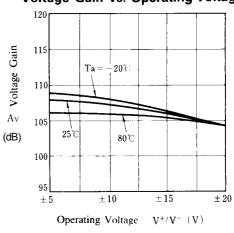
⁽ note1) A current limiting resistance is required when the input voltage is higher than 1V. (note2) For supply voltage less than ± 15 V, the absolute maximum input voltage is equal to the supply voltage.

■ TYPICAL CHARACTERISTICS

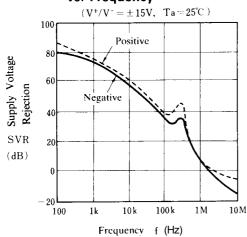
Input Bias Current, Input Offset Current vs. Temperature



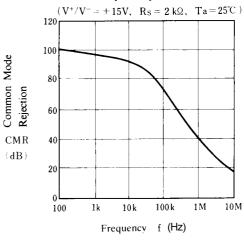
Voltage Gain vs. Operating Voltage



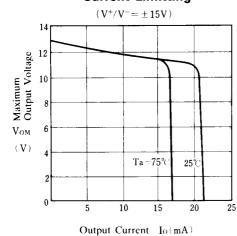
Supply Voltage Rejection vs. Frequency



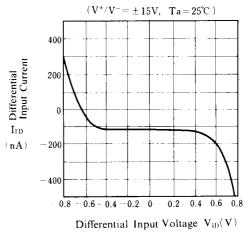
Common Mode Rejection vs. Frequency



Current Limitting

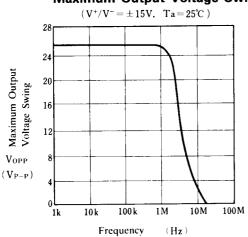


Differential Input Current vs. Differential Input Voltage

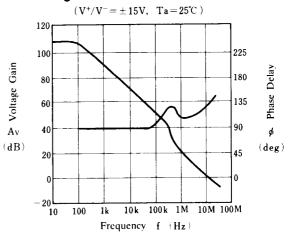


■ TYPICAL CHARACTERISTICS

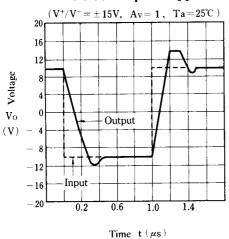
Maximum Output Voltage Swing



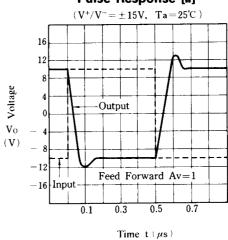
Voltage Gain, Phase vs. Frequency



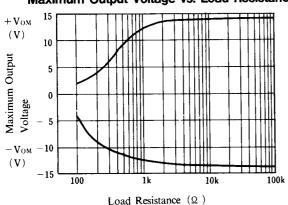
Pulse Response [I]



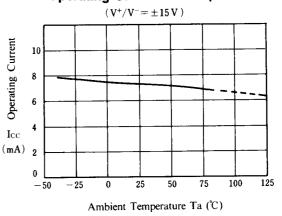
Pulse Response [II]



Maximum Output Voltage vs. Load Resistance

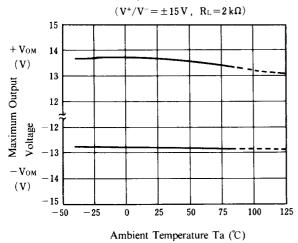


Operating Current vs. Temperature

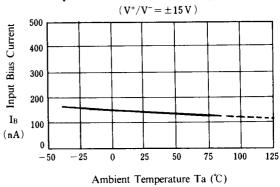


■ TYPICAL CHARACTERISTICS

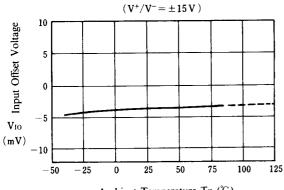
Maximum Output Voltage vs. Temperature



Input Bias Current vs. Temperature

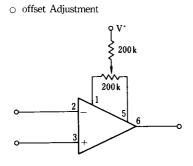


Input Offset Voltage vs. Temperature

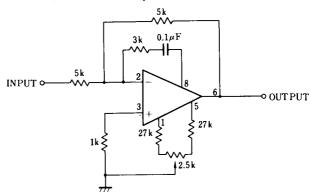


Ambient Temperature Ta (℃)

■ ADJUSTMENT METHOD



O Feedforward Compensation



[CAUTION]

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