

Low Noise, High-Speed Dual Operational Amplifier

■ GENERAL DESCRIPTION

The NJM2719 is a high speed voltage feedback dual operational amplifier specifically optimized for low voltage noise.

Combining a voltage noise of $2.5\text{nV}/\sqrt{\text{Hz}}$ typ. (at $f=100\text{kHz}$) and unity gain of 100MHz , the NJM2719 is ideal for I/Q baseband amplifier, RFID reader application and other wireless communication system designs.

The NJM2719 is available in three 8-lead package options: surface mount (DMP), tiny fine pitch surface mount (SSOP/MSOP).

■ PACKAGE OUTLINE



NJM2719M
(DMP8)



NJM2719V
(SSOP8)



NJM2719RB1
(MSOP8(TVSP8))

■ FEATURES

- Low Noise
- Unity Gain Bandwidth
- Phase Margin
- Slew Rate
- Output Rail-to-Rail
- Operating Voltage
- Bipolar Technology
- Package Outline

$V_{ni} = 2.5\text{nV}/\sqrt{\text{Hz}}$ typ. (at $f=100\text{kHz}$)

$V_{ni} = 3\text{nV}/\sqrt{\text{Hz}}$ typ. (at $f=10\text{kHz}$)

$f_T = 100\text{MHz}$ typ. (at $V^+/V^- = \pm 5\text{V}$)

$f_T = 90\text{MHz}$ typ. (at $V^+/V^- = \pm 2.5\text{V}$)

$\Phi_m = 60\text{deg}$ typ.

$60\text{V}/\mu\text{s}$ typ. (at $V^+/V^- = \pm 5\text{V}$)

$35\text{V}/\mu\text{s}$ typ. (at $V^+/V^- = \pm 2.5\text{V}$)

$V_{OH} \geq +4.7\text{V}$, $V_{OL} \leq -4.8\text{V}$ (at $V^+/V^- = \pm 5\text{V}$)

$V_{OH} \geq +2.4\text{V}$, $V_{OL} \leq -2.4\text{V}$ (at $V^+/V^- = \pm 2.5\text{V}$)

$\pm 2.25\text{V} \sim \pm 5\text{V}$

NJM2719M : DMP8

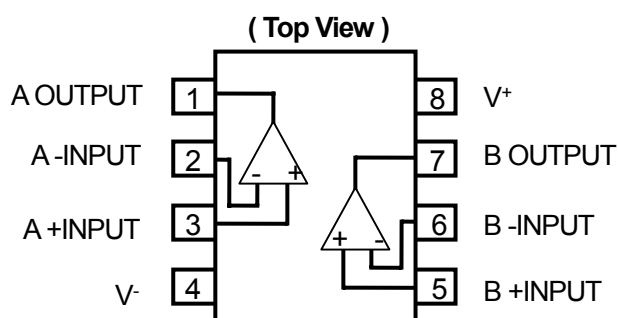
NJM2719V : SSOP8

NJM2719RB1 : MSOP8 (TVSP8) MEET JEDEC MO-187-DA / THIN TYPE

■ APPLICATION

- Wireless Communication Equipment
- I/Q Baseband Application
- RFID Reader Application
- Active Filter
- ADC/DAC Buffer
- Ultrasound Amplifier

■ PIN CONFIGURATION



DMP8 [NJM2719M]

SSOP8 [NJM2719V]

MSOP8(TVSP8) [NJM2719RB1]

■ ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ⁺	±5.5	V
Common Mode Input Voltage Range	V _{ICM}	±5.5 (Note1)	V
Differential Input Voltage Range	V _{ID}	±3	V
Power Dissipation	P _D	370[DMP8], 310[SSOP8], 400[MSOP8(TVSP8)]	mW
		470[DMP8](Note2), 410[SSOP8](Note2), 510[MSOP8(TVSP8)] (Note2)	mW
Operating Temperature Range	T _{opr}	-40 to +85	°C
Storage Temperature Range	T _{stg}	-50 to +150	°C

(Note 1) The output voltage of normal operation will be the Output Voltage Swing of electrical characteristics.

(Note 2) On the PCB " EIA/JEDEC (114.3x76.2x1.57mm, two layers, FR-4) "

(Note 3) Do not exceed "Power dissipation: PD" in which power dissipation in IC is shown by the absolute maximum rating.

Refer to following Figure 1 for a permissible loss when ambient temperature (Ta) is Ta ≥ 25°C.

Figure1A: Power Dissipation – Ambient Temperature

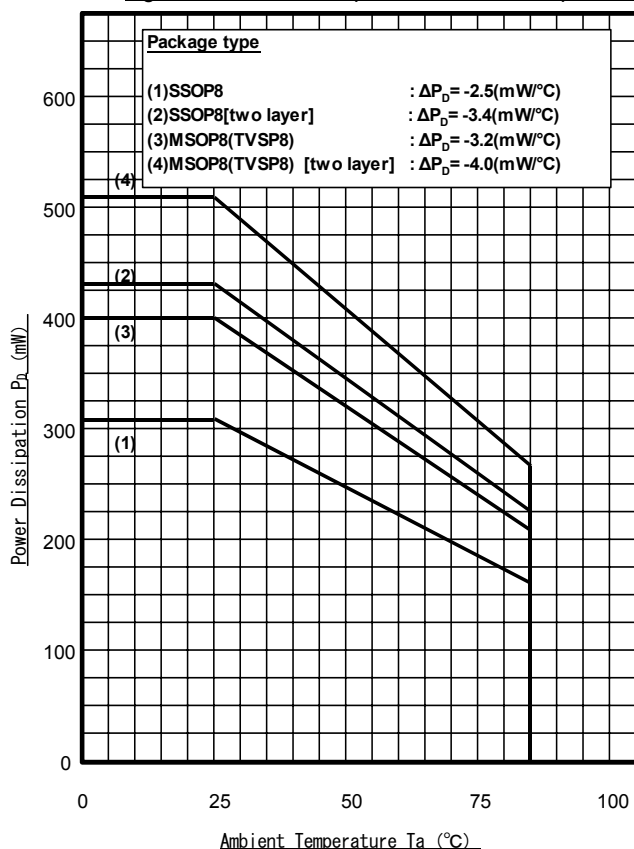
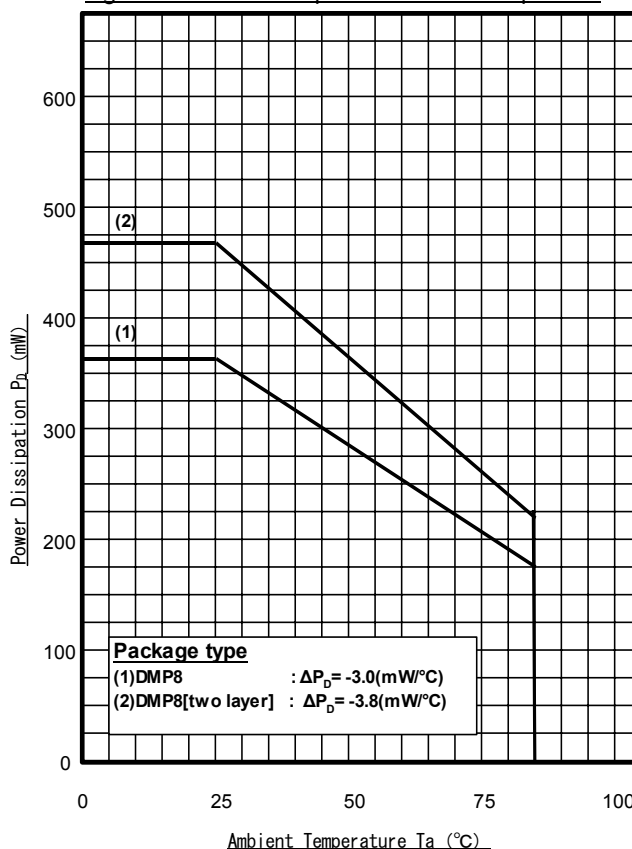


Figure1B: Power Dissipation – Ambient Temperature



■ OPERATING VOLTAGE (Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage	V^+ / V^-	(Note3)	±2.25	-	±5.5	V

■ ELECTRICAL CHARACTERISTICS

●DC CHARACTERISTICS ($V^+ / V^- = \pm 2.5V$, Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Current	I _{CC}	No Signal	-	11	14	mA
Input Offset Voltage	V _{IO}	R _s =50Ω	-	1	9	mV
Input Offset Voltage Drift	ΔV _{IO} /ΔT	R _s =50Ω	-	10	-	μV/deg
Input Bias Current	I _B		-	2.9	25	μA
Input Offset Current	I _{IO}		-	0.2	2	μA
Voltage Gain	A _v	R _L = 1kΩ to 0V, V _o = ±1V	68	91	-	dB
Common Mode Rejection Ratio	CMR	-2V ≤ V _{CM} ≤ +1.2V	82	92	-	dB
Supply Voltage Rejection Ratio	SVR	±2.25V ≤ V ⁺ /V ⁻ ≤ ±5V	84	97	-	dB
Maximum Output Voltage 1	V _{OH1}	R _L = 1kΩ to 0V	+2.3	+2.4	-	V
	V _{OL1}		-	-2.4	-2.3	
Maximum Output Voltage 2	V _{OH2}	I _{source} =4mA, +Input =+0.1V, -Input =-0.1V	+2.2	+2.3	-	V
	V _{OL2}		-	-2.3	-2.2	
Common Mode Input Voltage Range	V _{ICM+}	CMR≥82dB	+1.2	-	-	V
	V _{ICM-}		-	-	-2	

●AC CHARACTERISTICS ($V^+ / V^- = \pm 2.5V$, Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Unity Gain	f _T	A _v =+40dB,	-	90	-	MHz
Phase Margin	φ _m	R _f =1.98kΩ, R _g =20Ω,	-	60	-	deg
Gain Margin	G _m	R _L =1kΩ to 0V, C _L =5pF	-	10	-	dB
Equivalent Input Noise Voltage	V _{NI1}	f=100kHz	-	2.5	-	nV/√Hz
	V _{NI2}	f=10kHz	-	3	-	
Equivalent Input Noise Current	I _{NI}	f=100kHz	-	3	-	pA/√Hz
Channel Separation	CS	f=1MHz, V _{in} =0.2Vpp, A _v =+1, R _L =1kΩ to 0V, C _L =5pF	-	70	-	dB

●TRANSIENT CHARACTERISTICS ($V^+ / V^- = \pm 2.5V$, Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Slew Rate 1	+SR1	A _v =0dB, R _L =1kΩ to 0V, C _L =5pF, V _{out} =2Vpp	-	35	-	V/μs
	-SR1		-	35	-	
Slew Rate 2	+SR2	A _v =+6dB, R _L =1kΩ to 0V, C _L =5pF, V _{out} =2Vpp	-	30	-	V/μs
	-SR2		-	30	-	
Rise Time	t _r	A _v =+6dB, R _L =1kΩ to 0V, C _L =5pF, V _{out} =0.2Vpp, 10% to 90%	-	8.3	-	ns
Fall Time	t _f		-	8.3	-	
Power Band Width	PBW	A _v =+6dB, R _L =1kΩ to 0V, C _L =5pF, V _{out} =2Vpp, HD2 ≤40dB, HD3 ≤40dB	-	3	-	MHz
Total Harmonic Distortion	THD	A _v =+6dB, R _L =1kΩ to 0V, C _L =5pF, f=10kHz, V _{out} =2Vpp	-	0.1	-	%
Second Harmonic	HD2	A _v =+6dB, R _L =1kΩ to 0V, C _L =5pF, f=1MHz, V _{out} =2Vpp	-	-50	-	dBc
Third Harmonic	HD3		-	-50	-	
Settling time (1%)	ts1	A _v =+6dB, R _L =1kΩ to 0V, C _L =5pF, V _{out} =2Vpp	-	100	-	ns
Settling time (0.1%)	ts2		-	110	-	

●DC CHARACTERISTICS ($V^+V^- = \pm 5V$, $T_a = 25^\circ C$)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Current	I_{CC}	No Signal	-	14	17	mA
Input Offset Voltage	V_{IO}	$R_s = 50\Omega$	-	1	9	mV
Input Offset Voltage Drift	$\Delta V_{IO}/\Delta T$	$R_s = 50\Omega$	-	10	-	$\mu V/deg$
Input Bias Current	I_B		-	2.9	25	μA
Input Offset Current	I_{IO}		-	0.2	2	μA
Voltage Gain	A_v	$R_L = 1k\Omega$ to 0V, $V_o = \pm 1V$	70	91	-	dB
Common Mode Rejection Ratio	CMR	$-4.5V \leq V_{CM} \leq +3.7V$	82	92	-	dB
Supply Voltage Rejection Ratio	SVR	$\pm 2.25V \leq V^+V^- \leq \pm 5V$	84	97	-	dB
Maximum Output Voltage 1	V_{OH1}	$R_L = 1k\Omega$ to 0V	+4.6	+4.7	-	V
	V_{OL1}		-	-4.8	-4.7	
Maximum Output Voltage 2	V_{OH2}	IsOURCE = 5mA, +Input = +0.1V, -Input = -0.1V	+4.5	+4.6	-	V
	V_{OL2}		-	-4.7	-4.6	
Common Mode Input Voltage Range	V_{ICM+}	CMR ≥ 82 dB	+3.7	-	-	V
	V_{ICM-}		-	-	-4.5	

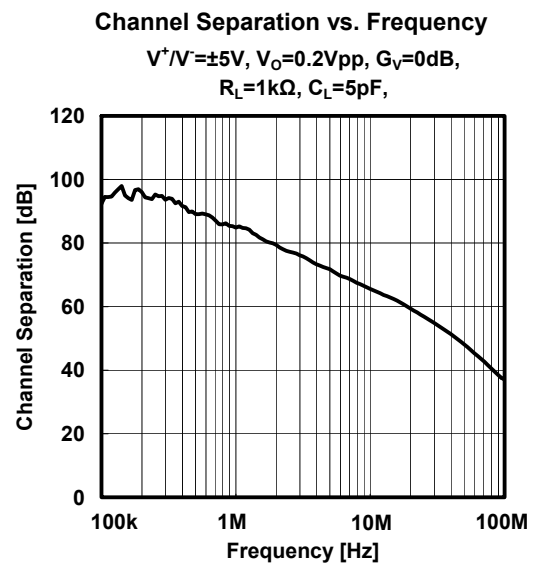
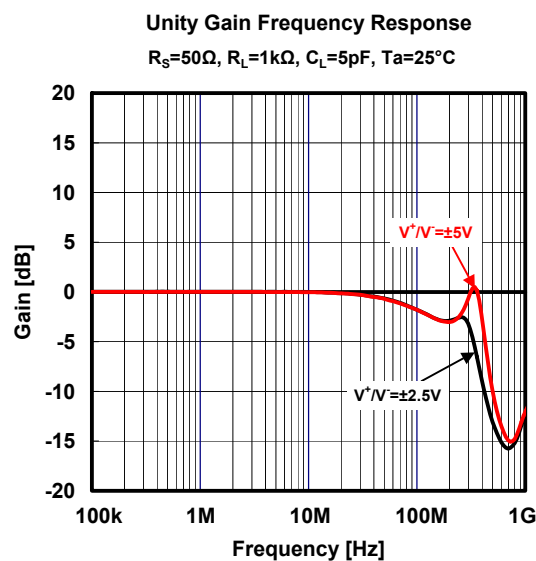
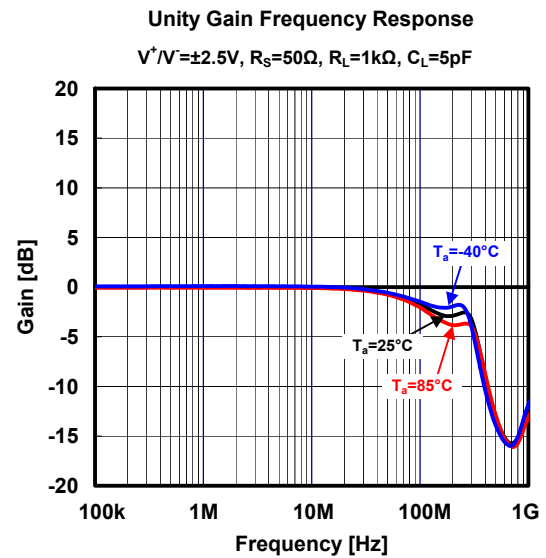
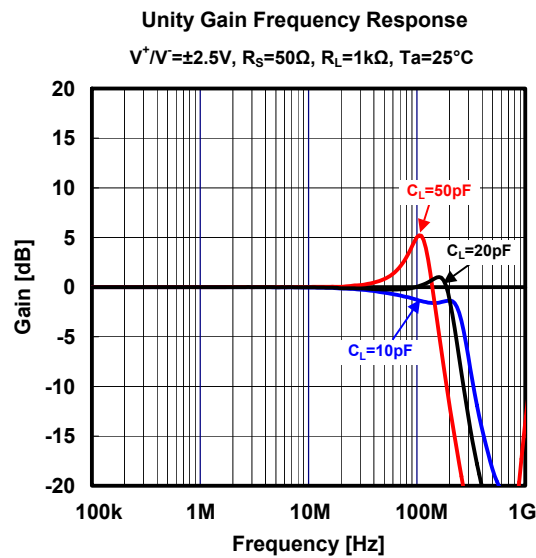
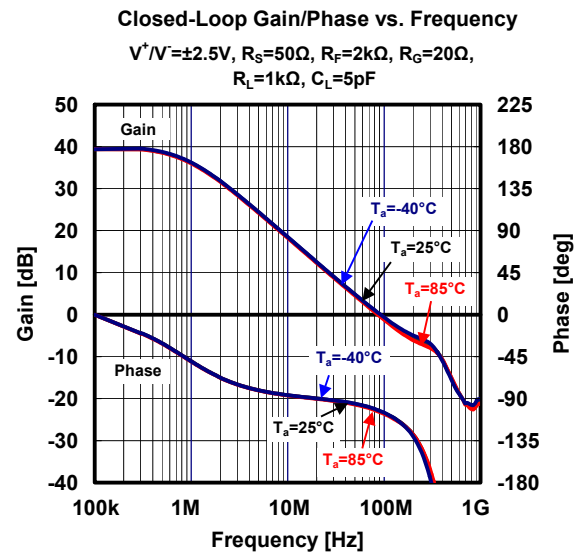
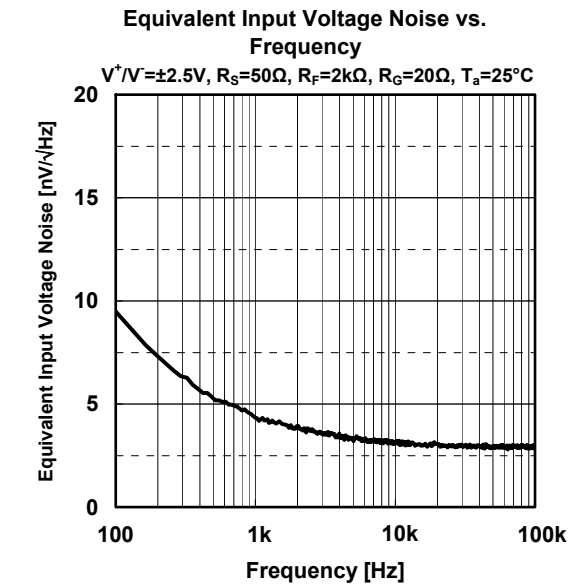
●AC CHARACTERISTICS ($V^+V^- = \pm 5V$, $T_a = 25^\circ C$)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Unity Gain	fT	$A_v = +40$ dB,	-	100	-	MHz
Phase Margin	ϕ_m	$R_f = 1.98k\Omega$, $R_g = 20\Omega$,	-	60	-	deg
Gain Margin	Gm	$R_L = 1k\Omega$ to 0V, $C_L = 5pF$	-	10	-	dB
Equivalent Input Noise Voltage	V_{NI1}	f = 100kHz	-	2.5	-	$nV/\sqrt{Hz}$
	V_{NI2}	f = 10kHz	-	3	-	
Equivalent Input Noise Current	I_{NI}	f = 100kHz	-	3	-	$pA/\sqrt{Hz}$
Channel Separation	CS	f = 1MHz, $V_{in} = 0.2V_{pp}$, $A_v = +1$, $R_L = 1k\Omega$ to 0V, $C_L = 5pF$	-	70	-	dB

●TRANSIENT CHARACTERISTICS ($V^+V^- = \pm 5V$, $T_a = 25^\circ C$)

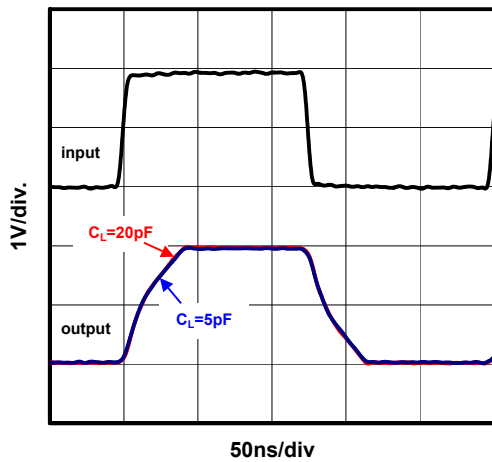
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Slew Rate 1	+SR1	$A_v = 0$ dB, $R_L = 1k\Omega$ to 0V, $C_L = 5pF$, $V_{out} = 5V_{pp}$	-	60	-	V/ μs
	-SR1		-	60	-	
Slew Rate 2	+SR2	$A_v = +6$ dB, $R_L = 1k\Omega$ to 0V, $C_L = 5pF$, $V_{out} = 5V_{pp}$	-	55	-	V/ μs
	-SR2		-	55	-	
Rise Time	t_r	$A_v = +6$ dB, $R_L = 1k\Omega$ to 0V, $C_L = 5pF$, $V_{out} = 0.2V_{pp}$, 10% to 90%	-	8	-	ns
Fall Time	t_f		-	8	-	
Power Band Width	PBW	$A_v = +6$ dB, $R_L = 1k\Omega$ to 0V, $C_L = 5pF$, $V_{out} = 2V_{pp}$, HD2 ≤ 40 dB, HD3 ≤ 40 dB	-	4	-	MHz
Total Harmonic Distortion	THD	$A_v = +6$ dB, $R_L = 1k\Omega$ to 0V, $C_L = 5pF$, f = 10kHz, $V_{out} = 2V_{pp}$	-	0.1	-	%
Second Harmonic	HD2	$A_v = +6$ dB, $R_L = 1k\Omega$ to 0V, $C_L = 5pF$, f = 1MHz, $V_{out} = 2V_{pp}$	-	-50	-	dBc
Third Harmonic	HD3		-	-50	-	
Settling time (1%)	ts1	$A_v = +6$ dB, $R_L = 1k\Omega$ to 0V, $C_L = 5pF$, $V_{out} = 2V_{pp}$	-	90	-	ns
Settling time (0.1%)	ts2		-	110	-	

TYPICAL CHARACTERISTICS



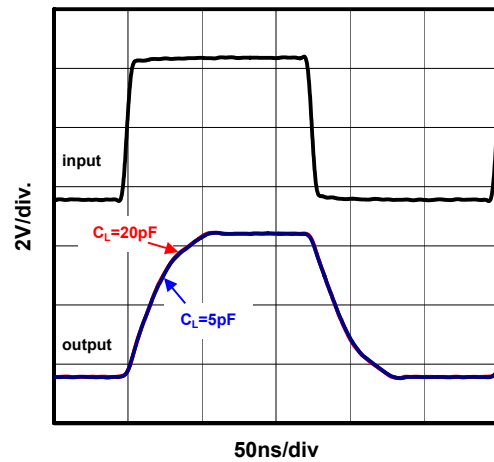
Transinet Response

$V^+/V^- = \pm 2.5V$, $f = 4MHz$, $V_O = 2V_{PP}$, $G_V = 1$, $R_T = 50\Omega$,
 $R_L = 1k\Omega$, $T_a = 25^\circ C$



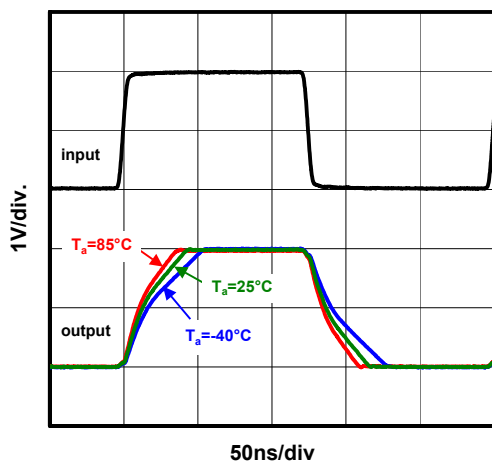
Transient Response

$V^+/V^- = \pm 5V$, $f = 4MHz$, $V_O = 5V_{PP}$, $G_V = 1$, $R_T = 50\Omega$,
 $R_L = 1k\Omega$, $T_a = 25^\circ C$



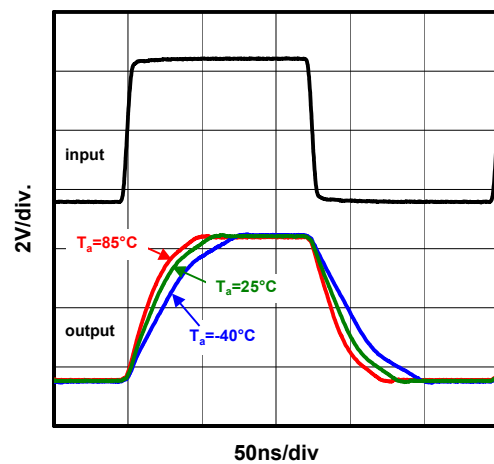
Transeint Response

$V^+/V^- = \pm 2.5V$, $f = 4MHz$, $V_O = 2V_{PP}$, $G_V = 1$, $R_T = 50\Omega$,
 $R_L = 1k\Omega$, $C_L = 5pF$



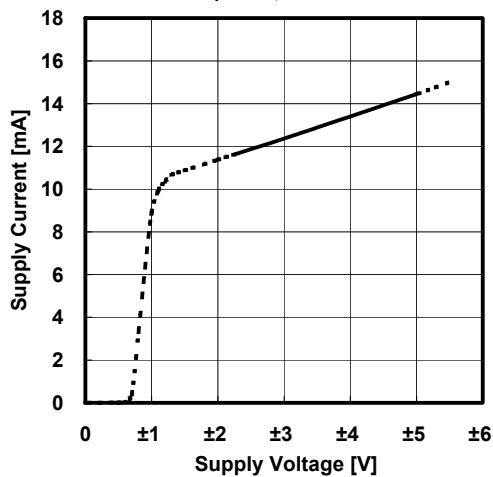
Transient Response

$V^+/V^- = \pm 5V$, $f = 4MHz$, $V_O = 5V_{PP}$, $G_V = 1$, $R_T = 50\Omega$,
 $R_L = 1k\Omega$, $C_L = 5pF$



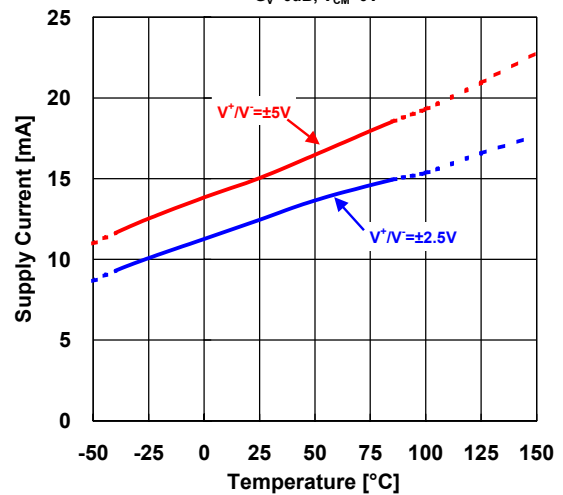
Supply Current vs. Supply Voltage

$G_V = 0dB$, $T_a = 25^\circ C$

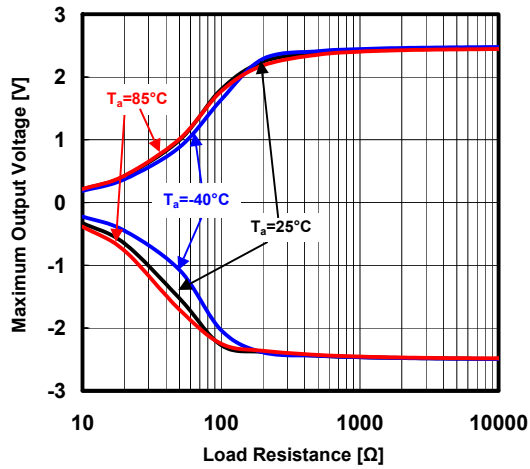


Supply Current vs. Temperature

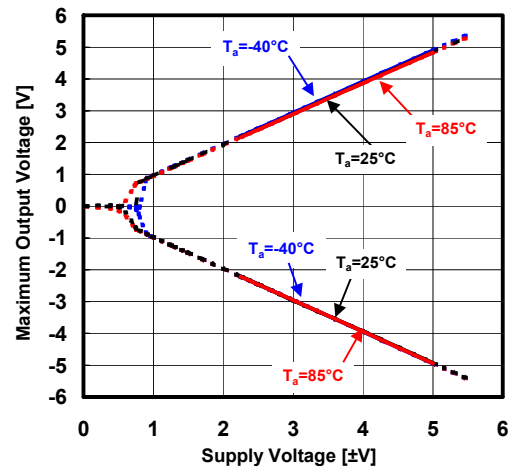
$G_V = 0dB$, $V_{CM} = 0V$



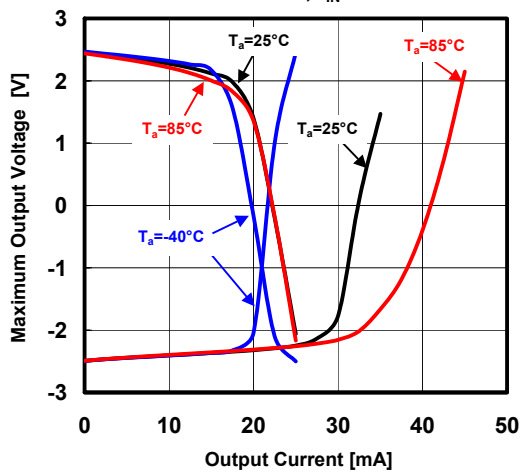
Maximum Output Voltage
vs. Load Resistance
 $V^+/V^- = \pm 2.5V, V_{IN} = \pm 0.2V$



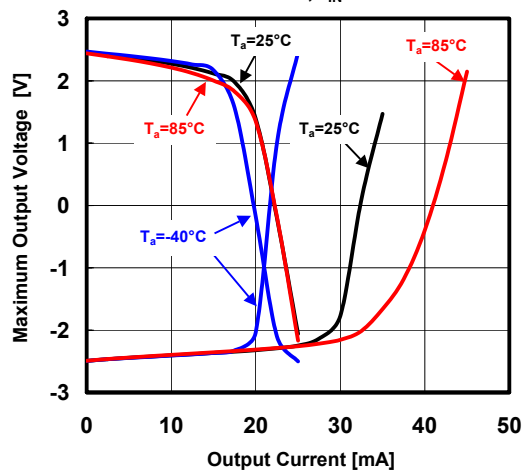
Maximum Output Voltage
vs. Supply Voltage
 $V_{IN} = \pm 0.2V, R_L = 1k\Omega$



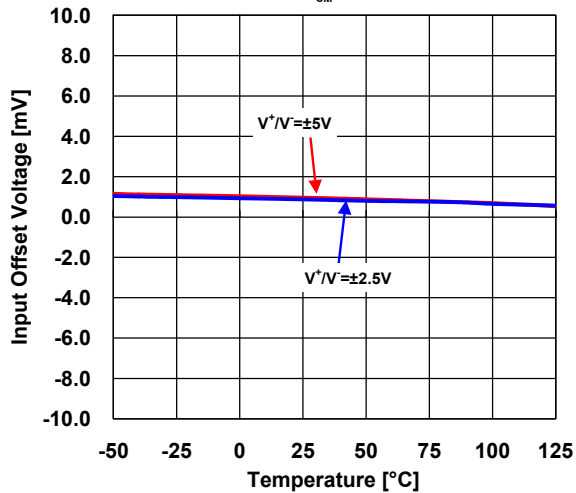
Maximum Output Voltage
vs. Output Current
 $V^+/V^- = \pm 2.5V, V_{IN} = \pm 0.2V$



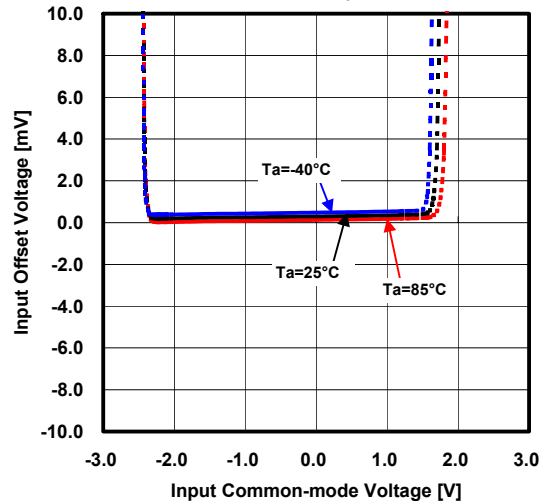
Maximum Output Voltage
vs. Output Current
 $V^+/V^- = \pm 2.5V, V_{IN} = \pm 0.2V$

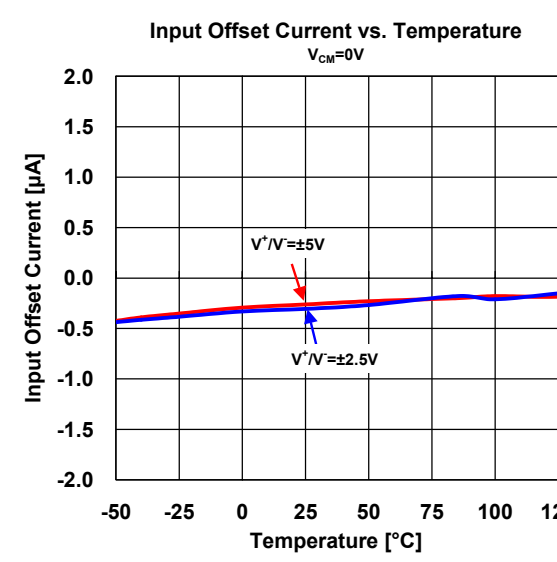
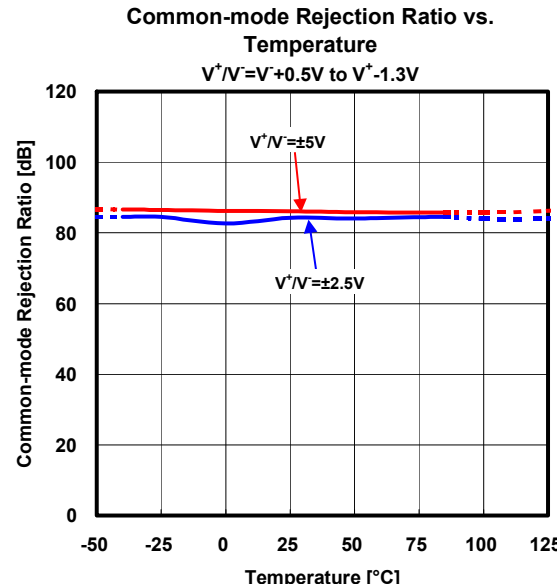
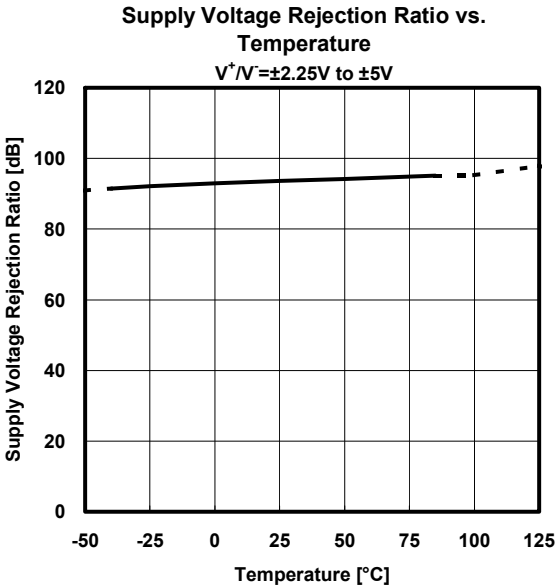
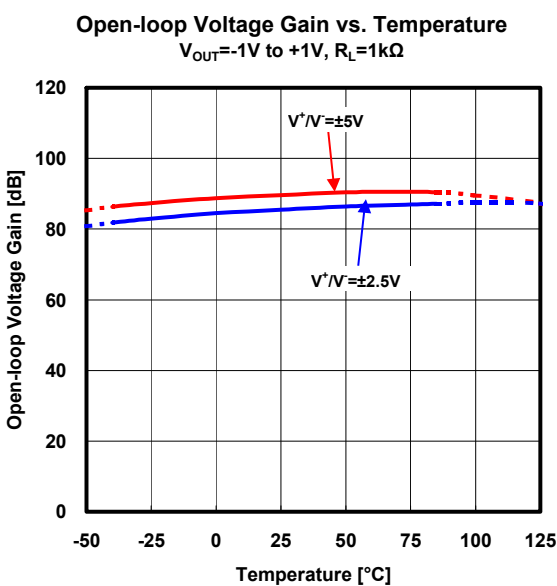


Input Offset Voltage vs. Temperature
 $V_{CM} = 0V$



Input Offset Voltage
vs. Input Common-mode Voltage
 $V^+/V^- = \pm 2.5V$





■ APPLICATION

● Stability

Generally, when driving a large capacitive load in low closed-loop gain or unity-gain configurations, circuit stability is reduced. In the case of using the NJM2719 for these configurations, it is necessary to care about unwanted oscillation.

An effective way to improve stability and to avoid oscillation is to add an isolation resistor as shown in Figure 1.

Figure 2 shows required resistor values (R_{ISO}) for stability versus load capacitances (C_L) in the unity-gain configuration (Figure 1). To ensure the stability, add a larger isolation resistor in Figure 2. (Resistor values in Figure 2 are reference values when parasitic capacitance of an evaluation board is minimized.)

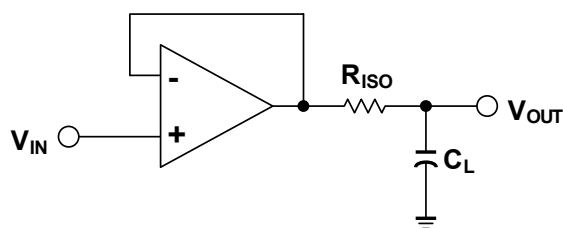


Figure 1.

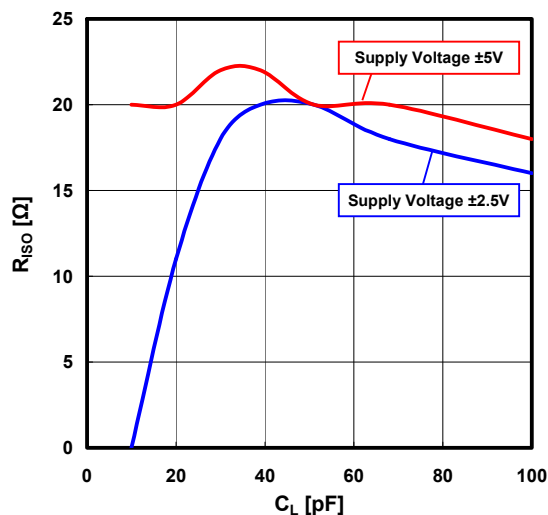


Figure 2. Required Isolation Resistor values for stability, R_{ISO} [Ω], versus Capacitive Loads, C_L [pF]. ($G_V=0$ dB)

■ NOTE

[CAUTION]

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