

# **Supervisory Circuit**

#### **Features**

- Highly accurate: ±1.5% (25°C)
- Accurate power monitoring: 2.5V, 2.9V, 3.0V (PT7M1818), and 4.1, 4.3, 4.6V (PT7M1813)
- Operating voltage range: 1.0V ~ 5.5V
- Operating temperature range: -40°C to +85°C
- Detect voltage temperature characteristics:  $\pm 2.5\% \times TYP$
- Efficient operation output with internal  $5k\Omega$  pull-up resistor
- Maintains reset for 200ms after V<sub>CC</sub> returns to an intolerance condition

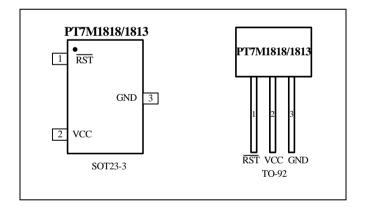
### **Description**

The series are designed to monitor power supplies in  $\mu P$  and digital systems. It provides excellent circuit reliability and low cost by eliminating external components and adjustments.

This device performs a single function: it asserts a reset signal whenever the  $V_{CC}$  supply voltage falls below a preset threshold. Reset remains asserted for 200ms after  $V_{CC}$  has risen above the reset threshold.

PT7M1818/1813 are bidirectional output, allowing it to be directly connected to  $\mu P$  with bidirectional reset inputs.

### **Pin Configuration**



### **Pin Description**

| Name     | Туре | Description   |
|----------|------|---|
| RST      | I/O  | Reset Output and Pushbutton Input: $\overline{RST}$ is asserted when $V_{CC}$ drops below voltage threshold $V_{TH-}$ . Active low. When other devices pull $\overline{RST}$ low, the reset condition occurs and will remain a reset timeout period after the external signal is off. |
| GND      | P    | Ground  |
| $V_{CC}$ | P    | Supply Voltage.   |



## **Maximum Ratings**

| Storage Temperature                              | 65°C to +150°C                |
|--|-------------------------------|
| Ambient Temperature with Power Applied           | 40°C to +85°C                 |
| Supply Voltage to Ground Potential (Vcc to GND)  | 0.3V to +7.0V                 |
| DC Input Voltage (All inputs except Vcc and GND) | 0.3V to V <sub>CC</sub> +0.3V |
| DC Output Current (All outputs)                  | 30mA                          |
| Power Dissipation                                | (Depend on package)           |
|  |                               |

#### Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

### **DC** Electrical Characteristics

 $(T_A = -40 \sim 85$ °C, unless otherwise noted. Typical values are at  $T_A = +25$ °C)

| Description               |                   | Sym.            | Test Conditions                       | Min                           | Тур       | Max                           | Unit |
|---------------------------|-------------------|-----------------|---------------------------------------|-------------------------------|-----------|-------------------------------|------|
| Supply Volta              | age               | V <sub>CC</sub> |                                       | 1.0                           | -         | 5.5                           | V    |
| G1 . C                    | 4                 | T               | $V_{CC} = 5.5V$ . No load.            | -                             | -         | 12                            | μΑ   |
| Supply Curr               | ent               | $I_{CC}$        | $V_{CC} = 3.6V$ . No load.            | -                             | -         | 10                            | μΑ   |
|                           |                   | $V_{	ext{TH-}}$ | +25°C                                 | (V <sub>TH-</sub> )<br>×0.985 | $V_{TH-}$ | (V <sub>TH-</sub> )<br>×1.015 | V    |
| Voltage Info              | Voltage Threshold |                 | -40°C~85°C                            | (V <sub>TH-</sub> )<br>×0.975 | $V_{TH-}$ | (V <sub>TH-</sub> )<br>×1.025 | V    |
| Hysteresis                |                   | $V_{HYS}$       | V <sub>TH+</sub> - V <sub>TH-</sub> * | -                             | 50        | -                             | mV   |
|                           |                   |                 | $I_{OH} = 8mA$ , $V_{CC} = 5V$        | -                             | -         | 0.4                           |      |
| Output<br>Driving         | Output low        | V <sub>OL</sub> | $I_{OH} = 4mA$ , $V_{CC} = 3V$        | -                             | -         | 0.3                           | V    |
|                           |                   |                 | $I_{OH} = -50 \mu A, V_{CC} = 1 V$    | -                             | -         | 0.09                          |      |
| Internal pull-up resistor |                   | Rp              | RST                                   | 3.75                          | 5         | 6.25                          | kΩ   |

Note: V<sub>TH-</sub> is voltage threshold when V<sub>CC</sub> falls from high to low. V<sub>TH+</sub> is voltage threshold when V<sub>CC</sub> rises from low to high.



### **AC Electrical Characteristics**

Fig 1. Timing diagram

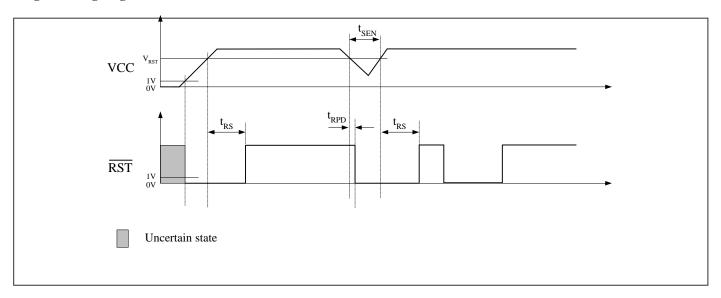
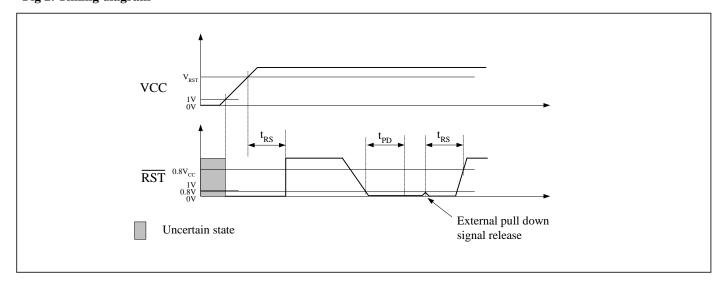


Fig 2. Timing diagram



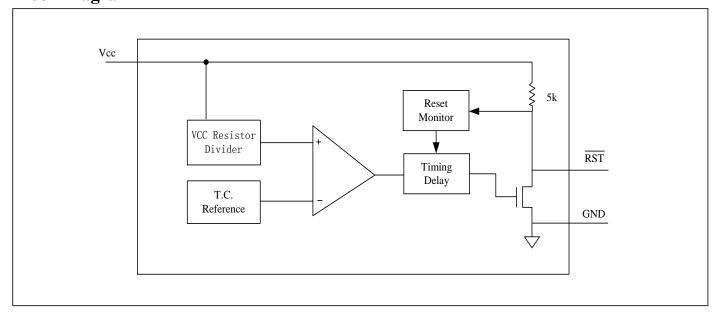
 $(V_{CC} = 1.0 \text{V to } 5.5 \text{V}, T_A = -40 \sim 85 ^{\circ}\text{C}, \text{ unless otherwise noted. Typical values are at } T_A = +25 ^{\circ}\text{C})$ 

| Sym.             | Description                     | Test Conditions | Min | Тур | Max | Unit |
|------------------|---------------------------------|-----------------|-----|-----|-----|------|
| t <sub>RS</sub>  | Reset Timeout Period            | -               | 140 | 200 | 280 | μs   |
| t <sub>RPD</sub> | Delay                           | -               | -   | 17  | -   | μs   |
| t <sub>SEN</sub> | Sensitivity                     | -               | 20  | -   | -   | μs   |
| t <sub>PD</sub>  | External Pull Down Signal Pulse | RST pin         | 1   | -   | -   | μs   |
|                  | Pushbutton Detect*              | RST pin         | 0.8 | 1.5 | 2.0 | V    |

<sup>\*</sup>Note: RST will be asserted when it is pull down to the typical value or less.



## **Block Diagram**



## **Function Description**

#### **Power Monitor**

A microprocessor's ( $\mu$ P's) reset input starts the  $\mu$ P in a known state. Whenever the  $\mu$ P is in an unknown state, it should be held in reset. The supervisory circuits assert reset during power-up and prevent code execution errors during power-down or brownout conditions.

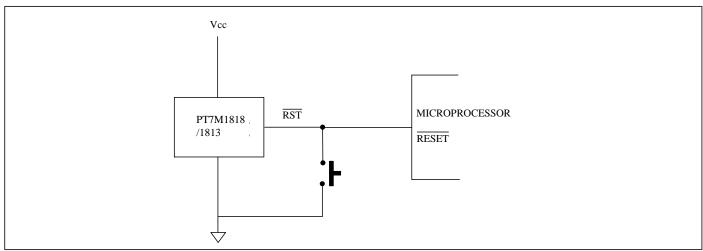
On power-up, once Vcc reaches about 1.0V,  $\overline{RST}$  is a guaranteed logic low of 0.4V or less. As Vcc rises,  $\overline{RST}$  stays low. When Vcc rises above the reset threshold  $V_{RST}$ , an internal timer releases  $\overline{RST}$  after about 200ms.  $\overline{RST}$  asserts whenever Vcc drops below the reset threshold, i.e. brownout condition. If brownout occurs in the middle of a previously initiated reset pulse, the pulse continues for at least another 200ms. On power-down, once Vcc falls below the reset threshold,  $\overline{RST}$  stays low and is guaranteed to be 0.4V or less until Vcc drops below 1V.

#### **Reset Output: Bi-direction**

The devices provide  $\overline{RST}$  output pin for a pushbutton switch. When the devices are not in a reset cycle, it continuously monitors the  $\overline{RST}$  signal for a low going edge. If an edge is detected, the devices will debounce the switch by pulling the  $\overline{RST}$  line low. After the internal timer has expired, the devices will continue to monitor the  $\overline{RST}$  line. If the line is still low, they will continue to monitor the line looking for a rising edge. Upon detecting a release, they will force the  $\overline{RST}$  line low and hold it low for 200ms.

### **Application Information**

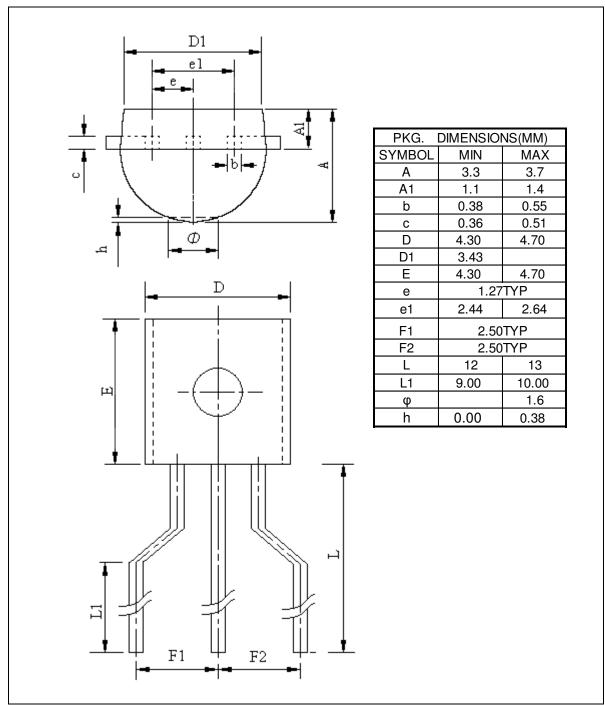
#### **Typical Operation Circuit**





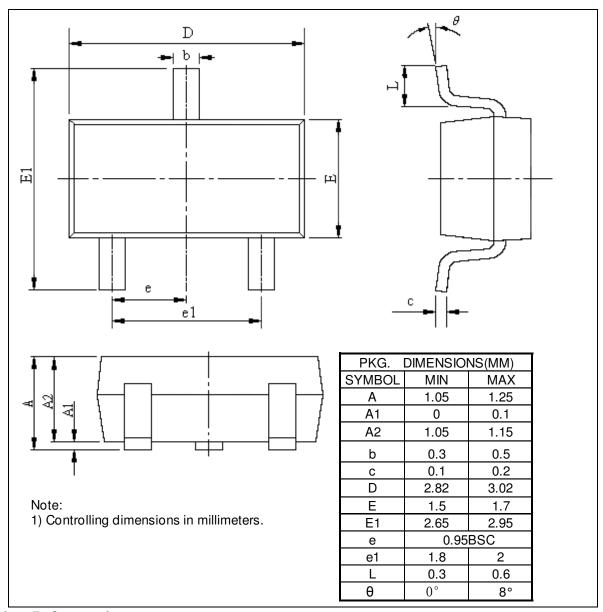
## **Mechanical Information**

TO92-3





#### SOT23-3



# **Ordering Information**

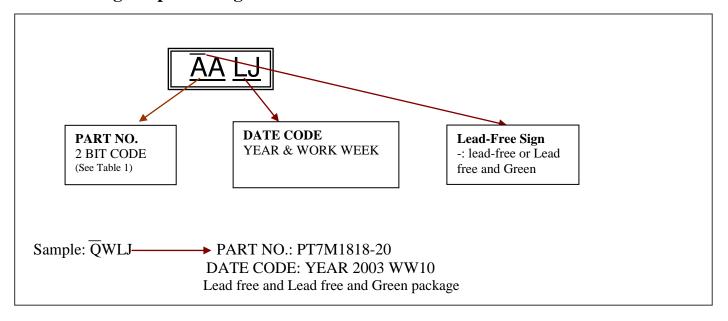
| Part Number    | Package Code | Package                     |  |
|----------------|--------------|-----------------------------|--|
| PT7M1818-20TE  | T            | Lead free and Green SOT23-3 |  |
| PT7M1818-20NBE | NB           | Lead free TO92              |  |
| PT7M1818-10TE  | T            | Lead free and Green SOT23-3 |  |
| PT7M1818-10NBE | NB           | Lead free TO92              |  |
| PT7M1818-5TE   | T            | Lead free and Green SOT23-3 |  |
| PT7M1818-5NBE  | NB           | Lead free TO92              |  |
| PT7M1813-15TE  | T            | Lead free and Green SOT23-3 |  |
| PT7M1813-15NBE | NB           | Lead free TO92              |  |
| PT7M1813-10TE  | T            | Lead free and Green SOT23-3 |  |
| PT7M1813-10NBE | NB           | Lead free TO92              |  |
| PT7M1813-5TE   | T            | Lead free and Green SOT23-3 |  |
| PT7M1813-5NBE  | NB           | Lead free TO92              |  |

#### Note:

- E = Pb-free or Pb-free and Green
- Adding X Suffix= Tape/Reel
- Contact Pericom for availability.



## **Small Package Top Marking Instruction**



**Table 1. Function comparison** 

|      | Reset Output |                               |            |                     |            |   |     |
|------|--------------|-------------------------------|------------|---------------------|------------|---|-----|
| Item | Part No.     | Part No. Open-Drain Push-Pull |            | Push-Pull Bi-direct |            | $\mathbf{V}_{\mathbf{TH}-}(\mathbf{V})$ |     |
|      |              | Active high                   | Active low | Active high         | Active low | Active low                              |     |
| 1    | PT7M1818-20  | -                             | -          | -                   | -          | $\checkmark$                            | 2.5 |
| 2    | PT7M1818-10  | -                             | -          | -                   | -          | $\sqrt{}$                               | 2.9 |
| 3    | PT7M1818-5   | -                             | -          | -                   | -          | $\sqrt{}$                               | 3.0 |
| 4    | PT7M1813-15  | -                             | -          | -                   | -          | $\sqrt{}$                               | 4.1 |
| 5    | PT7M1813-10  | -                             | -          | -                   | -          | $\sqrt{}$                               | 4.3 |
| 6    | PT7M1813-5   | -                             | -          | -                   | -          | $\sqrt{}$                               | 4.6 |

**Table 2 Marking code for products** 

| No. | Part No.    | Code |
|-----|-------------|------|
| 1   | PT7M1818-20 | QW   |
| 2   | PT7M1818-10 | RA   |
| 3   | PT7M1818-5  | RB   |
| 4   | PT7M1813-15 | RL   |
| 5   | PT7M1813-10 | RO   |
| 6   | PT7M1813-5  | RR   |

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