

LDO Regulator

Features

- Output Current: 300mA or more
- Dropout Voltage: 270mV @ 300mA for 3.3V
- Operating Voltage Range: 1.8 to 5.5V
- Output Voltage Range: 1.2 to 3.3V (100mV Step)
- Low Power Consumption: 65μA
- High Ripple Rejection: 75dB @ 1kHz
- Output Current-Limit Protection
- Thermal Shutdown Protection
- Operating Temperature Range: -40°C to +85°C
- Low ESR Capacitor Compatible: Ceramic Capacitor
- Lead Free Package: SOT23-3L

Applications

- Mobile phones (GSM, CDMA, WCDMA, CDMA2000 etc.)
- Cordless phones and radio communication
- Digital still cameras and video cameras
- PDAs
- MP3/MP4 players
- Portable devices

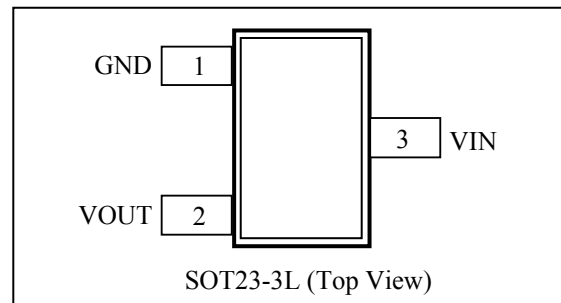
Description

The PT7M8205 series are highly accurate, low dropout voltage regulators with low noise, high ripple rejection and low current consumption.

The PT7M8205 includes a reference voltage source, an error amplifier, a driver transistor, a current limit protection, a thermal protection and an internal phase compensator.

The output voltage for the regulator is set by factory trimming within a range of 1.2V to 3.3V in 100mV step includes 2.85V. The PT7M8205 series are stable with low ESR ceramic capacitors.

Pin Configuration



Pin Description

Pin No.	I/O	Name	Descriptions
1	P	GND	Ground.
2	O	VOUT	Output of Regulator. Bypass with a 1μF ceramic capacitor (X5R/X7R) to GND.
3	I	VIN	Regulator Supply Input. Supply voltage can range from 1.8V to 5.5V. Bypass with a 1μF ceramic capacitor (X5R/X7R) to GND.