

Ceramic Heating Controller (LCD)

Features

- High watt density Alumina Heaters
- Pulse trigger with high current for SCR
- Quickly Heat-Up and Quickly Lost-Heat Recover
- External adjustable to fit wide range heating plates
- External adjustable to fit TCR of sensors
- Full temperature compensation and insensitive to environment temperature
- Internal zener
- 11 heat temperature settings
- Auto power off for PT8A3351/3/5/7
- Backlights for indicating work status
- LCD screen for showing temperature setting
- Dual Temperature (Fahrenheit/Centigrade) Display
- Wet function: It will keep heating on state to level 2.
- Dual voltage (120V/240V) operations.
- Support 3 keys, on/off, up and down
- SOIC-24, QFN-24 and TSSOP-24 package

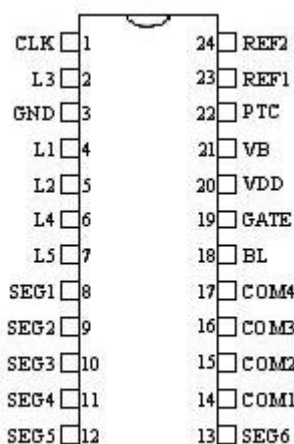
Description

The PT8A335xA/B is special designed for ceramic heating control with 11 temperature levels(show as below function comparison table). It can drive SCR directly and detect heater temperature by heater-self without extra temperature sensor. It has 3 keys for function setting. LCD displays and backlight indicate the working status. Build-in timer will be auto-power off after power on 1 hour for 60Hz or 1.2 hour for 50Hz.

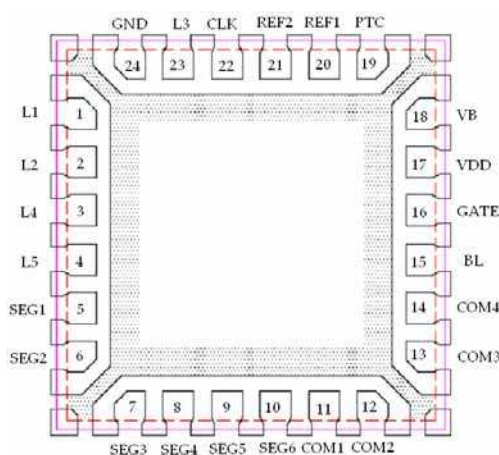
Applications

- Ceramic heating controller

Pin Configuration



SOIC-24/TSSOP-24



QFN-24

Pin Description

Name	Pin No.		Type	Description
	SOIC-24/TSSOP-24	QFN-24		
CLK	1	22	I	Clock input from power line and Detect operation voltage.
L3	2	23	I	Input to adjust Recovery Rate.
L1	4	1	I	Power on/off key inputs
L2	5	2	I	Up and down key inputs.
L4	6	3	I	High: LCD shows °F, Low : LCD shows °C.
L5	7	4	I	Supply LCD power.
SEG1,SEG2, SEG3,SEG4, SEG5,SEG6,	8,9,10,11,12,13	5,6,7,8,9,10	O	LCD digits control output.
COM1,COM 2, COM3,COM 4	14,15,16,17	11,12,13,14	O	LCD digits control output.
BL	18	15	O	Drive LCD backlight
GATE	19	16	O	SCR trigger output, active high
VB	21	18	O	PTC sampling power source
PTC	22	19	I	Temperature sensor input
REF1	23	20	I	Reference 1 for internal comparator
REF2	24	21	O	Reference 2 for internal comparator
GND	3	24	Power	Ground and Power
VDD	20	17		

The block diagram illustrates the internal architecture of the L3960 LCD controller, showing the flow of data and control signals between various functional blocks and external interfaces.

Internal Blocks and Connections:

- CLK** (Clock) is the primary input to the **Line Clk Sample** block, which outputs a **50Hz** signal to the **OSC** (Oscillator) block.
- The **OSC** block outputs an **80kHz** signal to the **Timing** block and the **Recover ADJ** (Recovery Adjust) block.
- The **Timing** block receives a **stop** signal from the **Recover ADJ** block and provides control signals to the **CTRL** (Control) block, the **Level Set** block, and the **KEY IN** (Key Input) block.
- The **CTRL** block receives **ptci** (Panel Temperature Control Input) and **vbo** (Video Bias Output) signals and outputs **LEVEL** (Level) signals to the **Level Set** block and the **LCD Driver**.
- The **Level Set** block outputs **LEVEL** signals to the **LCD Driver**.
- The **KEY IN** block outputs **LEVEL** signals to the **LCD Driver**.
- The **Recover ADJ** block outputs a **stop** signal to the **Timing** block and a **Vlcd** (Video Level) signal to the **LCD Driver**.
- The **LCD Driver** block outputs **COM1** through **COM4** (Common) signals and **SEG1** through **SEG6** (Segment) signals to the LCD panel.
- The **LCD Driver** also receives an **F_C select** (Frame Control Select) signal from the **L4** interface.
- The **Res Chain** (Resistor Chain) block receives **VB** (Video Bias) and **REF1** (Reference 1) signals and outputs **REF2** (Reference 2) and **GATE** (Gate) signals to the **CTRL** block.
- The **Timer** block outputs a **Timer_rst** (Timer Reset) signal to the **CTRL** block.
- The **Test** block outputs **test1** through **test6** signals to the **CTRL** block.

External Interfaces:

- L3:** Provides **rst** (Reset) to the **POR** (Power-On Reset) block and **Recover ADJ**.
- L4:** Provides **F_C select** to the **LCD Driver**.
- L5:** Provides **Vlcd** to the **LCD Driver**.
- L1 (Up/Dn):** Provides **BL** (Backlight) and **L2 (On/Off)** signals to the **KEY IN** block.
- PTC:** Provides a signal to the **CMP R1** (Comparator 1) block, which outputs to the **CTRL** block.

Storage Temperature	-55°C to +150°C
Ambient Temperature with Power applied.....	-20°C to +85°C
Supply Voltage to Ground Potential (Input & V _{DD} Only)	-0.5V to +6.5V
Supply Voltage to Ground Potential (Output s Only)	-0.5V to +6.5V
DC Input Voltage	-0.5V to +6.5V
Input/Output Current	50mA
Input/Output Current (Pin VDD, VB only).....	200mA
Power Dissipation	500mW

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Sym	Parameter	Pin	Min	Typ	Max	Unit
V _{DD}	Operating Voltage	VDD	4.0	-	V _Z	V
T _A	Operating temperature	-	-20	-	85	°C