

**CFTVS5V0LC**

**SURFACE MOUNT SILICON  
TRANSIENT VOLTAGE SUPPRESSOR**



[www.centrasemi.com](http://www.centrasemi.com)



**DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CFTVS5V0LC is an ultra low capacitance, low leakage, fast response TVS in the space saving SOD-882L surface mount package. This device is designed to protect sensitive equipment connected to high speed data lines against ESD damage.

**MARKING CODE: 5**

**APPLICATIONS:**

- High speed data line protection
- User interface protection
- Charging/power port protection

**FEATURES:**

- Space saving SOD-882L package
- Ultra low capacitance
- Low leakage current
- 15kV ESD protection

**MAXIMUM RATINGS:** ( $T_A=25^\circ\text{C}$ )

Peak Power Dissipation (8x20 $\mu\text{s}$ )  
ESD Voltage (IEC 61000-4-2, Air)  
Operating Junction Temperature  
Storage Temperature

**SYMBOL**

$P_{PK}$                       12  
 $V_{ESD}$                       15  
 $T_J$                           -55 to +125  
 $T_{stg}$                       -55 to +150

**UNITS**

W  
kV  
 $^\circ\text{C}$   
 $^\circ\text{C}$

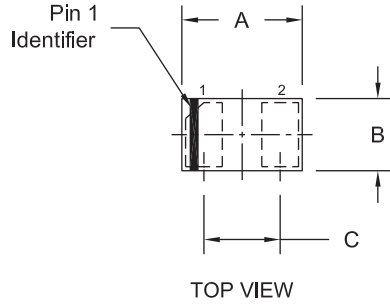
**ELECTRICAL CHARACTERISTICS:** ( $T_A=25^\circ\text{C}$ )  $V_F=1.0\text{V MAX @ } I_F=10\text{mA}$

Maximum Reverse Stand-off Voltage $V_{RWM}$	Minimum Breakdown Voltage $V_{BR @ I_T}$	Test Current $I_T$	Maximum Reverse Leakage Current $I_R @ V_{RWM}$	Maximum Clamping Voltage $V_C @ I_{PP}$	Peak Pulse Current $I_{PP}$	Maximum Junction Capacitance @ 0V Bias $C_J$
V	V	mA	$\mu\text{A}$	V	A	pF
5.0	6.0	1.0	1.0	12	1.0	0.9

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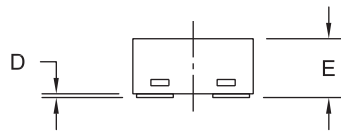


**SOD-882L CASE - MECHANICAL OUTLINE**



SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.037	0.041	0.95	1.05
B	0.022	0.026	0.55	0.65
C	0.026		0.65	
D	0.000	0.002	0.00	0.05
E	0.012	0.016	0.30	0.40
F	0.018	0.022	0.45	0.55
G	0.008	0.012	0.20	0.30

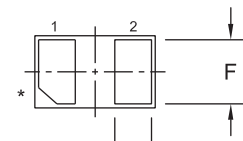
SOD-882L (REV:R2)



**LEAD CODE:**

- 1) Cathode
- 2) Anode

**MARKING CODE: 5**



\* Pin 1 chamfer may appear on any corner.

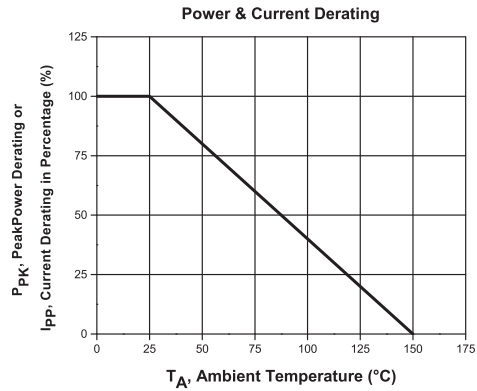
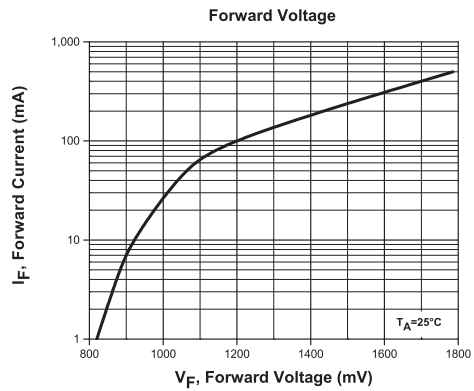
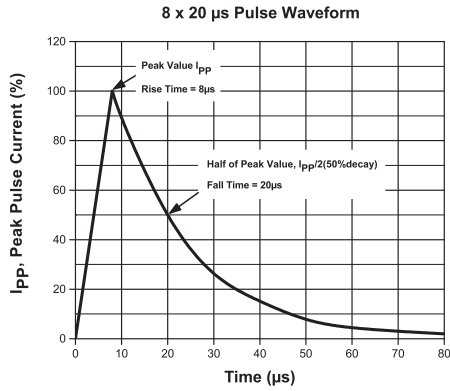
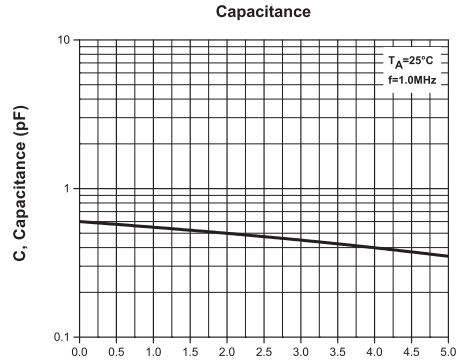
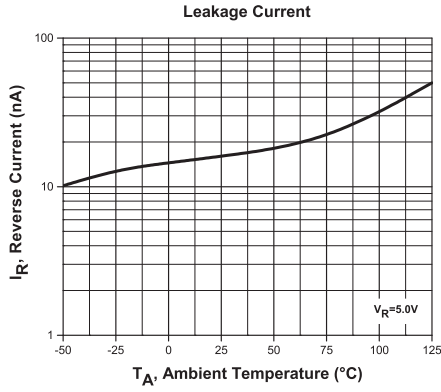
R2

R1 (3-April 2013)

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**TYPICAL ELECTRICAL CHARACTERISTICS**



R1 (3-April 2013)