

Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company





PNP SILICON TRANSISTOR



CSA 643

TO-92 Plastic Package

Audio Frequency General Purpose Power Amplifier Application

Complementary CSD 261

ABSOLUTE MAXIMUM RATINGS (Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	VALUE	UNIT
Collector Emitter Voltage	V _{CEO}	20	V
Collector Base Voltage	BV _{CBO}	40	V
Emitter Base Voltage	V _{EBO}	5	V
Collector Current Continuous	I _C	500	mA
Peak	I _{CM}	700	mA
Collector Power Dissipation	P _C	500	mW
Derate Above 25°C		4.0	mW/ºC
Operating and Storage Junction	T_{j},T_{stg}	-55 to +150	ōС
Temperature Range	',' stg	33 to +130	

ELECTRICAL CHARACTERISTICS (Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL TEST CONDITION		MIN	MAX	UNIT	
Collector Emitter Breakdown Voltage	BV _{CEO}	$I_C=10mA, I_B=0$	20		V	
Collector Base Breakdown Voltage	BV _{CBO}	$I_C=100\mu A, I_B=0$	40		V	
Emitter Base Breakdown Voltage	BV _{EBO}	I _E =100μA, I _C =0	>5		V	
Collector Cut off Current	I _{CBO}	$V_{CB} = 25V, I_E = 0$		0.1	μΑ	
Emitter Cut off Current	I _{EBO}	$V_{EB} = 3V, I_{C} = 0$		0.1	μΑ	
DC Current Gain	h _{FE}	V _{CE} =1V,I _C =100mA*	40	400		
Base Emitter Saturation Voltage	V _{BE(sat)} *	I _C =500mA,I _B = 50mA*		1.3	V	
Collector Emitter Saturation	V _{CE(sat)}	I _C =500mA,I _B =50mA*		0.4	V	
Voltage						

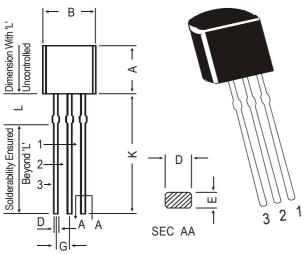
*Pulse Condition: = Width ≤ 300us, Duty Cycle ≤ 2%.

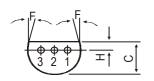
h _{FE} CLASSIFICATION	R	0	Υ	G
	40-80	70-140	20-240	200-400

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TO-92 Transistors on Tape and Ammo Pack



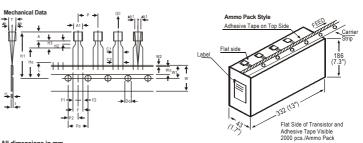


PIN CONFIGURATION

- 1. COLLECTOR
- 2. BASE
- 3. EMITTER

S	EC AA	I	3 2		
	DIM	MIN.	MAX.		
	Α	4.32	5.33		
	В	4.45	5.20		
	С	3.18	4.19		
	D	0.41	0.55		
	Е	0.35	0.50		
	F	5 D	EG		
	G	1.14	1.40		
	Н	1.14	1.53		
	K	12.70	_		
	L	1.982	2.082		
,	A 11 -111				

All diminsions in mm.



All dimensions in mr	n
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			SPEC		SPECIFICATION		
ITEM		SYMBOL	MIN.	NOM.	MAX.	TOL.	REMARKS
BODY WIDTH		A1	4.0		4.8		
BODY HEIGHT		A	4.8		5.2		
BODY THICKNESS		T	3.9		4.2		
PITCH OF COMPONENT	Γ	P		12.7		± 1.0	
FEED HOLE PITCH		Po		12.7		± 0.3	CUMULATIVE PITCH ERROR 1.0 mm/20 PITCH
FEED HOLE CENTRE TO)						
COMPONENT CENTRE		P2		6.35		± 0.4	TO BE MEASURED AT BOTTOM OF CLINCH
DISTANCE BETWEEN O	UTER					+0.6	
LEADS		F		5.08		- 0.2	
COMPONENT ALIGNME	NT SIDE VIEW	Δh		0	1.0	0.2	AT TOP OF BODY
COMPONENT ALIGNME	NT FRONT VIEW	∆h1		0	1.3		AT TOP OF BODY
TAPE WIDTH		w		18		± 0.5	
HOLD-DOWN TAPE WIDTH	1	Wo		6		± 0.2	
HOLE POSITION		W1		9		+ 0.7	
						- 0.5	
HOLD-DOWN TAPE POS	SITION	W2		0.5		± 0.2	
LEAD WIRE CLINCH HE	IGHT	Ho		16		± 0.5	
COMPONENT HEIGHT		H1			23.25		
LENGTH OF SNIPPED L	EADS	L			11.0		
FEED HOLE DIAMETER		Do		4		± 0.2	
TOTAL TAPE THICKNES	SS	t			1.2		t1 0.3-0.6
LEAD - TO - LEAD DISTAN	CE	F1, F2		2.54		+ 0.4	
STAND OFF		H2	0.45		1.45	- 0.1	
CLINCH HEIGHT		H3			3.0		
LEAD PARALLELISM		C1 - C2			0.22		
PULL - OUT FORCE		(P)	6N				

- NOTES

 1. Maximum alignment deviation between leads will not to be greater than 0.2mm.

 2. Maximum non-cumulative variation between tape feed holes shall not exceed 1 mm in 20 pitches.

 3. Holddown tape will not exceed beyond the edge(s) of carrier tape and there shall be no exposure of adhesive.

 4. There will be no more than three (3) consecutive missing components in a tape.

 5. A tape trailer, having at least three feed holes are provided after the last component in a tape.

 6. Splices should not interfere with the sprocket feed holes.

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details Net Weight /Qty		Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

Notes CSA 643

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Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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