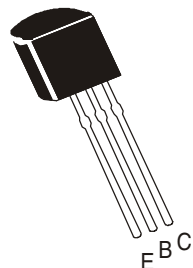


## PNP SILICON PLANAR EPITAXIAL TRANSISTORS

CP749



TO-92  
Plastic Package

Use in Wide Variety of Industrial and Consumer Applications Including Lamp and Solenoid Drivers and Audio Amplifier

Complementary CN649

### ABSOLUTE MAXIMUM RATINGS

DESCRIPTION	SYMBOL	VALUE	UNITS
Collector Base Voltage	$V_{CBO}$	35	V
Collector Emitter Voltage	$V_{CEO}$	25	V
Emitter Base Voltage	$V_{EBO}$	5	V
Peak Pulse Current	$I_{CM}$	6	A
Collector Current Continuous	$I_C$	2	A
Power Dissipation @ $T_a=25^\circ\text{C}$	$P_D$	0.9	W
Derate Above $25^\circ\text{C}$		7.2	mW/ $^\circ\text{C}$
Power Dissipation @ $T_a=25^\circ\text{C}$	$^*P_D$	1.1	W
Power Dissipation @ $T_C=25^\circ\text{C}$	$P_D$	2.2	W
Operating and Storage Junction Temperature Range	$T_j, T_{stg}$	- 65 to +150	$^\circ\text{C}$

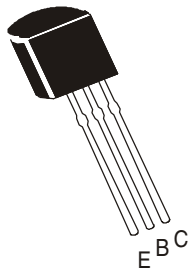
\* Transistors mounted on printed circuit board. Lead Length 4mm, mounting pad for collector lead min 10mm x 10 mm, copper

### ELECTRICAL CHARACTERISTICS ( $T_a=25^\circ\text{C}$ unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNITS
Collector Emitter Voltage	$V_{CEO}$	$I_C=1\text{mA}, I_B=0$	25		V
Collector Base Voltage	$V_{CBO}$	$I_C=100\mu\text{A}, I_E=0$	35		V
Emitter Base Voltage	$V_{EBO}$	$I_E=100\mu\text{A}, I_C=0$	5.0		V
Collector Cut Off Current	$I_{CBO}$	$V_{CB}=30\text{V}, I_E=0$		100	nA
		$V_{CB}=30\text{V}, I_E=0, T_a=100^\circ\text{C}$		10	$\mu\text{A}$
Emitter Cut Off Current	$I_{EBO}$	$V_{EB}=4\text{V}, I_C=0$		100	nA
Collector Emitter Saturation Voltage	$^{**}V_{CE(sat)}$	$I_C=1\text{A}, I_B=100\text{mA}$		0.3	V
		$I_C=2\text{A}, I_B=200\text{mA}$		0.6	V
Base Emitter Saturation Voltage	$^{**}V_{BE(sat)}$	$I_C=1\text{A}, I_B=100\text{mA}$		1.25	V
Base Emitter on Voltage	$^{**}V_{BE(on)}$	$I_C=1\text{A}, V_{CE}=2\text{V}$		1.0	V
DC Current Gain	$^{**}h_{FE}$	$I_C=50\text{mA}, V_{CE}=2\text{V}$	70		
		$I_C=1\text{A}, V_{CE}=2\text{V}$	100		
		$I_C=2\text{A}, V_{CE}=2\text{V}$	75		
		$I_C=6\text{A}, V_{CE}=2\text{V}$	10		

**\*\*Pulse Condition: Pulse Width = 300 $\mu\text{s}$ , Duty Cycle  $\leq$  2%.**

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ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C unless specified otherwise)

DYNAMIC CHARACTERISTICS

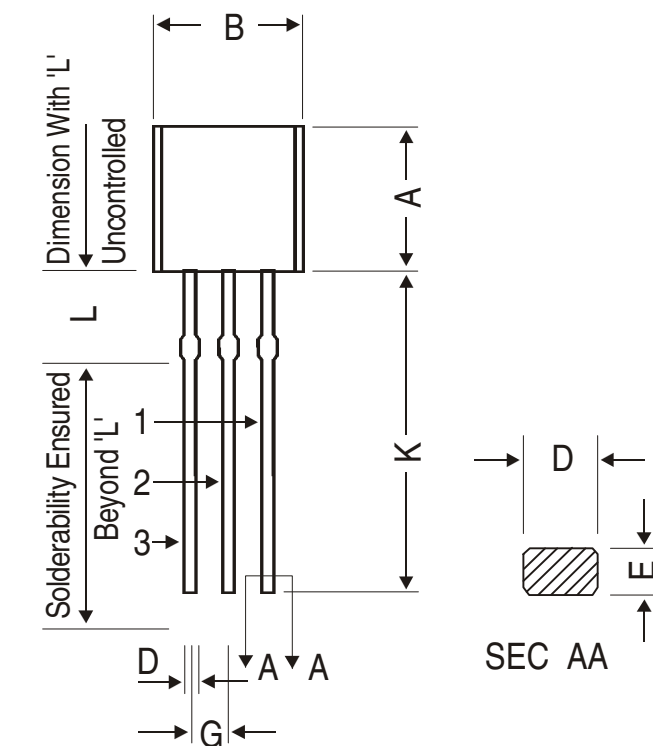
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNITS
Output Capacitance	C <sub>obo</sub>	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=1MHz		100	pF
Transition Frequency	f <sub>T</sub>	V <sub>CE</sub> =5V, I <sub>C</sub> =100mA, f=100MHz	100		MHz

SWITCHING TIMES

DESCRIPTION	SYMBOL	TEST CONDITION	TYP	UNIT
Turn On Time	t <sub>on</sub>	I <sub>C</sub> =500mA, I <sub>B1</sub> = I <sub>B2</sub> =50mA, V <sub>CC</sub> =10V	40	ns
Turn Off Time	t <sub>off</sub>		500	ns

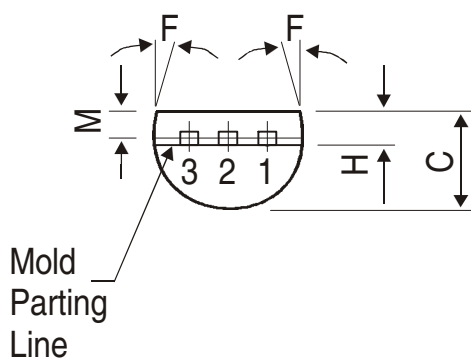
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## TO-92 Plastic Package



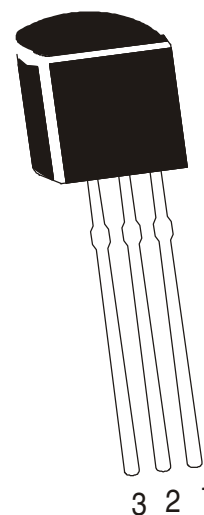
DIM	MIN.	MAX.
A	4.32	5.33
B	4.45	5.20
C	3.18	4.19
D	0.41	0.55
E	0.35	0.50
F	5 DEG	
G	1.14	1.40
H	1.20	1.40
K	12.70	—
L	1.982	2.082
M	1.03	1.20

All dimensions are in mm



## PIN CONFIGURATION

1. COLLECTOR
2. BASE
3. EMITTER



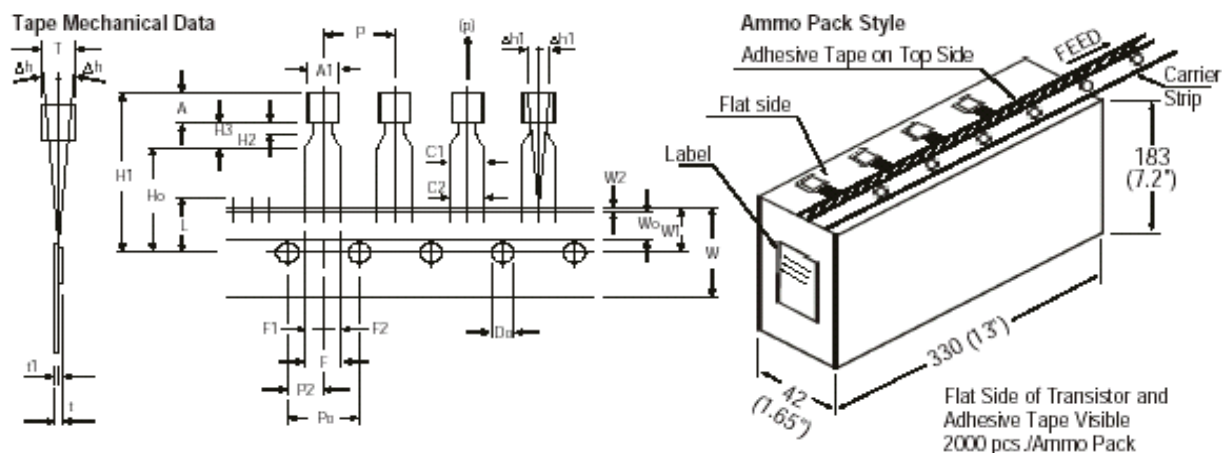
The TO-92 Package, Tape and Ammo Pack Drawings are correct as on the date of issue/revision of this Data Sheet.

The currently valid dimensions and information, may please be confirmed from the TO-92 Drawing in the Packages and Packing Section of the Product Catalogue.

## Packing Details

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

## TO-92 Tape and Ammo Pack



All dimensions are in mm

ITEM	SYMBOL	SPECIFICATION				
		MIN.	NOM.	MAX.	TOL.	
BODY WIDTH	A1	4.45		5.20		<b>NOTES</b> 1. Maximum alignment deviation between leads will not to be greater than 0.2mm. 2. Maximum non-cumulative variation between tape feed holes shall not exceed 1 mm in 20 pitches. 3. Holddown tape will not exceed beyond the edge(s) of carrier tape and there shall be no exposure of adhesive. 4. There will be no more than three (3) consecutive missing components in a tape. 5. A tape trailer, having at least three feed holes are provided after the last component in a tape. 6. Splices should not interfere with the sprocket feed holes.
BODY HEIGHT	A	4.32		5.33		
BODY THICKNESS	T	3.18		4.19		
PITCH OF COMPONENT	P		12.7		$\pm 1.0$	
*1 FEED HOLE PITCH	P <sub>0</sub>		12.7		$\pm 0.3$	
*2 FEED HOLE CENTRE TO COMPONENT CENTRE	P <sub>2</sub>		6.35		$\pm 0.4$	
DISTANCE BETWEEN OUTER LEADS	F		5.08		+ 0.6 - 0.2	<b>REMARKS</b> *1 Cumulative pitch error 1.0 mm/20 pitch *2 To be measured at bottom of clinch *3 At top of body *4 At top of body *5 t1 0.3 – 0.6 mm
*3 COMPONENT ALIGNMENT SIDE VIEW	$\Delta h$		0	1.0		
*4 COMPONENT ALIGNMENT FRONT VIEW	$\Delta h_1$		0	1.3		
TAPE WIDTH	W		18		$\pm 0.5$	
HOLD-DOWN TAPE WIDTH	W <sub>0</sub>		6		$\pm 0.2$	
HOLE POSITION	W <sub>1</sub>		9		+ 0.7 - 0.5	
HOLD-DOWN TAPE POSITION	W <sub>2</sub>	0.0		0.7		
LEAD WIRE CLINCH HEIGHT	H <sub>0</sub>		16		$\pm 0.5$	
COMPONENT HEIGHT	H <sub>1</sub>			24.0		
LENGTH OF SNIPPED LEADS	L			11.0		
FEED HOLE DIAMETER	D <sub>0</sub>		4		$\pm 0.2$	
*5 TOTAL TAPE THICKNESS	t			1.2		
LEAD - TO - LEAD DISTANCE	F <sub>1</sub> , F <sub>2</sub>	2.40		2.70		
STAND OFF	H <sub>2</sub>	0.45		1.45	- 0.1	
CLINCH HEIGHT	H <sub>3</sub>			3.0		
LEAD PARALLELISM	C <sub>1</sub> - C <sub>2</sub>			0.22		
PULL - OUT FORCE	(p)	6N				

**Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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