

### Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company





## NPN EPITAXIAL PLANAR SILICON HIGH VOLTAGE TRANSISTOR

2N5550



TO-92 Plastic Package

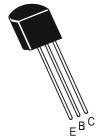
# High Voltage NPN Transistor For General Purpose and Telephony Applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	VALUE	UNITS	
Collector Emitter Voltage	$V_{CEO}$	140	V	
Collector Base Voltage	$V_{CBO}$	160	V	
Emitter Base Voltage	$V_{EBO}$	6	V	
<b>Collector Current Continuous</b>	$I_{C}$	600	mA	
Power Dissipation@ Ta=25°C	$P_D$	625	mW	
Derate Above 25 <sup>o</sup> C		5.0	mW/ºC	
Power Dissipation@ Tc=25°C	$P_D$	1.5	W	
Derate Above 25°C		12	mW/ºC	
<b>Operating And Storage Junction</b>	$T_{j},T_{stg}$	-55 to +150	ōC	
Temperature Range				
THERMAL RESISTANCE				
Junction to ambient	$R_{th(j-a)(1)}$	357	ºC/W	
Junction to case	$R_{th(j-c)}$	125	<sup>o</sup> C/W	

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ELECTRICAL CHARACTERISTICS (Ta=25°C unless specified otherwise)

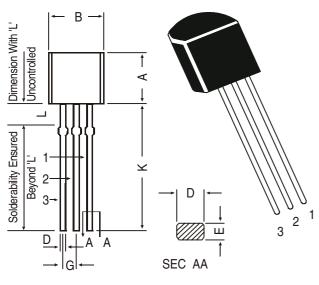
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Collector Base Voltage	V <sub>CEO</sub> *	$I_C=1 \text{ mA}, I_B=0$	140			V
Collector Base Voltage	$V_{\sf CBO}$	$I_{C} = 100 \mu A, I_{E} = 0$	160			V
Emitter Base Voltage	$V_{EBO}$	$I_{E}=10\mu A,\ I_{C}=0$	6			V
Collector Cut off Current	$I_{CBO}$	$V_{CB} = 100V, I_{E} = 0$			100	nA
		Ta=100 <sup>O</sup> C				
		$V_{CB} = 100V, I_{E} = 0$			100	μΑ
Emitter Cut off Current	I <sub>EBO</sub>	$V_{BE}=4V$ , $I_C=0$			50	nA
						μΑ
DC Current Gain	h <sub>FE</sub> *	$V_{CE}=5V,I_{C}=1mA$	60			
		$V_{CE}=5V,I_{C}=10mA$	60		250	
		$V_{CE}=5V,I_{C}=50mA$	20			
Base Emitter Saturation Voltage	$V_{BE(sat)}^{\star}$	$I_C=10mA, I_B=1mA$			1.0	V
		$I_C=50$ mA, $I_B=5$ mA			1.2	V
<b>Collector Emitter Saturation Voltage</b>	$V_{CE(sat)}^*$	$I_C=10mA, I_B=1mA$			0.15	V
		$I_C=50$ mA, $I_B=5$ mA			0.25	V

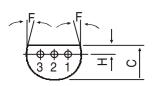
#### **DYNAMIC CHARACTERISTICS**

Small Signal Current Gain	I hfe I	I <sub>C</sub> =1mA, V <sub>CE</sub> =10V f=1KHz	50	200	
Transition Frequency	$f_T$	I <sub>C</sub> =10mA, V <sub>CE</sub> =10V			
		f=100MHz	100	300	MHz
Output Capacitance	$C_obo$	I <sub>E</sub> =0, V <sub>CB</sub> =10V			
		f=1MHz		6.0	$_{P}F$
Input Capacitance	$C_{ibo}$	$Ic=0, V_{EB}=0.5V$			
		f=1MHz		30	₽F
Noise Figure	NF	$V_{CE} = 5V, I_{C} = 250 \mu A$		10	dB
_		$R=1\Omega$ , $f=10H_z$ to 15.7 kHz			
*Pulse Condition: Width = 300µs	s, Duty Cycle=	: 2%.			

## **TO-92 Plastic Package**

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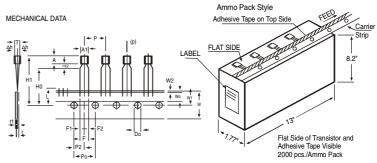
#### PIN CONFIGURATION

- 1. COLLECTOR
- 2. BASE
- 3. EMITTER

DIM	MIN.	MAX.						
Α	4.32	5.33						
В	4.45	5.20						
C	3.18	4.19						
D	0.41	0.55						
Е	0.35	0.50						
F	5 DEG							
G	1.14	1.40						
Η	1.14	1.53						
K	12.70							
L	1.982	2.082						
All diminaiana in mm								

All diminsions in mm.

## **TO-92 Transistors on Tape and Ammo Pack**



#### All dimensions in mm unless specified otherwise

ITEM			SPECIF	ICATIO	N	
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL .	REMARKS
BODY WIDTH BODY HEIGHT BODY THICKNESS	A1 A T	4.0 4.8 3.9		4.8 5.2 4.2		
PITCH OF COMPONENT	P	3.9	12.7	4.2	±1	
FEED HOLE PITCH FEED HOLE CENTRE TO	Po		12.7		±0.3	CUMULATIVE PITCH ERROR 1.0 mm/20 PITCH
COMPONENT CENTRE	P2		6.35		±0.4	TO BE MEASURED AT BOTTOM OF CLINCH
DISTANCE BETWEEN OUTER LEADS	F		5.08		+0.6 -0.2	
COMPONENT ALIGNMENT TAPE WIDTH HOLD-DOWN TAPE WIDTH HOLE POSITION	∆h W Wo W1		0 18 6 9	1	±0.5 ±0.2 +0.7 -0.5	AT TOP OF BODY
HOLD-DOWN TAPE POSITION LEAD WIRE CLINCH HEIGHT COMPONENT HEIGHT LENGTH OF SNIPPED LEADS	W2 Ho H1 I		0.5 16	23.25 11.0	±0.2 ±0.5	
FEED HOLE DIAMETER TOTAL TAPE THICKNESS LEAD - TO - LEAD DISTANCEF1,	Do t F2		4 2.54	1.2	±0.2 +0.4	t1 0.3 - 0.6
CLINCH HEIGHT PULL - OUT FORCE	H2 (P)	6N		3	-0.1	

- NOTES

  NOTES

  MAXIMUM ALIGNMENT DEVIATION BETWEEN LEADS NOT TO BE GREATER THAN 0.2 mm.

  MAXIMUM NON-CUMULATIVE VARIATION BETWEEN TAPE FEED HOLES SHALL NOT EXCEED 1 mm IN 20 PITCHES.
- HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO
- 3. HOLDDOWN TAPENOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO EXPOSURE OF ADHESIVE.

  4. NO MORE THAN 3 CONSECUTIVE MISSING COMPONENTS ARE PERMITTED.

  5. A TAPE TRAILER, HAVING AT LEAST THREE FEED HOLES ARE REQUIRED AFTER THE LAST COMPONENT.

  6. SPLICES SHALL NOT INTERFERE WITH THE SPROCKET FEED HOLES.

## Packing Detail

<u> </u>									
PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX				
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt		
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs		
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs		

Notes 2N5550

TO-92 Plastic Package

#### **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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2N5550Rev190701