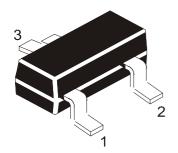






An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company

NPN SILICON PLANAR EPITAXIAL TRANSISTORS



PIN CONFIGURATION (NPN)

1 = BASE

2 = EMITTER

3 = COLLECTOR

3

CMBT8098 CMBT8099

SOT-23 Formed SMD Package

Marking CMBT8098- KA CMBT8099- KB

ABSOLUTE MAXIMUM RATING

DESCRIPTION	SYMBOL	CMBT8098	CMBT8099	UNITS
Collector Base Voltage	V_{CBO}	60	80	V
Collector Emitter Voltage	V_{CEO}	60	80	V
Emitter Base Voltage	V_{EBO}	6	V	
Collector Current Continuous	I _C	5	mA	
Power Dissipation T _a =25°C (Note1)	P_{D}	2	mW	
Derate Above 25ºC		1	mW/ºC	
Power Dissipation T _a =25°C (Note2)	P_{D}	300		mW
Derate Above 25ºC		2	mW/ºC	
Operating And Storage Junction Temperature Range	T_{j}, T_{stg}	- 55 t	ºC	

Thermal Resistance

111011114111100101411100	1		
Junction to Ambient (Note1)	R _{th(j-a)}	556	ºC/W
Junction to Ambient (Note2)	R _{th(j-a)}	417	ºC/W

Note (1) FR-5 Board=25.4 x 19.05 x 1.58 mm (1.0 x 0.75 x 0.062 inches.)

Note (2) Alumina Substrate=10.16 x 7.62 x 0.61 mm (0.4 x 0.3 x 0.024 inches.) 99.5% alumina.

ELECTRICAL CHARACTERISTICS (T_a=25° C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Collector Base Voltage	V_{CBO}	$I_{C}=100\mu A, I_{E}=0$				
		CMBT8098	60			V
		CMBT8099	80			V
Collector Emitter Voltage	V_{CEO}	$I_C=10$ mA, $I_B=0$				
		CMBT8098	60			V
		CMBT8099	80			V
Emitter Base Voltage	V_{EBO}	I _E =10μΑ, I _C =0	6.0			V
Collector Cut Off Current	ces	$V_{CE}=60V$, $I_{B}=0$			0.1	μΑ
Collector Cut Off Current	Сво	CMBT8098				
		$V_{CB}=60V$, $I_{E}=0$			0.1	μΑ
		CMBT8099				
		$V_{CB}=80V$, $I_{E}=0$			0.1	μΑ
Emitter Cut Off Current	Ево	$V_{EB}=6V, I_{C}=0$			0.1	μA

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ELECTRICAL CHARACTERISTICS (T_a=25° C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
DC Current Gain	h _{FE} *	I _C =1mA, V _{CE} =5V	100		300	
		I _C =10mA, V _{CE} =5V	100			
		$I_C=100$ mA, $V_{CE}=5$ V	75			
Collector Emitter Saturation Voltage	V _{CE(sat)} *	I _C =100mA, I _B =5mA			0.4	V
		$I_C=100$ mA, $I_B=10$ mA			0.3	V
Base Emitter On Voltage	V _{BE(on)} *	CMBT8098				
		$I_{C}=1$ mA, $V_{CE}=5$ V	0.5		0.7	V
		CMBT8099				
		$I_{C}=10mA, V_{CE}=5V$	0.6		0.8	V

Small Signal Characteristics

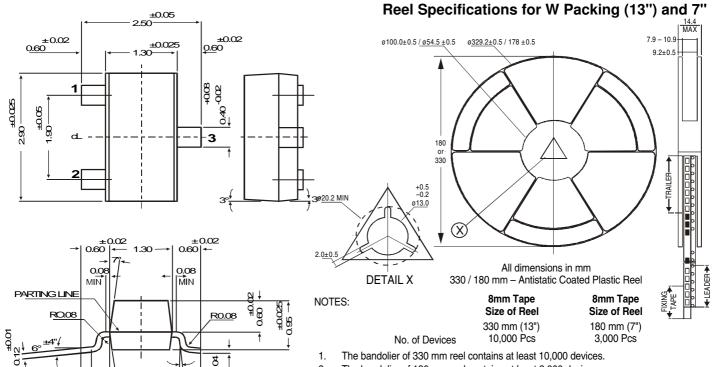
Current Gain Bandwidth Product	f _T	I _C =10mA, V _{CE} =5V,f=100MHz	150		MHz
Output Capacitance	C_obo	$I_E=0$, $V_{CB}=5V$, $f=1MHz$		6.0	рF
Input Capacitance	C_{ibo}	$I_{C}=0, V_{EB}=0.5V, f=1MHz$		25	pF

^{*}Pulse Test: Pulse Width ≤ 300µs, Duty Cycle≤2%

SOT-23 Formed SMD Package

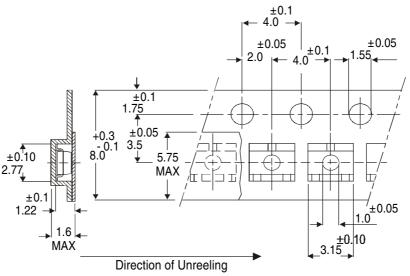
SOT-23 Formed SMD Package

SOT-23 Package Reel Information Reel Specifications for W Packing (13") and 7"



- 2. The bandolier of 180 mm reel contains at least 3,000 devices.
- No more than 0.5% missing devices / reel. 50 empty compartments for 330 mm reel.
 empty compartments for 180 mm reel.
- Three consecutive empty places might be found provided this gap is followed by 6 consecutive devices.
- 5. The carrier tape (leader) starts with at least 75 empty positions (equivalent to 330 mm). In order to fix the carrier tape a self adhesive tape of 20 to 50 mm is applied. At the end of the bandolier at least 40 empty positions (equivalent to 160 mm) are there.

Tape Specification for SOT-23 Surface Mount Device



Packing Detail

All dimensions in mm

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details Net Weight/Qty		Size	Qty	Size	Qty	Gr Wt
SOT-23 T&R	3K/reel	136 gm/3K pcs	3" x 7.5" x 7.5"	12 K	17" x 15" x 13.5"	192 K	12 kgs
			9" x 9" x 9"	51 K	19" x 19" x 19"	408 K	28 kgs
	10K/reel	415 gm/10K pcs	13" x 13" x 0.5"	10 K	17" x 15" x 13.5"	300 K	16 kgs

Notes

CMBT8098 CMBT8099

SOT-23 Formed SMD Package

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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