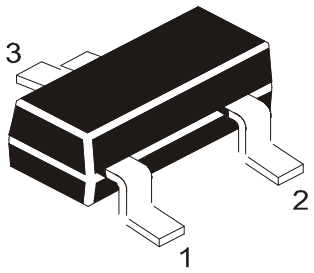
SOT-23 Formed SMD Package **CMBT5550**

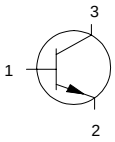
SILICON N–P–N HIGH–VOLTAGE TRANSISTOR

N–P–N transistor

Marking
CMBT5550 = 1F



Pin configuration
1 = BASE
2 = EMITTER
3 = COLLECTOR



ABSOLUTE MAXIMUM RATINGS

Collector–base voltage (open emitter)	V_{CBO}	*max.*	*160 V*
Collector–emitter voltage (open base)	V_{CEO}	*max.*	*140 V*
Collector current	I_C	*max.*	*600 mA*
Total power dissipation up to T_{amb} = 25°C	P_{tot}	*max*	*250 mW*
Collector–emitter saturation voltage			
I_C = 50 mA; I_B = 5 mA	V_{CEsat}	*max.*	*0.25 V*
D.C. current gain			
I_C = 10 mA; V_{CE} = 5 V	h_{FE}		*60 to 250*

***RATINGS** (at T_A = 25°C unless otherwise specified)*

Limiting values			
Collector–base voltage (open emitter)	V_{CBO}	*max.*	*160 V*
Collector–emitter voltage (open base)	V_{CEO}	*max.*	*140 V*
Emitter–base voltage (open collector)	V_{EBO}	*max.*	*6 V*
Collector current	I_C	*max.*	*600 mA*

Total power dissipation up to T_{amb} = 25°C	P_{tot}	*max*	*250*	*mW*
Storage temperature	T_{stg}		*–55 to +150*	*° C*
Junction temperature	T_j	*max.*	*150*	*° C*

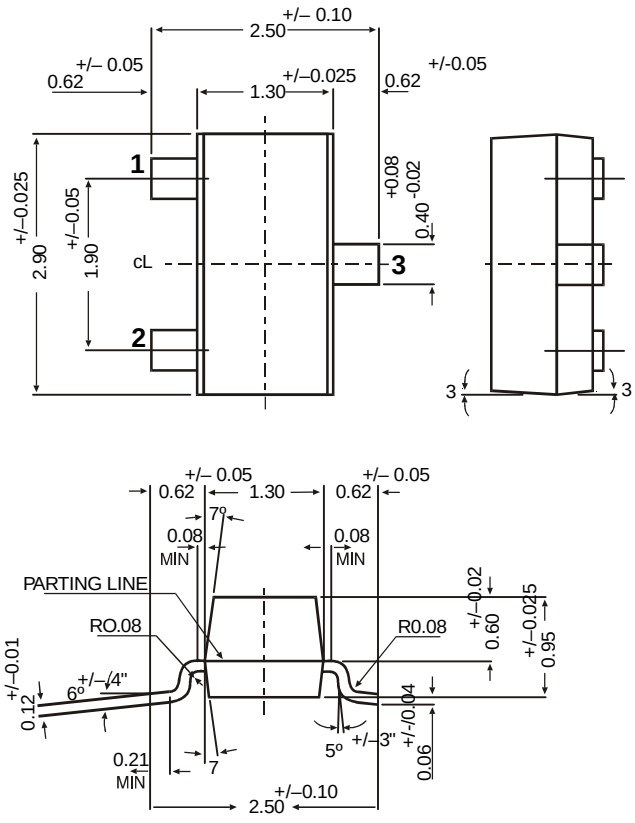
THERMAL RESISTANCE

from junction to ambient	$R_{th\ j-a}$		*500*	K/W

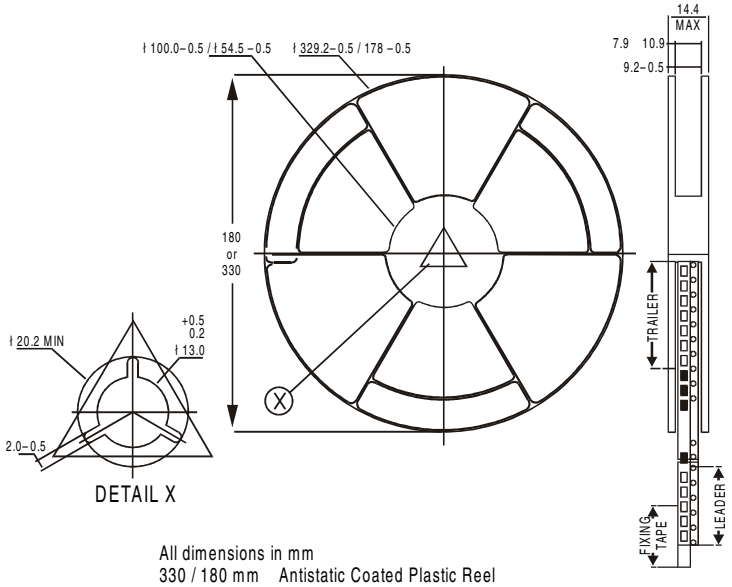
CHARACTERISTICS *(at T_A = 25°C unless otherwise specified)*

Collector cut–off current				
I_E = 0; V_{CB} = 100 V	I_{CBO}	*max.*	*100*	*nA*
I_E = 0; V_{CB} = 100 V; T_{amb} = 100 °C	I_{CBO}	*max.*	*100*	*µA*
Emitter cut–off current				
I_C = 0; V_{EB} = 4.0 V	I_{EBO}	*max.*	*50*	*nA*
Breakdown voltages				
I_C = 1 mA; I_B = 0	$V_{(BR)CEO}$	*min.*	*140*	*V*
I_C = 10 µA; I_E = 0	$V_{(BR)CBO}$	*min.*	*160*	*V*
I_C = 0; I_E = 10 µA	$V_{(BR)EBO}$	*min.*	*6*	*V*
Saturation voltages				
I_C = 10 mA; I_B = 1 mA	V_{CEsat}	*max.*	*0.15*	*V*
	V_{BEsat}	*max.*	*1*	*V*
I_C = 50 mA; I_B = 5 mA	V_{CEsat}	*max.*	*0.25*	*V*
	V_{BEsat}	*max.*	*1.2*	*V*
D.C. current gain				
I_C = 1 mA; V_{CE} = 5 V	h_{FE}	*min.*	*60*	
I_C = 10 mA; V_{CE} = 5 V	h_{FE}	*min.*	*60*	
		max.	*250*	
I_C = 50 mA; V_{CE} = 5 V	h_{FE}	*min.*	*20*	
Output capacitance at f = 1 MHz				
I_E = 0; V_{CB} = 10 V	C_o	*max.*	*6*	*pF*
Input capacitance at f = 1 MHz				
I_C = 0; V_{EB} = 10 V	C_i	*max.*	*30*	*pF*
Transition frequency at f = 100 MHz				
I_C = 10 mA; V_{CE} = 10 V; T_{amb} = 25 °C	f_T	*min.*	*100*	MHz
		max.	*300*	MHz

SOT-23 Formed SMD Package



SOT-23 Package Reel Information
Reel specifications for Packing (13"/7" reels)

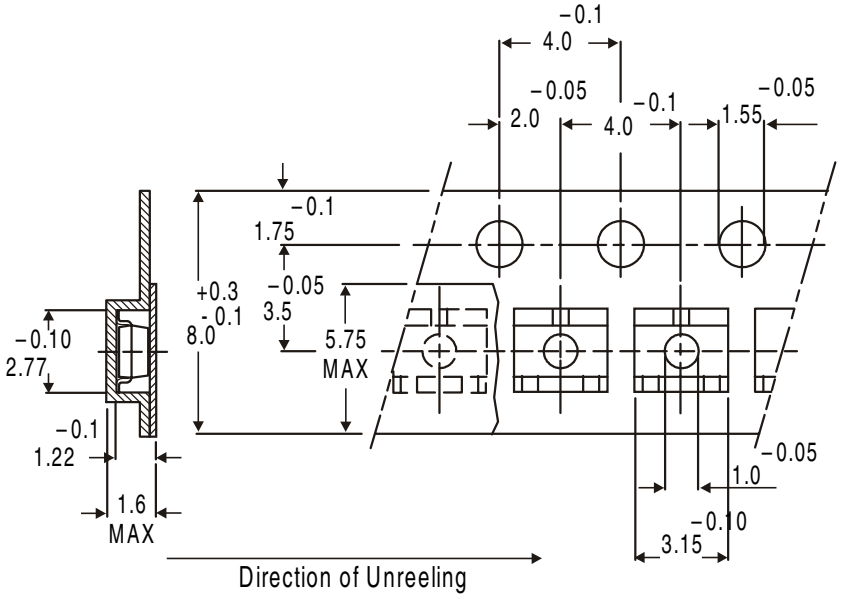


All dimensions in mm
330 / 180 mm Antistatic Coated Plastic Reel

NOTES:	8mm Tape Size of Reel 330 mm (13")	8mm Tape Size of Reel 180 mm (7")
No. of Devices	10,000 Pcs	3,000 Pcs

1. The bandolier of 330 mm reel contains at least 10,000 devices.
2. The bandolier of 180 mm reel contains at least 3,000 devices.
3. No more than 0.5% missing devices / reel. 50 empty compartments for 330 mm reel. 15 empty compartments for 180 mm reel.
4. Three consecutive empty places might be found provided this gap is followed by 6 consecutive devices.
5. The carrier tape (leader) starts with at least 75 empty positions (equivalent to 330 mm). In order to fix the carrier tape a self adhesive tape of 20 to 50 mm is applied. At the end of the bandolier at least 40 empty positions (equivalent to 160 mm) are there.

Tape Specification for SOT-23 Surface Mount Device



All dimensions in mm

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
SOT-23 T&R	3K/reel	136 gm/3K pcs	3" x 7.5" x 7.5"	12.0K	17" x 15" x 13.5"	192.0K	12 kgs
			9" x 9" x 9"	51.0K	19" x 19" x 19"	408.0K	28 kgs
	10K/reel	415 gm/10K pcs	13" x 13" x 0.5"	10.0K	17" x 15" x 13.5"	300.0K	16 kgs

Customer Notes

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

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