

Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company





SOT-23 Formed SMD Package

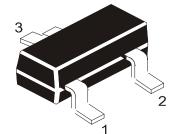
CMBT5087

SILICON PLANAR EPITAXIAL TRANSISTORS

PNP transistor

Marking

CMBT5087= 2Q



Pin configuration

1 = BASE

2 = EMITTER

3 = COLLECTOR



ABSOLUTE MAXIMUM RATINGS

	T.7		5 0	T 7
Collector-base voltage (open emitter)	V_{CBO}	max.	50	V
Collector-emitter voltage (open base)	V_{CEO}	max.	50	V
Emitter-base voltage (open collector)	V_{EBO}	max.	3	V
Collector current	I_C	max.	50	mA
Total power dissipation at $T_{amb} = 25^{\circ}C$	P_{tot}^*	max.	225	mW
Junction temperature	T_{j}	max.	150	° C
D.C. current gain	,			
$-I_C = 100 \ \mu A; \ V_{CE} = 5 \ V$	h_{FE}	min.	250	
		max.	800	
Transition frequency at $f = 20 \text{ MHz}$				
$I_C = 500 \ \mu A; \ V_{CE} = 5 \ V$	f_T	min.	40	MHz
RATINGS (at $T_A = 25^{\circ}\text{C}$ unless otherwise specified)				
Limiting values				
Collector-base voltage (open emitter)	V_{CBO}	max.	50	V
Collector-emitter voltage (open base)	V_{CEO}	max.	50	V

^{*}FR-5 Board = $1.0 \times 0.75 \times 0.062$ in.

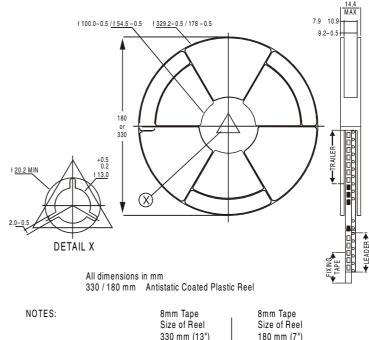
Emitter-base voltage (open collector) Collector current (d.c.) Total power dissipation at $T_{amb} = 25^{\circ}C$ Storage temperature Junction temperature	V_{EBO} I_{C} P_{tot}^* T_{stg} T_j	max. max. max. -55 to max.	50 225	
THERMAL RESISTANCE From junction to ambient	R _{th j–a}		417	°/ W
CHARACTERISTICS (at $T_A = 25$ °C unless otherwise specified	fied)			
Collector cut-off current $I_E = 0; V_{CB} = 10 V$ $I_E = 0; V_{CB} = 35 V$	I_{CBO}	max. max.		nA nA
Breakdown voltages				
$I_C = 1 mA; I_B = 0$ $I_C = 100 \mu A; I_E = 0$	V_{CEO} V_{CBO}	min. min.	50 50	
Saturation voltage				
$I_C = 10 \text{ mA}; I_B = 1.0 \text{ mA}$ $I_C = 10 \text{ mA}; I_B = 1.0 \text{ mA}$	V _{CEsat} V _{BEsat}	max. max.	300 0.85	m V V
D.C. current gain $I_C = 100 \ \mu A; \ V_{CE} = 5 \ V$	h_{FE}	min. max.	250 800	
$I_C = 1 mA; V_{CE} = 5 V$		min.	250	
$I_C = 10 mA; V_{CE} = 5 V$		min.	250	
Collector capacitance at $f = 100 \text{ KHz}$ $I_E = 0$; $V_{CB} = 5 \text{ V}$	C_{ob}	max.	4.0	рF
Transition frequency at $f = 20$ MHz $I_C = 500 \mu A$; $V_{CE} = 5 V$	f_T	min.	40	МНг
Small signal current $IC = 1 \text{ mA}$; $VCE = 5 \text{ V}$; $f = 1 \text{ KHz}$	h _{fe}	min. max.	250 900	
Noise figure $I_C = 20 \ \mu A; \ V_{CE} = 5 \ V; \ R_S = 10 \ k\Omega$	N_F	max.	2.0	dВ
f=10 Hz to 15.7 KHz $I_C=100~\mu A;~V_{CE}=5~V;~R_S=3.0~k\Omega~f=1.0~KHz$	N_F	max.	2.0	dВ

^{*}FR-5 Board = $1.0 \times 0.75 \times 0.62$ in.

SOT-23 Formed SMD Package

2.50 +/- 0.10 +/- 0.05 0.62 1.30----+/-0.05 0.62 40.08 -0.02 +/-0.025 1.90 cL 3 - 0.05 - 1.30 +/- 0.05 0.62 |← 0.62 0.08 0.08 MIN MIN PARTING LINE RO.08 R0.08 0.21 2.50 +/-0.10

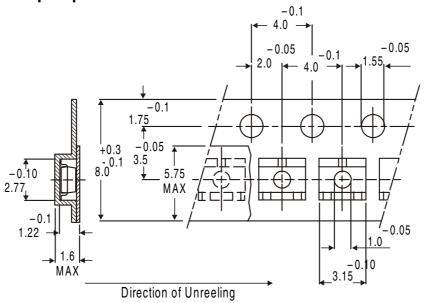
SOT-23 Package Reel Information Reel specifications for Packing (13"/7" reels)



330 mm (13") 180 mm (7") No. of Devices 10.000 Pcs 3.000 Pcs

- The bandolier of 330 mm reel contains at least 10,000 devices.
- The bandolier of 180 mm reel contains at least 3,000 devices.
- No more than 0.5% missing devices / reel. 50 empty compartments for 330 mm reel. 15 empty compartments for 180 mm reel.
- Three consecutive empty places might be found provided this gap is followed by 6 consecutive devices.
- The carrier tape (leader) starts with at least 75 empty positions (equivalent to 330 mm). In order to fix the carrier tape a self adhesive tape of 20 to 50 mm is applied. At the end of the bandolier at least 40 empty positions (equivalent to 160 mm) are there.

Tape Specification for SOT-23 Surface Mount Device



All dimensions in mm

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
SOT-23 T&R	3K/reel	136 gm/3K pcs	3" x 7.5" x 7.5" 9" x 9" x 9"		17" x 15" x 13.5" 19" x 19" x 19"	192.0K 408.0K	12 kgs 28 kgs
	10K/reel	415 gm/10K pcs	13" x 13" x 0.5"	10.0K	17" x 15" x 13.5"	300.0K	16 kgs

Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
 - 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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