

SOT-23 Formed SMD Package

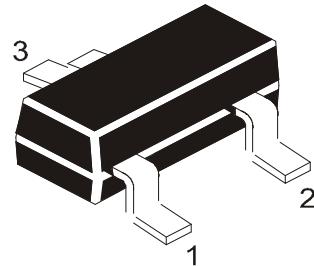
CMBT4125

GENERAL PURPOSE TRANSISTOR

P-N-P transistor

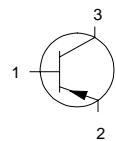
Marking

CMBT4125 = 5D



Pin configuration

- 1 = BASE
- 2 = Emitter
- 3 = COLLECTOR



ABSOLUTE MAXIMUM RATINGS

Collector-base voltage (open emitter)	$-V_{CBO}$	max.	30	V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	30	V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	4	V
Collector current (d.c.)	$-I_C$	max.	200	mA
Total power dissipation at $T_{amb} = 25^\circ\text{C}$	P_{tot}	max	350	mW
D.C. current gain $-I_C = 2 \text{ mA}; -V_{CE} = 1 \text{ V}$	h_{FE}	min.	50	
		max.	150	

RATINGS (at $T_A = 25^\circ\text{C}$ unless otherwise specified)

Limiting values

Collector-base voltage (open emitter)	$-V_{CBO}$	max.	30	V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	30	V

<i>Emitter-base voltage (open collector)</i>	$-V_{EBO}$	<i>max.</i>	4	V
<i>Collector current (d.c.)</i>	$-I_C$	<i>max.</i>	200	mA
<i>Total power dissipation at $T_{amb} = 25^\circ C$</i>	P_{tot}	<i>max</i>	350	mW
<i>Storage temperature</i>	T_{stg}	-55 to $+150$	$^\circ C$	
<i>Junction temperature</i>	T_j	<i>max.</i>	150	$^\circ C$

THERMAL CHARACTERISTICS

$T_j = P (R_{th} j-t + R_{th} s-a) + T_{amb}$	$R_{th} j-a$	556	$^\circ C/mW$
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CHARACTERISTICS (at $T_A = 25^\circ C$ unless otherwise specified)
Collector-emitter breakdown voltage

$-I_C = 1$ mA; $I_B = 0$	$-V_{(BR)CEO}$	<i>min.</i>	30	V
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Collector-base breakdown voltage

$-I_C = 10$ mA; $I_E = 0$	$-V_{(BR)CBO}$	<i>min.</i>	30	V
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Emitter-base breakdown voltage

$-I_E = 10$ mA; $I_C = 0$	$-V_{(BR)EBO}$	<i>min.</i>	4	V
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Collector cut-off current

$-V_{CB} = 20$ V; $I_E = 0$ V	$-I_{CBO}$	<i>max.</i>	50	nA
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Emitter cut-off current

$V_{BE} = 3$ V; $I_C = 0$	I_{EBO}	<i>max.</i>	50	nA
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Output capacitance at $f = 100$ kHz

$I_E = 0$; $-V_{CB} = 5$ V	C_c	<i>max.</i>	4.5	pF
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Input capacitance at $f = 100$ kHz

$I_C = 0$; $-V_{BE} = 0.5$ V	C_e	<i>max.</i>	10	pF
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Saturation voltages

$-I_C = 50$ mA; $-I_B = 5$ mA	$-V_{CEsat}$	<i>max.</i>	0.4	V
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$-I_C = 50$ mA; $-I_B = 5$ mA	$-V_{BEsat}$	<i>max.</i>	0.95	V
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D.C. current gain

$-I_C = 2$ mA; $-V_{CE} = 1$ V	h_{FE}	<i>min.</i>	50	
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$-I_C = 50$ mA; $-V_{CE} = 1$ V	h_{FE}	<i>min.</i>	25	
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Noise figure at $R_S = 1$ kW

$-I_C = 100$ mA; $-V_{CE} = 5$ V	NF	<i>max.</i>	5	dB
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Small signal current gain

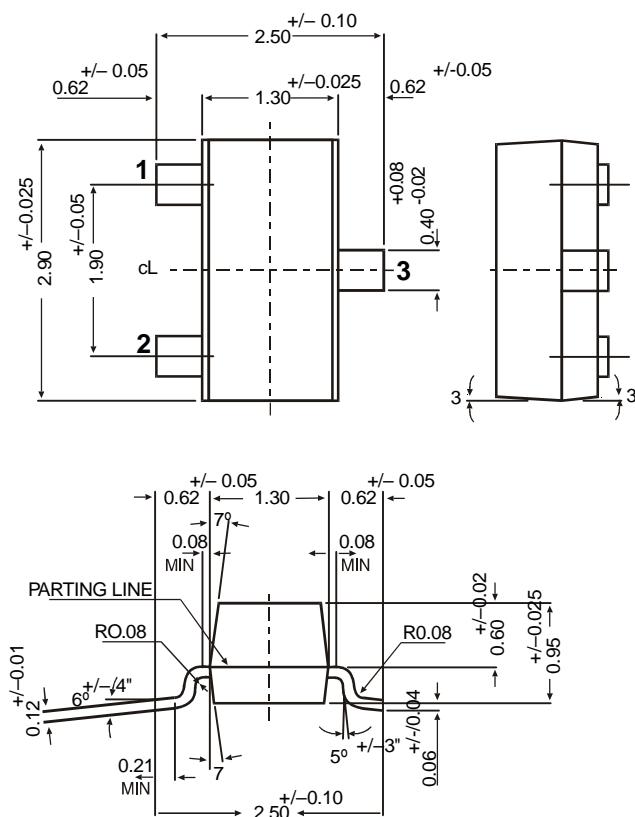
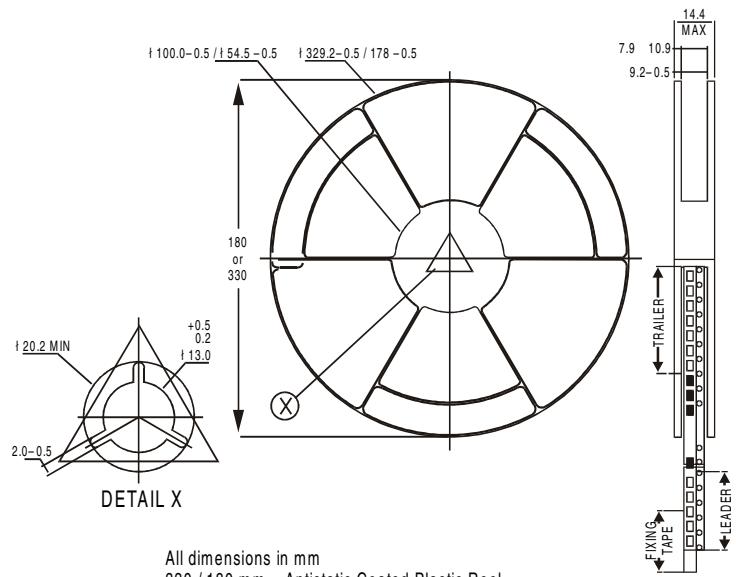
$-V_{CE} = 1V$; $-I_C = 2$ mA; $f = 1$ KHz	h_{fe}	<i>min.</i>	50	
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	<i>max.</i>	150	
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Transition frequency

$-V_{CE} = 20V$; $-I_C = 10$ mA; $f = 100$ MHz	f_T	<i>min.</i>	200	MHz
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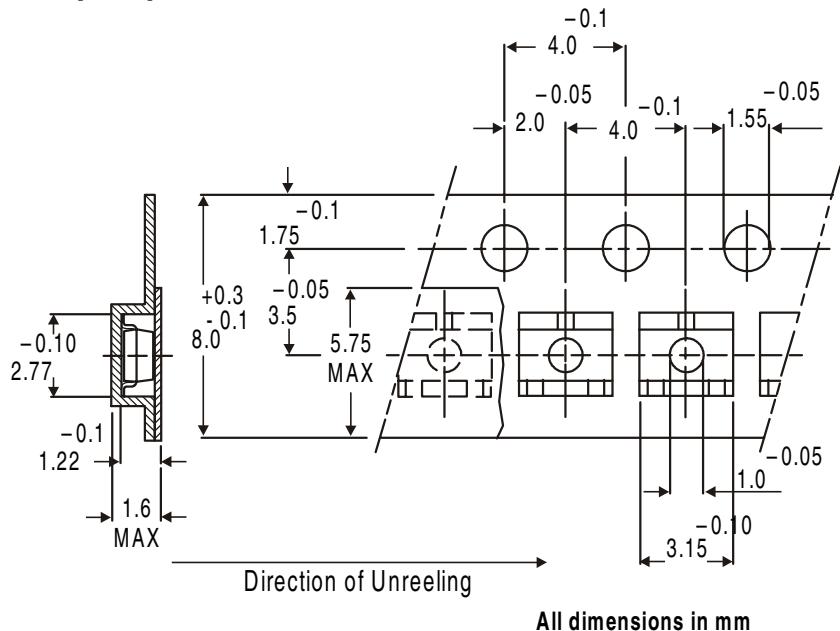
SOT-23 Package Reel Information
Reel specifications for Packing (13"/7" reels)

NOTES:

8mm Tape Size of Reel 330 mm (13") No. of Devices	8mm Tape Size of Reel 180 mm (7") 10,000 Pcs	8mm Tape Size of Reel 180 mm (7") 3,000 Pcs
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1. The bandolier of 330 mm reel contains at least 10,000 devices.
2. The bandolier of 180 mm reel contains at least 3,000 devices.
3. No more than 0.5% missing devices / reel. 50 empty compartments for 330 mm reel. 15 empty compartments for 180 mm reel.
4. Three consecutive empty places might be found provided this gap is followed by 6 consecutive devices.
5. The carrier tape (leader) starts with at least 75 empty positions (equivalent to 330 mm). In order to fix the carrier tape a self adhesive tape of 20 to 50 mm is applied. At the end of the bandolier at least 40 empty positions (equivalent to 160 mm) are there.

Tape Specification for SOT-23 Surface Mount Device



Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
SOT-23 T&R	3K/reel	136 gm/3K pcs	3" x 7.5" x 7.5" 9" x 9" x 9"	12.0K 51.0K	17" x 15" x 13.5" 19" x 19" x 19"	192.0K 408.0K	12 kgs 28 kgs
	10K/reel	415 gm/10K pcs	13" x 13" x 0.5"	10.0K	17" x 15" x 13.5"	300.0K	16 kgs

Customer Notes

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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