



An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company

BCW29 BCW30

SILICON PLANAR EPITAXIAL TRANSISTORS

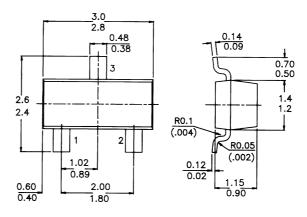
P-N-P transistors

Marking

BCW29 = C1

BCW30 = C2

PACKAGE OUTLINE DETAILS ALL DIMENSIONS IN mm



Pin configuration

1 = BASE

2 = EMITTER 3 = COLLECTOR

1

ABSOLUTE MAXIMUM RATINGS

			BCW29		BCW30
D.C. current gain at $T_j = 25$ °C		>	120		215
$-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$	h_{FE}	<	260		500
Collector–base voltage (open emitter)	$-V_{CB0}$	max		32	V
Collector–emitter voltage (open base)	$-V_{CE0}$	max		32	V
Collector current (peak value)	$-I_{CM}$	max		200	mA
Total power dissipation up to $T_{amb} = 25$ °C	P_{tot}	max	,	250	mW
Junction temperature	T_j	max		150	° C
Transition frequency at $f = 35 \text{ MHz}$	•				
$-I_C = 10 \ mA; -V_{CE} = 5 \ V$	f_T	typ.		150	MHz
Noise figure at $R_S = 2 \text{ kW}$					
$-I_c = 200 \text{ mA}; -V_{CE} = 5 \text{ V};$					
f = 1 kHz; B = 200 Hz	F	<		10	dВ

BCW29 BCW30

RATINGS (at $T_A = 25^{\circ}$ C unless otherwise spec	cified)					
Limiting values	Vana	444 G M			32	17
Collector-base voltage (open emitter)	$-V_{CB0}$	max.				
Collector–emitter voltage ($V_{BE} = 0$) Collector–emitter voltage (open base)	$-V_{CES}$	max.			32	V
$-I_C = 2 mA$	$-V_{CE0}$	max.			32	V
Emitter–base voltage (open collector)	$-V_{EB0}$	max.			5	V
Collector current (d.c.)	$-I_C$	max.			100	mA
Collector current (peak value)	$-I_{CM}$	max.			200	mA
Total power dissipation up to $T_{amb} = 25$ °C	P_{tot}	max.			250	mW
Storage temperature ° C	T_{stg}			–55 to	+150)
Junction temperature	T_j	max.			150	° C
THERMAL RESISTANCE						
From junction to ambient	R_{th} $j-a$	=			500	K/W
CHARACTERISTICS						
$T_i = 25$ °C unless otherwise specified						
Collector cut–off current						
$I_E = 0$; $-V_{CB} = 32 \ V$	$-I_{CB0}$	<			100	nA
$I_E = 0$; $-V_{CB} = 32 \text{ V}$; $T_i = 100 ^{\circ}\text{C}$	$-I_{CB0}$	<			10	
-L +, + CB += +, - -+++	-CD0					
Base-emitter voltage						
Base-emitter voltage	$-V_{DF}$		60	0 to 75	0	тV
$-I_C = 2 mA; -V_{CE} = 5 V$	$-V_{BE}$		60	0 to 75	0	mV
$-I_C = 2 \text{ mA}; -V_{CE} = 5 \text{ V}$ Saturation voltages		tun	60	0 to 75		
$-I_C = 2 mA; -V_{CE} = 5 V$	−V _{BE} −V _{CEsat}		60	0 to 75	80	mV
$-I_C = 2 \text{ mA}; -V_{CE} = 5 \text{ V}$ Saturation voltages	-V _{CEsat}	<	60	0 to 75	80 300	mV mV
$-I_C = 2 mA; -V_{CE} = 5 V$ Saturation voltages $-I_C = 10 mA; -I_B = 0.5 mA$	-V _{CEsat}	< typ.	60	0 to 75	80 300 720	mV mV mV
$-I_C = 2 \text{ mA}; -V_{CE} = 5 \text{ V}$ Saturation voltages	-V _{CEsat}	typ.typ.	60	0 to 75	80 300 720 150	mV mV mV
$-I_C = 2 mA; -V_{CE} = 5 V$ Saturation voltages $-I_C = 10 mA; -I_B = 0.5 mA$	-V _{CEsat}	typ.typ.	60	0 to 75	80 300 720 150	mV mV mV
$-I_C = 2 mA; -V_{CE} = 5 V$ Saturation voltages $-I_C = 10 mA; -I_B = 0.5 mA$ $-I_C = 50 mA; -I_B = 2.5 mA$	-V _{CEsat}	<pre>typ. typ. typ.</pre>		0 to 75	80 300 720 150 810	mV mV mV
$-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Saturation voltages $-I_C = 10 \ mA; \ -I_B = 0.5 \ mA$ $-I_C = 50 \ mA; \ -I_B = 2.5 \ mA$	-V _{CEsat} -V _{BEsat} -V _{CEsat} -V _{BEsat}	<pre>< typ. typ. typ. </pre>	29	0 to 75	80 300 720 150 810	mV mV mV
$-I_C = 2 mA; -V_{CE} = 5 V$ Saturation voltages $-I_C = 10 mA; -I_B = 0.5 mA$ $-I_C = 50 mA; -I_B = 2.5 mA$	-V _{CEsat}	<pre>typ. typ. typ.</pre>		0 to 75	80 300 720 150 810	mV mV mV
$-I_C = 2 mA; -V_{CE} = 5 V$ Saturation voltages $-I_C = 10 mA; -I_B = 0.5 mA$ $-I_C = 50 mA; -I_B = 2.5 mA$ D.C. current gain $-I_C = 10 mA; -V_{CE} = 5 V$	-V _{CEsat} -V _{BEsat} -V _{CEsat} -V _{BEsat}	<pre>< typ. typ. typ. </pre>	29	0 to 75	80 300 720 150 810	mV mV mV
$-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Saturation voltages $-I_C = 10 \ mA; \ -I_B = 0.5 \ mA$ $-I_C = 50 \ mA; \ -I_B = 2.5 \ mA$	-V _{CEsat} -V _{BEsat} -V _{CEsat} -V _{BEsat}	<pre>typ. typ. typ. BCW typ.</pre>	29 90	0 to 75	80 300 720 150 810 30 150	mV mV mV
$-I_C = 2 mA; -V_{CE} = 5 V$ Saturation voltages $-I_C = 10 mA; -I_B = 0,5 mA$ $-I_C = 50 mA; -I_B = 2,5 mA$ D.C. current gain $-I_C = 10 mA; -V_{CE} = 5 V$ $-I_C = 2 mA; -V_{CE} = 5 V$	-V _{CEsat} -V _{BEsat} -V _{CEsat} -V _{BEsat}	<pre> typ. typ. typ. BCW typ. ></pre>	29 90 120	0 to 75	80 300 720 150 810 30 150 215	mV mV mV
$-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Saturation voltages $-I_C = 10 \ mA; \ -I_B = 0,5 \ mA$ $-I_C = 50 \ mA; \ -I_B = 2,5 \ mA$ D.C. current gain $-I_C = 10 \ mA; \ -V_{CE} = 5 \ V$ $-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Collector capacitance at $f = 1 \ MHz$	-V _{CEsat} -V _{BEsat} -V _{CEsat} -V _{BEsat}	<	29 90 120		80 300 720 150 810 30 150 215	mV mV mV
$-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Saturation voltages $-I_C = 10 \ mA; \ -I_B = 0,5 \ mA$ $-I_C = 50 \ mA; \ -I_B = 2,5 \ mA$ D.C. current gain $-I_C = 10 \ mA; \ -V_{CE} = 5 \ V$ $-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Collector capacitance at $f = 1 \ MHz$ $I_E = I_e = 0; \ -V_{CB} = 10 \ V$	-V _{CEsat} -V _{BEsat} -V _{CEsat} -V _{BEsat}	<pre> typ. typ. typ. typ. BCW typ. ></pre>	29 90 120	0 to 75	80 300 720 150 810 30 150 215	mV mV mV
$-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Saturation voltages $-I_C = 10 \ mA; \ -I_B = 0,5 \ mA$ $-I_C = 50 \ mA; \ -I_B = 2,5 \ mA$ D.C. current gain $-I_C = 10 \ mA; \ -V_{CE} = 5 \ V$ $-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Collector capacitance at $f = 1 \ MHz$ $I_E = I_e = 0; \ -V_{CB} = 10 \ V$ Transition frequency at $f = 35 \ MHz$	-V _{CEsat} -V _{Esat} -V _{CEsat} -V _{BEsat} -V _{BEsat}	<pre> typ. typ. typ. BCW typ. > </pre>	29 90 120	4,5	80 300 720 150 810 30 150 215	mV mV mV mV
$-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Saturation voltages $-I_C = 10 \ mA; \ -I_B = 0.5 \ mA$ $-I_C = 50 \ mA; \ -I_B = 2.5 \ mA$ D.C. current gain $-I_C = 10 \ mA; \ -V_{CE} = 5 \ V$ $-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Collector capacitance at $f = 1 \ MHz$ $I_E = I_e = 0; \ -V_{CB} = 10 \ V$ Transition frequency at $f = 35 \ MHz$ $-I_C = 10 \ mA; \ -V_{CE} = 5 \ V$	-V _{CEsat} -V _{BEsat} -V _{CEsat} -V _{BEsat}	<	29 90 120		80 300 720 150 810 30 150 215	mV mV mV
$-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Saturation voltages $-I_C = 10 \ mA; \ -I_B = 0.5 \ mA$ $-I_C = 50 \ mA; \ -I_B = 2.5 \ mA$ D.C. current gain $-I_C = 10 \ mA; \ -V_{CE} = 5 \ V$ $-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Collector capacitance at $f = 1 \ MHz$ $I_E = I_e = 0; \ -V_{CB} = 10 \ V$ Transition frequency at $f = 35 \ MHz$ $-I_C = 10 \ mA; \ -V_{CE} = 5 \ V$ Noise figure at $RS = 2 \ kW$	-V _{CEsat} -V _{Esat} -V _{CEsat} -V _{BEsat} -V _{BEsat}	<pre> typ. typ. typ. BCW typ. > </pre>	29 90 120	4,5	80 300 720 150 810 30 150 215	mV mV mV mV
$-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Saturation voltages $-I_C = 10 \ mA; \ -I_B = 0.5 \ mA$ $-I_C = 50 \ mA; \ -I_B = 2.5 \ mA$ D.C. current gain $-I_C = 10 \ mA; \ -V_{CE} = 5 \ V$ $-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Collector capacitance at $f = 1 \ MHz$ $I_E = I_e = 0; \ -V_{CB} = 10 \ V$ Transition frequency at $f = 35 \ MHz$ $-I_C = 10 \ mA; \ -V_{CE} = 5 \ V$ Noise figure at $RS = 2 \ kW$ $-I_C = 200 \ mA; \ -V_{CE} = 5 \ V$	-VCEsat -VBEsat -VCEsat -VBEsat hFE Cc	<pre> typ. typ. typ. BCW typ. > typ. typ. </pre>	29 90 120	4,5 150	80 300 720 150 810 30 150 215	mV mV mV mV
$-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Saturation voltages $-I_C = 10 \ mA; \ -I_B = 0.5 \ mA$ $-I_C = 50 \ mA; \ -I_B = 2.5 \ mA$ D.C. current gain $-I_C = 10 \ mA; \ -V_{CE} = 5 \ V$ $-I_C = 2 \ mA; \ -V_{CE} = 5 \ V$ Collector capacitance at $f = 1 \ MHz$ $I_E = I_e = 0; \ -V_{CB} = 10 \ V$ Transition frequency at $f = 35 \ MHz$ $-I_C = 10 \ mA; \ -V_{CE} = 5 \ V$ Noise figure at $RS = 2 \ kW$	-V _{CEsat} -V _{Esat} -V _{CEsat} -V _{BEsat} -V _{BEsat}	<pre> typ. typ. typ. BCW typ. > </pre>	29 90 120	4,5	80 300 720 150 810 30 150 215	mV mV mV mV

Notes

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of
Continental Device India Limited
C-120 Naraina Industrial Area, New Delhi 110 028, India.
Telephone + 91-11-579 6150 Fax + 91-11-579 9569, 579 5290
e-mail sales@cdil.com www.cdil.com