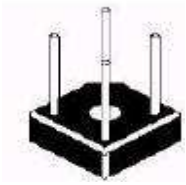
SINGLE PHASE SILICON BRIDGE RECTIFIER

KBPC8005-KBPC810



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Maximum Ratings (Ratings at T_a=25°C Ambient Temperature unless otherwise specified.
Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%)

DESCRIPTION	SYMBOL	KBPC 8005	KBPC 801	KBPC 802	KBPC 804	KBPC 806	KBPC 808	KBPC 810	UNIT
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current at T_c=50ºC	$I_{F(AV)}$	8.0							A
Peak Forward Surge Current, 8.3ms single half sine wave superimposed on rated load (JEDEC method)	I_{FSM}	250							A
Maximum Forward Voltage Drop Per Element at 4A DC and 25ºC	V_F	1.0							V
Maximum Reverse Current T_a=25°C	I_R	10							µA
at Rated DC Blocking Voltage T_a=100°C		500							µA
Typical Junction Capacitance	*C_J	200							pF
Typical Thermal Resistance	**$R_{th\ (j-a)}$	21							°C/W
Typical Thermal Resistance	***$R_{th\ (j-c)}$	6.0							°C/W
Operating and Storage Temperature Range	T_J, T_{stg}	- 55 to +125							°C

***Measured at 1MHz and applied reverse voltage of 4.0 VDC**
****Unit mounted on 8.6 x 8.6 x 0.24" thick (22 x 22 x 0.6cm) Ai.Plate**
*****United mounted on PCB at 0.375" (9.5mm) lead length with 0.5 x 0.5" (12 x 12mm) copper pads**

KBPC8005-810Rev280506E

KBPC-8
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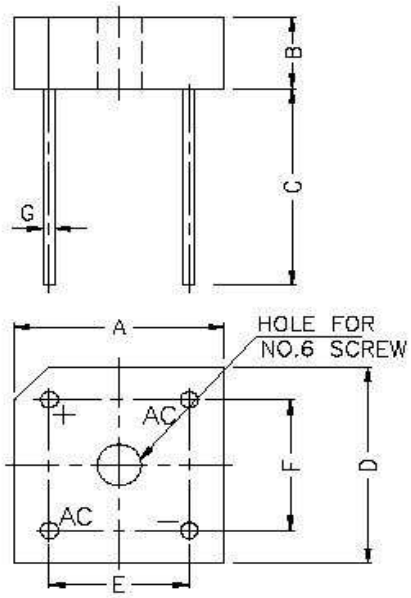

DIM	MIN.	MAX.
A	18.5	19.6
B	6.5	7.5
C	19.1	–
D	18.5	19.6
E	12.2	13.2
F	12.2	13.2
G	1.0	1.1

ALL DIMENSIONS ARE mm

50 Pcs/BOX

KBPC8005-810Rev280506E

KBPC-8
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Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.

2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

CDIL is a registered Trademark of
Continental Device India Limited
C-120 Naraina Industrial Area, New Delhi 110 028, India.
Telephone + 91-11-2579 6150, 4141 1112 Fax + 91-11-2579 5290, 4141 1119
email@cdil.com www.cdilsemi.com