

Continental Device India Limited

IS/ISO 9002 Lic# QSC/I - 000019.2

An IS/ISO 9002 and IECQ Certified Manufacturer

NPN SILICON HILGH VOLTAGE VIDEO TRANSISTORS

BF391 BF392 BF393

TO-92

Plastic Package

Designed For High Voltage Video Amplifier in Television Receivers.

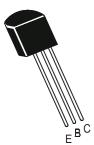
ABSOLUTE MAXIMUM RATINGS(Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	391	392	393	UNITS
Collector Emitter Voltage	V_{CEO}	200	250	300	V
Collector Base Voltage	V_{CBO}	200	250	300	V
Emitter Base Voltage	V_{EBO}	6	6	6	V
Collector Current Continuous	I _C		500		mA
Power Dissipation@ Ta=25°C	P_{D}		625		mW
Power Dissipation@ Tc=25°C	P_{D}		1.5		W
Operating And Storage Junction	T_{j},T_{stg}		-55 to +150	ı	ōC
Temperature Range					
THERMAL RESISTANCE					
Junction to ambient	$R_{th(j-a)}$		200		ºC/W
Junction to case	$R_{th(j-c)}$		83.3		ºC/W

ELECTRICAL CHARACTERISTICS (Ta=25°C Unless Otherwise Specified)

DESCRIPTION	SYMBOL	TEST CONDITION	391	392	393	UNITS
Collector Emitter Voltage	$V_{\sf CEO}^*$	$I_C=1.0$ mA, $I_B=0$	>200	>250	>300	V
Collector Base Voltage	V_{CBO}	$I_C = 100 \mu A.I_E = 0$	>200	>250	>300	V
Emitter Base Voltage	V_{EBO}	$I_E=100\mu A,\ I_C=0$	>6	>6	>6	V
Collector Cut off Current	I_{CBO}	$V_{CB} = 160 V, I_{E} = 0$	<0.1			μΑ
		V_{CB} =200 V , I_E =0		<0.1	<0.1	μΑ
Emitter Cut off Current	I _{EBO}	$V_{EB} = 4.0 V, I_C = 0$	<0.1			μΑ
		$V_{EB} = 6.0 V, I_C = 0$		<0.1	<0.1	μΑ
DC Current Gain	h _{FE}	I _C =1.0mA,V _{CE} =10V	>25	>25	>25	
		$I_C=10mA, V_{CE}=10V$	>40	>40	>40	

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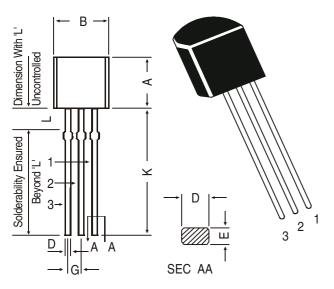
ELECTRICAL CHARACTERISTICS (Ta=25°C Unless Specified Otherwise)

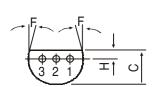
DESCRIPTION	SYMBOL	SYMBOL TEST CONDITION		392	393	UNITS	
Collector Emitter Saturation Voltage	$V_{\text{CE(sat)}}$	I _C =20mA,I _B =2mA	<2	<2	<2	٧	
Base Emitter Saturation Voltage	$V_{BE(sat)}$	I _C =20mA,I _B =2mA	<2	<2	<2	V	
Feedback Capacitance	C_{re}	$I_E=0$, $V_{CB}=60V$, $f=1.0MH_Z$	<2	<2	<2	pF	
Current Gain - Bandwidth Product	f _T	Ic=10mA, V _{CE} =20V, f=20MH _Z	>50	>50	>50	MHz	
*Pulse Condition: = Pulse Width < 30	Ous, Duty C	ycle < 2%.					

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TO-92 Transistors on Tape and Ammo Pack



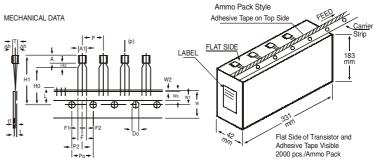


PIN CONFIGURATION

- 1. COLLECTOR
- 2. BASE
- **EMITTER**

DIM	MIN.	MAX.	
Α	4.32	5.33	
В	4.45	5.20	
С	3.18	4.19	
D	0.41	0.55	
Е	0.35	0.50	
F	5 D	EG	
G	1.14	1.40	
Н	1.14	1.53	
K	12.70	_	
L	1.982	2.082	

All diminsions in mm.



All dimensions in mm unless specified otherwise

ITEM		SPECIFICATION				
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL.	REMARKS
BODY WIDTH BODY HEIGHT BODY THICKNESS PITCH OF COMPONENT	A1 A T P	4.0 4.8 3.9	12.7	4.8 5.2 4.2	±1	
FEED HOLE PITCH FEED HOLE CENTRE TO COMPONENT CENTRE	Po P2		6.35		±0.3	CUMULATIVE PITCH ERROR 1.0 mm/20 PITCH TO BE MEASURED AT
DISTANCE BETWEEN OUTER LEADS COMPONENT ALIGNMENT TAPE WIDTH HOLD-DOWN TAPE WIDTH HOLE POSITION	F △h W Wo W1		5.08 0 18 6 9	1	+0.6 -0.2 ±0.5 ±0.2 +0.7 -0.5	BOTTOM OF CLINCH AT TOP OF BODY
HOLD-DOWN TAPE POSITION LEAD WIRE CLINCH HEIGHT COMPONENT HEIGHT LENGTH OF SNIPPED LEADS FEED HOLE DIAMETER TOTAL TAPE THICKNESS LEAD - TO - LEAD DISTANCEF1,	W2 Ho H1 L Do t F2		0.5 16 4 2.54	23.25 11.0 1.2	±0.2 ±0.5 ±0.2 +0.4 -0.1	t1 0.3 - 0.6
CLINCH HEIGHT PULL - OUT FORCE	H2 (P)	6N		3	-0.1	

NOTES

- MAXIMUM ALIGNMENT DEVIATION BETWEEN LEADS NOT TO BE GREATER THAN 0.2 mm.
 MAXIMUM NON-CUMULATIVE VARIATION BETWEEN TAPE FEED HOLES SHALL NOT EXCEED 1 mm IN 20 PITCHES.
 - PITCHES.

 3. HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO EXPOSURE OF ADHESIVE.

 4. NO MORE THAN 3 CONSECUTIVE MISSING COMPONENTS ARE PERMITTED.

 5. A TAPE TRAILER, HAVING AT LEAST THREE FEED HOLES ARE REQUIRED AFTER THE LAST COMPONENT.

 6. SPLICES SHALL NOT INTERFERE WITH THE SPROCKET FEED HOLES.

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX			
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt	
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs	
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs	

Notes **BF392**

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Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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