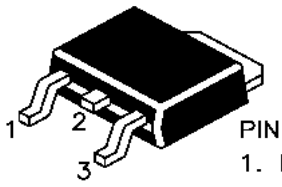
NPN SILICON POWER TRANSISTOR

CJD81

DPAK (TO-252)
Plastic Package



PIN CONFIGURATION
1. BASE
2. COLLECTOR
3. EMITTER

For High Current Driver Applications

ABSOLUTE MAXIMUM RATINGS (T_a=25ºC)

DESCRIPTION	SYMBOL	VALUE	UNITS
Collector Base Voltage	V_{CBO}	30	V
Collector Emitter Voltage	V_{CEO}	10	V
Emitter Base Voltage	V_{EBO}	6.0	V
Collector Current	I_C	3.0	A
Collector Current (Pulse)	I_{CP}	5.0	A
Collector Power Dissipation	P_D	0.9	W
Junction Temperature	T_j	150	ºC
Storage Temperature	T_{stg}	- 55 to +150	ºC

ELECTRICAL CHARACTERISTICS (T_a=25ºC unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Collector Emitter Voltage	V_{CEO}	I_C=1mA, I_B=0	10			V
Collector Base Voltage	V_{CBO}	I_C=10µA, I_E=0	30			V
Emitter Base Voltage	V_{EBO}	I_E=10µA, I_C=0	6.0			V
Collector Cut Off Current	I_{CBO}	V_{CB} =20V, I_E=0			0.1	µA
Emitter Cut Off Current	I_{EBO}	V_{EB}=4V, I_C = 0			0.1	µA
DC Current Gain	h_{FE}	I_C =3A, V_{CE}=2V	140			

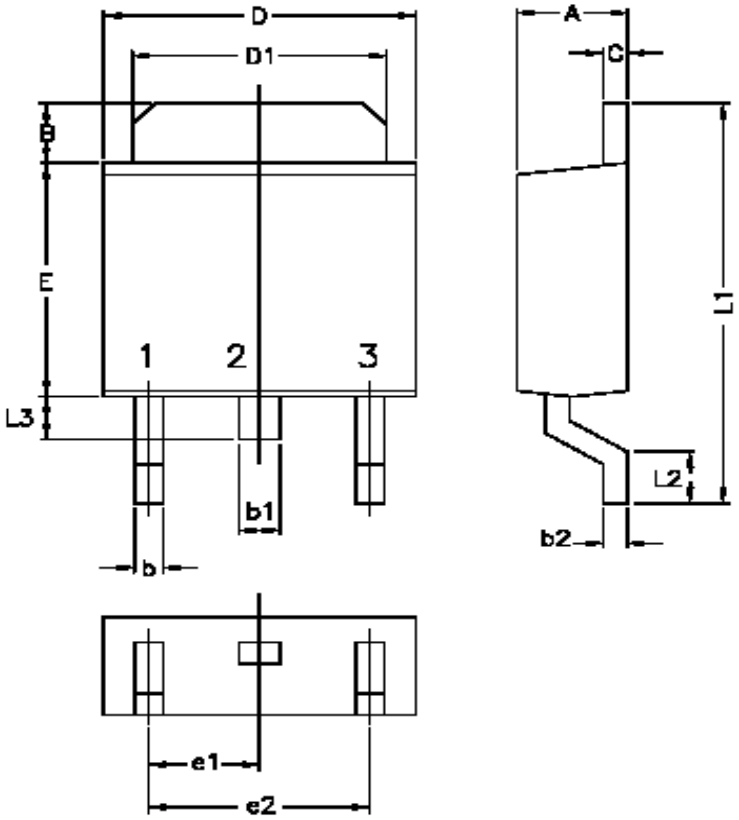
DYNAMIC CHARACTERISTICS

Transition Frequency	f_T	V_{CE}=10V, I_C=50mA		200		MHz
Output Capacitance	C_{ob}	V_{CB}=10V, I_E=0, f=1MHz	30			pF

MARKING	CDIL CJD81 MX XY
XY= Date Code	

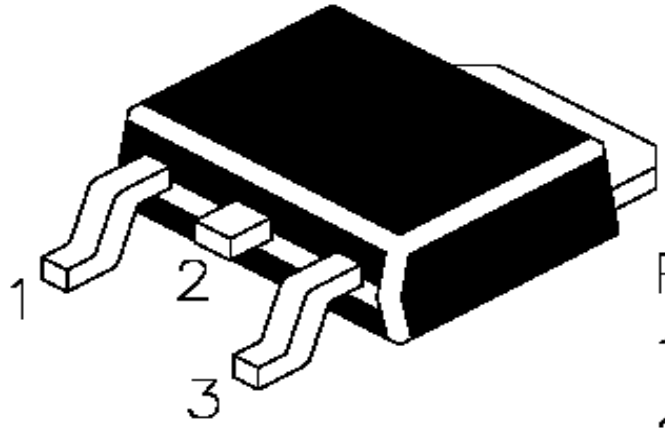
CJD81Rev230205E

DPAK PACKAGE OUTLINE DIMENSIONS



DIM	MIN.	MAX.
A	2.20	2.40
B	1.30	1.50
b	0.55	0.65
b1	0.75	0.85
b2	0.46	0.56
C	0.46	0.56
D	6.40	6.60
D1	5.20	5.40
E	5.40	5.60
e1	2.25	2.35
e2	4.50	4.70
L1	9.25	9.75
L2	0.5	—
L3	0.90	1.10

ALL DIMENSIONS ARE IN mm



PIN CONFIGURATION

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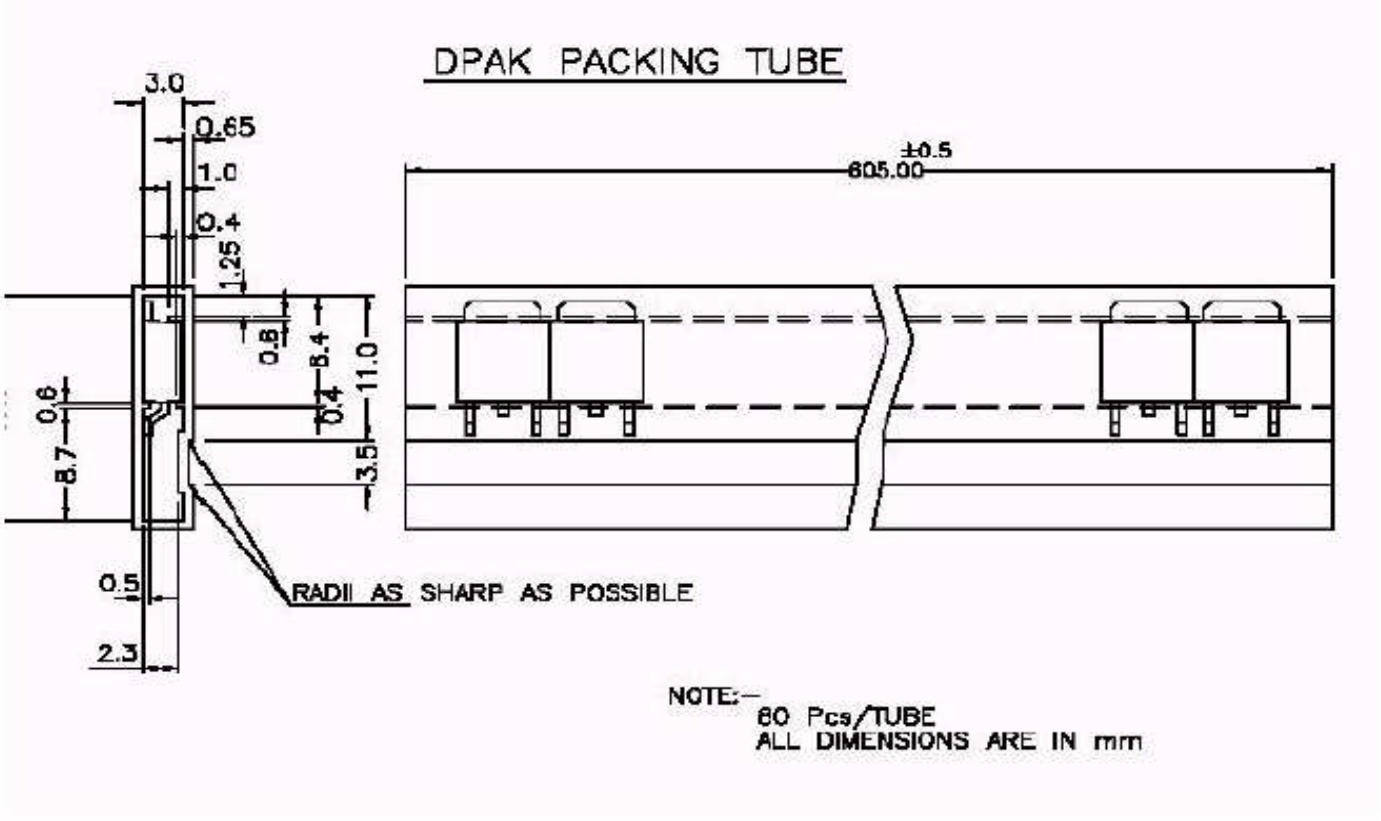

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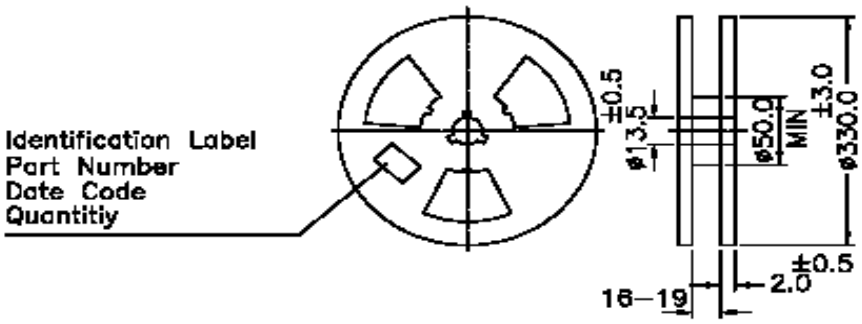
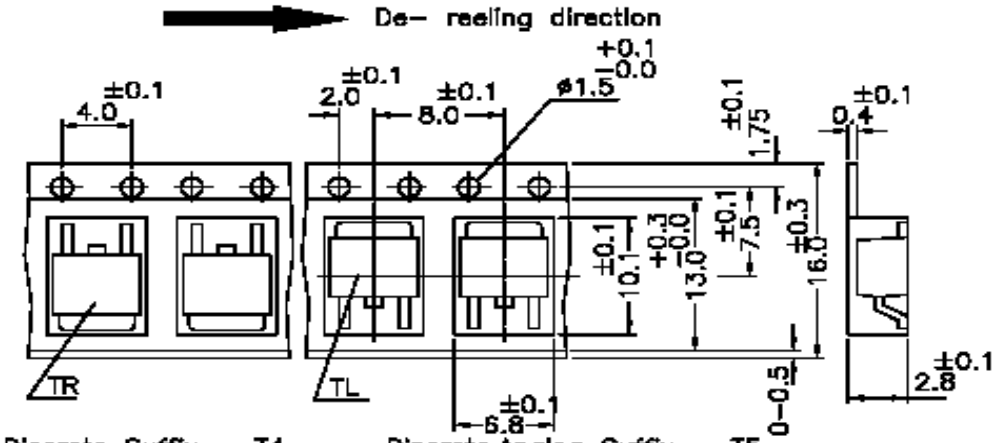
DPAK TAPE & REEL SPECIFICATION



ALL DIMENSIONS ARE IN mm
REEL ø 330 mm (13")
No of Device 2500

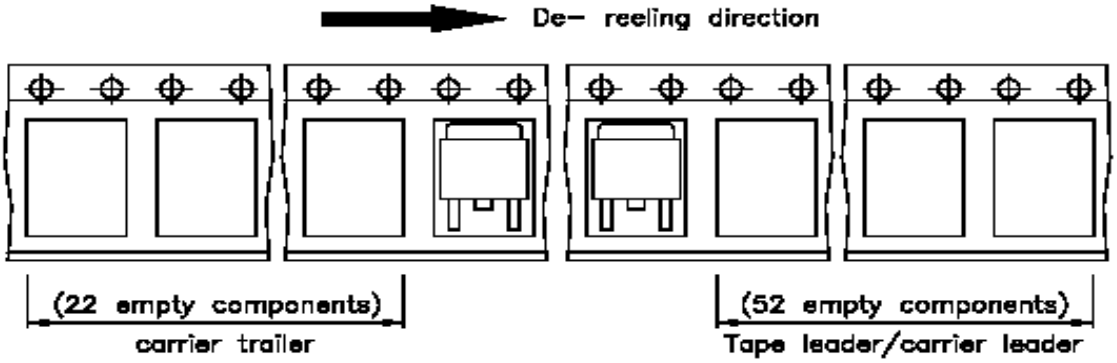
TAPE & REEL



Discrete Suffix – T4
Analog Suffix – RK

Discrete,Analog Suffix – T5

Notes:–
A maximum of three consecutive components may be missing. Provided this gap is followed by six consecutive components.



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Continental Device India Limited
C-120 Naraina Industrial Area, New Delhi 110 028, India.
Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-2579 5290, 5141 1119
email@cdil.com www.cdilsemi.com