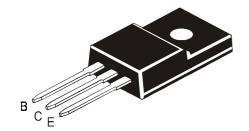




## SILICON PLANAR DARLINGTON POWER TRANSISTORS

CJF122 NPN CJF127 PNP

TO-220FP Fully Isolated Plastic Package



## **General Purpose Darlingtons Amplifier and Switching Applications**

### **ABSOLUTE MAXIMUM RATINGS**

DESCRIPTION	SYMBOL	VALUE	UNIT	
Collector Base Voltage	$V_{CBO}$	100	V	
Collector Emitter Voltage	V <sub>CEO</sub>	100	V	
Emitter Base Voltage	V <sub>EBO</sub>	5	V	
RMS Isolation Voltage (for 1sec, R.H.	(1) V <sub>ISOL</sub> (a)	3500	$V_{RMS}$	
<30%, T <sub>A</sub> =25°C )	(b)	1500	$V_{RMS}$	
Collector Current - Continuous	lc	5	A	
Peak		8	Α	
Base Current	l <sub>B</sub>	0.12	Α	
Total Power Dissipation @ Tc=25°C	$P_{D^{**}}$	30	W	
Derate Above 25°C		0.24	W/°C	
Total Power Dissipation @ Ta=25°C	$P_{D}$	2	W	
Derate Above 25°C		0.016	W/°C	
Operating and Storage Junction	$T_{i}T_{stg}$	- 65 to + 150	°C	
Temperature Range	<i>j,</i> 3			
THERMAL RESISTANCE				
From Junction to Ambient	$R_{\text{th (j-a)}}$	62.5	°C/W	
From Junction to Case	R <sub>th (j-c)</sub> **	4.1	°C/W	
Lead Temperature for Soldering Purpose	$T_L$	260	°C	

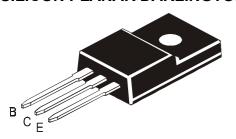
<sup>\*\*</sup>Measurement made with thermocouple contacting the bottom insulated mounting surface (in a location beneath the die), the device mounted on a heatsink with thermal grease and a mounting torque of >6 in.lbs.

(1) RMS Isolation Voltage : (a) 3500 V<sub>RMS</sub> with Package in Clip Mounting Position (b) 1500 V<sub>RMS</sub> with Package in Screw Mounting Position (for 1sec, R.H.<30%Ta=25°C; Pulse Test: Pulse Width ≤300µs, Duty Cycle≤2%)

## ELECTRICAL CHARACTERISTICS (T<sub>C</sub>=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Collector Emitter Sustaining Voltage	V <sub>CEO (sus)</sub> *	I <sub>C</sub> =100mA, I <sub>B</sub> =0	100	-	V
Collector Cut off Current	I <sub>CEO</sub>	$V_{CE}$ = 50V, $I_{B}$ =0	-	10	μΑ
	$I_{CBO}$	$V_{CB}$ = 100 $V$ , $I_{E}$ =0	-	10	μΑ
Emitter Cut off Current	$I_{EBO}$	$V_{BE}$ =5 $V$ , $I_{C}$ =0	-	2.0	mA
Collector Emitter Saturation Voltage	V <sub>CE (sat)</sub> *	$I_C=3A$ , $I_B=12mA$	-	2.0	V
•	(,	$I_C=5A$ , $I_B=20mA$	-	3.5	V

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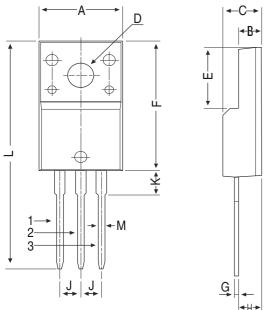
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Base Emitter on Voltage	$V_{BE (on)}$ *	I <sub>C</sub> =3A, V <sub>CE</sub> =3V	-	2.5	V
DC Current Gain	h <sub>FE</sub> *	$I_{C}$ =0.5A, $V_{CE}$ =3V $I_{C}$ =3A, $V_{CE}$ =3V	1000 2000	-	
DYNAMIC CHARACTERISTICS Small Signal Current Gain	lh <sub>fe</sub> l	I <sub>C</sub> =3A, V <sub>CE</sub> =4V,f=1MF	4.0	- -	
Output Capacitance	$C_ob$	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=0.1M CJF122 CJF127	lHz - -	200 300	pF pF

<sup>\*</sup> Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2 %

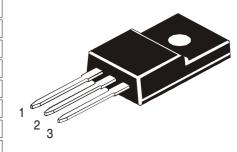
CJF122 NPN CJF127 PNP

# **TO-220FP Fully Isolated Plastic Package**

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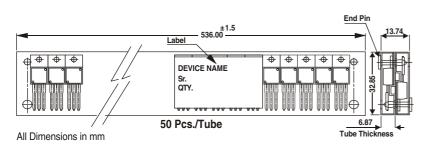
DIM	MIN	MAX			
А	9.80	10.36			
В	2.50	3.00			
С	4.30	4.90			
D	3.10	3.40			
Е	6.50	8.20			
F	14.80	17.27			
G	0.40	0.70			
Н	2.50	2.96			
J	2.34	2.74			
K	_	4.70			
L	_	30.05			
М	0.6	0.90			
All diminsions in mm.					

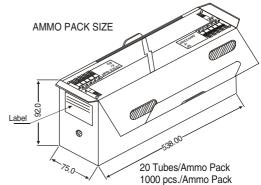


Pin Configuration

- 1. Base
- 2. Collector
- 3. Emitter

# **TO-220 FP Tube Packing**





## **Packing Detail**

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-220FP	200 pcs/polybag	396 gm/200 pcs	3" x 7.5" x 7.5"	1K	17" x 15" x 13.5"	16K	36 kgs
	50 pcs/tube	135 gm/50 pcs	3.5" x 3.7" x 21.5"	1K	19" x 19" x 19"	10K	28 kgs

Notes CJF122 NPN CJF127 PNP

TO-220FP Fully Isolated Plastic Package

## **Component Disposal Instructions**

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

#### Disclaimer

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