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4-channel 16bit 130 MSps FMC module

The module has four separate 16 bit ADC's (National Semiconductor 16V130)

Analog input is 50 Ω AC single ended through board-edge SMP connectors.

Sample clock input can be either external (single-ended, 50 Ω) connected through on-board UMCC connector or supplied from the carrier card as LVDS differential pair (mounting case).

On-board clock splitter supplies each individual ADC with matched clocks.

Sample data aligned clock output is taken from one of the ADC's and effed to the FMC connector to be used by the carrier card to clock in the digital samples. The clock is LVDS differential pair.

Power and temperature supervision is present.

The board is powered through the FMC connector.

Typical applications include

- radar
- sonar
- microwave
- general data aqcuisition systems

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For more information on the MAGIC solution, contact: sales@bitsim.com