PHE13005 Silicon diffused power transistor Rev. 03 — 20 November 2009

Product data sheet

Product profile 1.

1.1 General description

High voltage, high speed NPN planar-passivated power switching transistor in a SOT78 plastic package intended for use in high frequency electronic lighting ballast applications

1.2 Features and benefits

- Fast switching
- Low thermal resistance
- High voltage capability of 700 V

1.3 Applications

■ Electronic lighting ballasts

1.4 Quick reference data

Table 1. **Quick reference**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I_{C}	collector current	DC; see Figure 3, 1 and 2	-	-	4	Α
P _{tot}	total power dissipation	$T_{mb} \le 25 \text{°C}$; see Figure 4	-	-	75	W
V_{CESM}	collector-emitter peak voltage	$V_{BE} = 0 V$	-	-	700	V
Static ch	Static characteristics					
h _{FE}	DC current gain	$I_C = 1 \text{ A}; V_{CE} = 5 \text{ V};$ $T_{mb} = 25 \text{ C}; \text{ see } \frac{\text{Figure 11}}{\text{ constant }}$	12	20	40	
		$I_C = 2 \text{ A}$; $V_{CE} = 5 \text{ V}$; $T_{mb} = 25 \text{ C}$; see Figure 11	10	17	28	



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2. Ordering information

Table 2. Ordering information

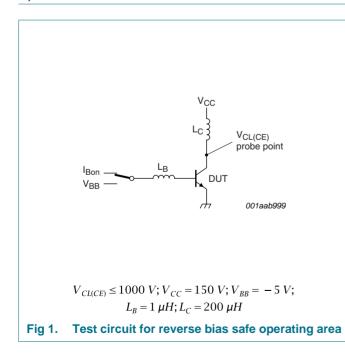
Type number	Гуре number Package		
	Name	Description	Version
PHE13005	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

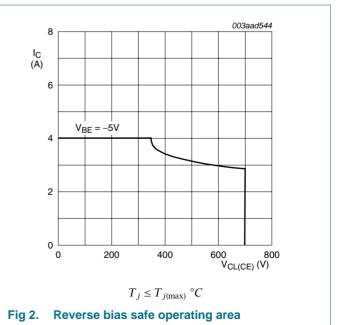
3. Limiting values

Table 3. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

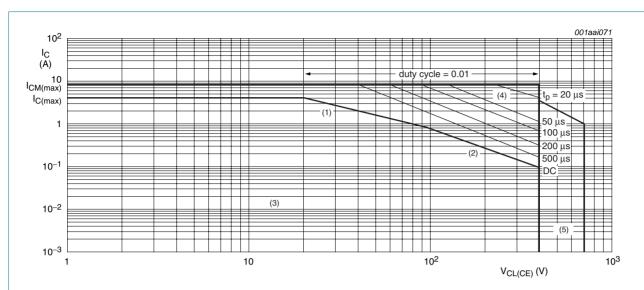
Symbol	Parameter	Conditions	Min	Max	Unit
V_{CESM}	collector-emitter peak voltage	$V_{BE} = 0 \text{ V}$	-	700	V
V_{CBO}	collector-base voltage	I _E = 0 A	-	700	V
V_{CEO}	collector-emitter voltage	I _B = 0 A	-	400	V
I _C	collector current	DC; see Figure 3, 1 and 2	-	4	Α
I _{CM}	peak collector current		-	8	Α
I _B	base current		-	2	Α
I _{BM}	peak base current		-	4	Α
P _{tot}	total power dissipation	T _{mb} ≤ 25 °C; see Figure 4	-	75	W
T _{stg}	storage temperature		-65	150	$\mathcal C$
Tj	junction temperature		-	150	$\mathcal C$





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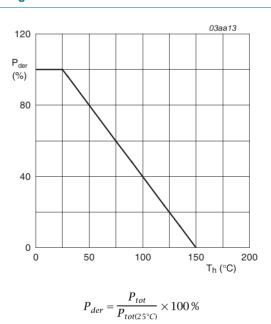
Silicon diffused power transistor



 $T_h \le 25$ °C Mounted with heatsink compound and (30 \pm 5)N force on the centre of the envelope

- (1) P_{tot} maximum and P_{tot} peak maximum lines
- (2) Second breakdown limits
- (3) Region of permissible DC operation
- (4)Extension of operating region for repetitive pulse operation
- (5) Extension of operating region during turn-on in single transistor converters provided that $R_{BE} \le 100~\Omega$ and $t_p \le 0.6~\mu s$

Fig 3. Forward bias safe operating area



Normalized total power dissipation as a function of heatsink temperature Fig 4.

Silicon diffused power transistor

4. Thermal characteristics

Table 4. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	see Figure 5	-	-	1.67	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient		-	60	-	K/W



Silicon diffused power transistor

5. Characteristics

Table 5. Characteristics

Table 5.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics					
I _{CES}	collector-emitter cut-off current	V_{BE} = -1.5 V; V_{CE} = 700 V; T_j = 25 °C	-	-	1	mA
		V_{BE} = -1.5 V; V_{CE} = 700 V; T_j = 100 °C	-	-	5	mA
I _{CBO}	collector-base cut-off current	$V_{CB} = 700 \text{ V}; I_E = 0 \text{ A}; T_{mb} = 25 \text{ °C}$	-	-	1	mA
I _{CEO}	collector-emitter cut-off current	$V_{CE} = 400 \text{ V}; I_{B} = 0 \text{ A}; T_{mb} = 25 \text{ °C}$	-	-	0.1	mA
I _{EBO}	emitter-base cut-off current	$V_{EB} = 9 \text{ V}; I_{C} = 0 \text{ A}; T_{mb} = 25 \text{ °C}$	-	-	1	mA
V_{CEOsus}	collector-emitter sustaining voltage	$I_B = 0$ A; $I_C = 10$ mA; $L_C = 25$ mH; $T_{mb} = 25$ °C; see Figure 6 and 7	400	-	-	V
V_{CEsat}	collector-emitter saturation voltage	$I_C = 1 \text{ A}$; $I_B = 0.2 \text{ A}$; $T_{mb} = 25 \text{ C}$; see Figure 8 and 9	-	0.1	0.5	V
		$I_C = 2 \text{ A}; I_B = 0.5 \text{ A}; T_{mb} = 25 \text{ C};$ see <u>Figure 8</u> and <u>9</u>	-	0.2	0.6	V
		$I_C = 4 \text{ A}$; $I_B = 1 \text{ A}$; $T_{mb} = 25 \text{ C}$; see Figure 8 and 9	-	0.3	1	V
V_{BEsat}	base-emitter saturation voltage	$I_C = 1 \text{ A}$; $I_B = 0.2 \text{ A}$; $T_{mb} = 25 \text{ °C}$; see Figure 10	-	0.85	1.2	V
		$I_C = 2 \text{ A}$; $I_B = 0.5 \text{ A}$; $T_{mb} = 25 \text{ °C}$; see Figure 10	-	0.92	1.6	V
h _{FE}	DC current gain	$I_C = 1 \text{ A}$; $V_{CE} = 5 \text{ V}$; $T_{mb} = 25 \text{ C}$; see Figure 11	12	20	40	
		$I_C = 2 \text{ A}$; $V_{CE} = 5 \text{ V}$; $T_{mb} = 25 \text{ C}$; see Figure 11	10	17	28	
Dynamic (characteristics					
$t_{\mathtt{S}}$	storage time	I_C = 2 A; I_{Bon} = 0.4 A; I_{Boff} = -0.4 A; R_L = 75 Ω ; T_{mb} = 25 $^{\circ}$ C; resistive load; see Figure 12 and 13	-	2.7	4	μs
		I_C = 2 A; I_{Bon} = 0.4 A; V_{BB} = -5 V; L_B = 1 μ H; T_{mb} = 25 °C; inductive load; see Figure 14 and 15	-	1.2	2	μs
		I_C = 2 A; I_{Bon} = 0.4 A; V_{BB} = -5 V; L_B = 1 μ H; T_{mb} = 100 °C; inductive load; see Figure 14 and 15	-	1.4	4	μs
t _f	fall time	I_C = 2 A; I_{Bon} = 0.4 A; I_{Boff} = -0.4 A; R_L = 75 Ω ; T_{mb} = 25 $^{\circ}$ C; resistive load; see Figure 12 and 13	-	0.3	0.9	μs
		I_C = 2 A; I_{Bon} = 0.4 A; V_{BB} = -5 V; L_B = 1 μ H; T_{mb} = 25 °C; inductive load; see Figure 14 and 15	-	0.1	0.5	μs
		I_C = 2 A; I_{Bon} = 0.4 A; V_{BB} = -5 V; L_B = 1 μ H; T_{mb} = 100 °C; inductive load; see Figure 14 and 15	-	0.16	0.9	μs

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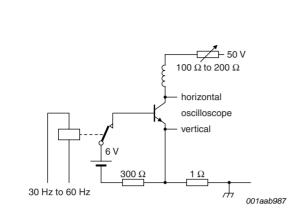


Fig 6. Test circuit for collector-emitter sustaining voltage Fig 7.

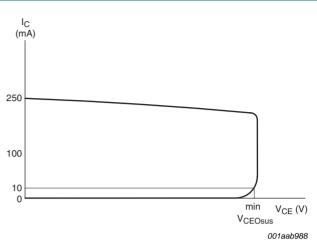


Fig 7. Oscilloscope display for collector-emitter sustaining voltage test waveform

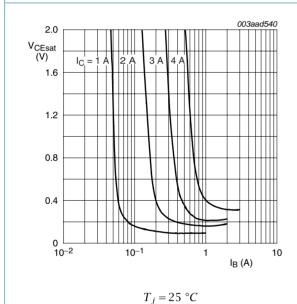
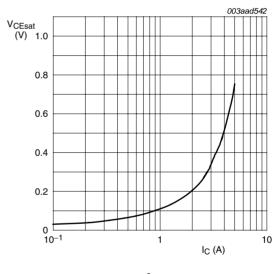


Fig 8. Collector-emitter saturation voltage; typical values



 $\frac{I_C}{I_R} = 4$

Fig 9. Collector-emitter saturation voltage as a function of collector current; typical values

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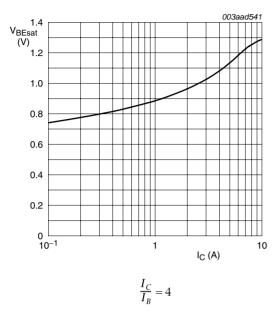


Fig 10. Base-emitter saturation voltage; typical values

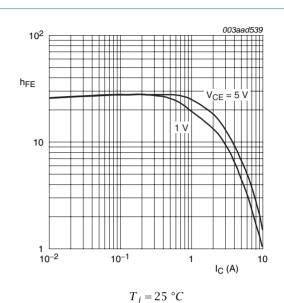
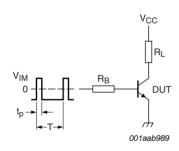


Fig 11. DC current gain as a function of collector current; typical values



 $V_{IM} = -6 \text{ to } +8 \text{ V}; V_{CC} = 250 \text{ V}; t_p = 20 \text{ } \mu\text{s}; \delta = \frac{t_p}{T} = 0.01$ R_B and R_L calculated from I_{Con} and I_{Bon} requirements.

Fig 12. Test circuit for resistive load switching

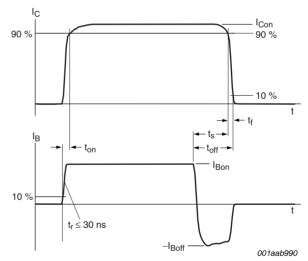
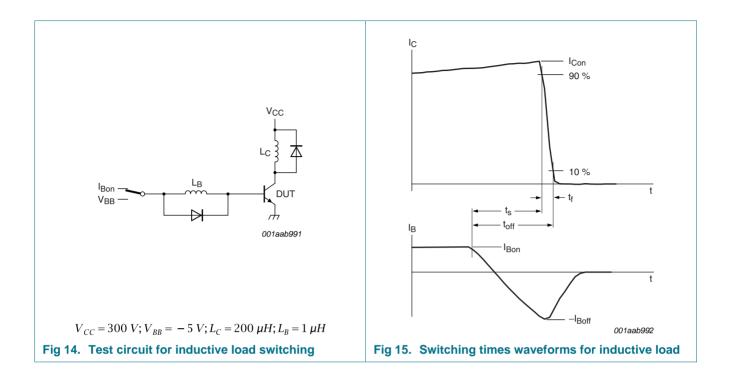


Fig 13. Switching times waveforms for resistive load

Silicon diffused power transistor



Package outline

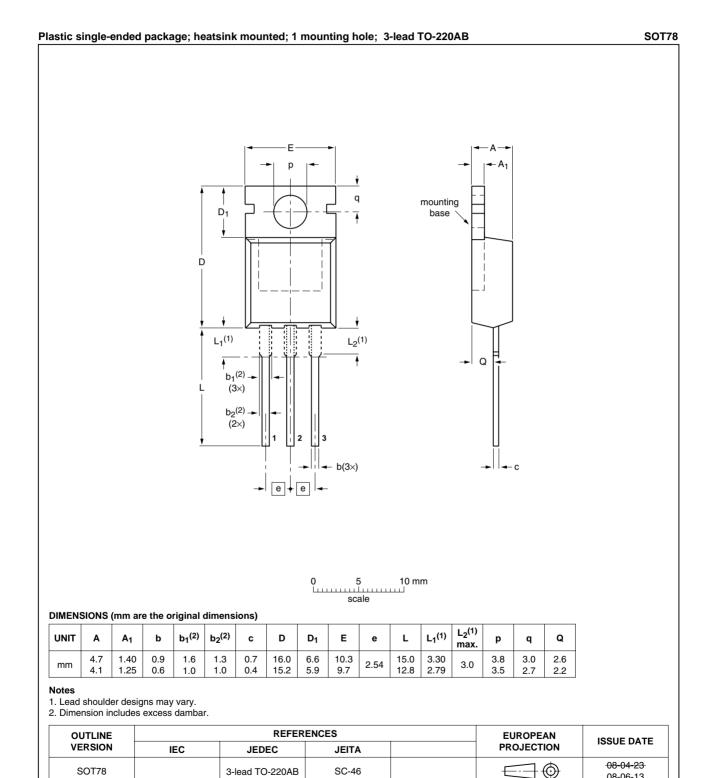


Fig 16. Package outline SOT78 (TO-220AB)

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Silicon diffused power transistor

10 of 12

Revision history

Table 6. **Revision history**

Product data sheet

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Document ID	Release date	Data sheet status	Change notice	Supersedes
PHE13005_3	20091120	Product data sheet	-	PHE13005_2
Modifications:		of this data sheet has been of NXP Semiconductors.	en redesigned to comp	ly with the new identity
	 Legal texts 	have been adapted to the	new company name v	where appropriate.
PHE13005_2	19990201	Product specification	-	PHE13005_1
PHE13005_1	19980801	Preliminary specification	١ -	-

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Document status [1][2]	Product status[3]	Definition
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Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions"
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Silicon diffused power transistor

10. Contents

1	Product profile
1.1	General description
1.2	Features and benefits
1.3	Applications
1.4	Quick reference data1
2	Ordering information
3	Limiting values2
4	Thermal characteristics4
5	Characteristics5
6	Package outline
7	Revision history10
8	Legal information11
8.1	Data sheet status
8.2	Definitions11
8.3	Disclaimers
8.4	Trademarks11
0	Contact information 11

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