

PJSR05TB4 SERIES

LOW CAPACITANCE TVS AND DIODE ARRAY

This diode array is configured to protect up to two data transmission lines acting as a line terminator, minimizing overshoot and undershoot conditions due to bus impedance as well as protect against over-voltage events as electrostatic discharges. Additionally the TVS Device offers overvoltage transient protection between the operating voltage bus and ground plane. New package SOT-543 offers an ideal solution, minimizing board space in portable consumer applications.

FEATURES

- Peak power dissipation of 350W 8x20μs
- Maximum capacitance of 1.2pF at 0Vdc 1MHz Line-to-Ground
- Maximum leakage current of 1.0μA@VRWM
- New SMT package SOT-543
- IEC61000-4-2 compliant 15kV Air, 8kV contact
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MECHANICAL DATA

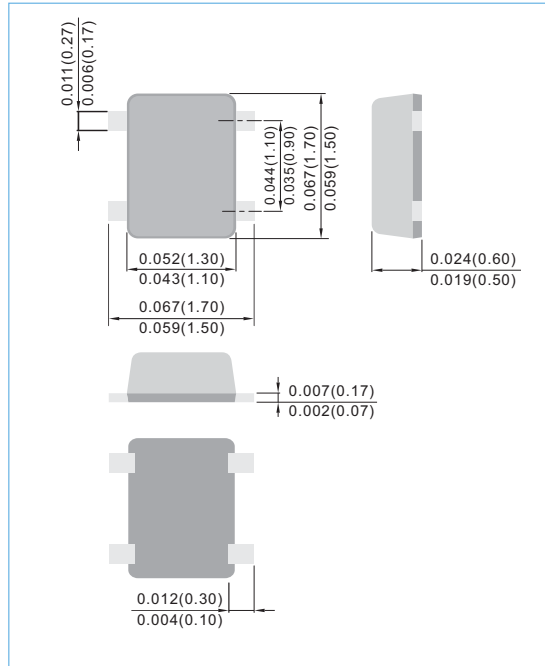
- Case: SOT-543, Molded Plastic
- Terminals: Solderable per MIL-STD-750, Method 2026
- Polarity: See circuit schematic below
- Approx. Weight: 0.002 grams

APPLICATIONS

- USB 2.0 and Firewire Ports Protection
- LAN / WLAN Access Point terminals
- HDMI V1.3 Video Port Protection
- DVI Port

SOT-543

Unit : inch(mm)



MAXIMUM RATINGS $T_J=25^{\circ}\text{C}$ unless otherwise noted

PARAMETER	SYMBOL	VALUE	UNIT
Peak Pulse Power (8/20μs Waveform)	P_{PPM}	350	W
Soldering Temperature, $t_{max}=10s$	T_L	260	$^{\circ}\text{C}$
Operating Junction Temperature Range	T_J	-55 to + 125	$^{\circ}\text{C}$
Storage Temperature Range	T_{STG}	-55 to + 150	$^{\circ}\text{C}$

