



## PJSR05TB4 SERIES

### LOW CAPACITANCE TVS AND DIODE ARRAY

This diode array is configured to protect up to two data transmission lines acting as a line terminator, minimizing overshoot and undershoot conditions due to bus impedance as well as protect against over-voltage events as electrostatic discharges. Additionally the TVS Device offers overvoltage transient protection between the operating voltage bus and ground plane. New package SOT-543 offers an ideal solution, minimizing board space in portable consumer applications.

#### FEATURES

- Peak power dissipation of 350W 8x20 $\mu$ s
- Maximum capacitance of 1.2pF at 0Vdc 1MHz Line-to-Ground
- Maximum leakage current of 1.0 $\mu$ A@VRWM
- New SMT package SOT-543
- IEC61000-4-2 compliant 15kV Air, 8kV contact
- 

#### MECHANICAL DATA

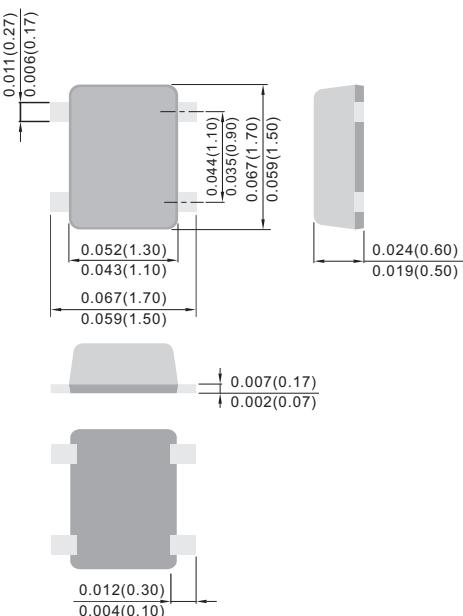
- Case: SOT-543, Molded Plastic
- Terminals: Solderable per MIL-STD-750, Method 2026
- Polarity: See circuit schematic below
- Approx. Weight: 0.002 grams

#### APPLICATIONS

- USB 2.0 and Firewire Ports Protection
- LAN / WLAN Access Point terminals
- HDMI V1.3 Video Port Protection
- DVI Port

**SOT-543**

Unit : inch(mm)



#### MAXIMUM RATINGS T<sub>J</sub>=25°C unless otherwise noted

PARAMETER	SYMBOL	VALUE	UNIT
Peak Pulse Power (8/20 $\mu$ s Waveform)	P <sub>PPM</sub>	350	W
Soldering Temperature, t max=10s	T <sub>L</sub>	260	°C
Operating Junction Temperature Range	T <sub>J</sub>	-55 to + 125	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to + 150	°C

